
the small systems journal
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## OFTEN FIRST - ALWAYS THE BEST

When we introduced the " S " system last year we knew that we were ahead of the industry. We didn't realize just how far.

## WE KNEW THE NEEDS-

When we began designing the S/09 computer, we knew that the normal eight-bit microprocessor system was not adequate for any but the smallest, single user business applications. What was worse there was little that could be done to expand the capabilities of the system if the customer needed it. There is nothing much worse to a business customer than a "dead end" system.

## MEMORY IS THE KEY-

Obviously a business system should be able to operate with multiple terminals if needed. It should also be able to do a variety of jobs; not just data processing, but also word processing and computer aided instruction. With a system limited to 64 K bytes of memory addresses such a system is just not practical. The amount of user memory available to each terminal is too small for useful work.

## HOW DO YOU GET IT-

The common solution to this problem is called bank switching. This process is similar to a selector switch that turns on the bank of memory that you want to work with. This, however, has a few problems. It is inefficient, therefore expensive, plus being slow. It is also extremely clumsy when data must be exchanged between two different programs. Besides with all this you still cannot use more than 64 K of memory for any one program. So what is the alternative?

## DO IT RIGHT-

The alternative is an address bus with more than the normal 16 bits found on eight-bit microprocessors. By using 20 address bits you can, for instance, address up to a million memory locations directly.

This way you have access to any part of memory at any time without any intermediate processes. Program interaction is now no problem at all.

## SOFTWARE MUST MATCH-

So far we have a computer system with a large memory capacity and the ability to operate with many terminals, but this is not enough. You need an operating system just as sophisticated as the
hardware to complete the job. It must be a multitasking (therefore multiuser) operating system and it must be fast if it is to be useful with multiterminal systems. UniFLEX ${ }^{\circledR}$ fills these requirements and more. It also has multiple directories, log-in and password features. UniFLEX ${ }^{\circledR}$ was patterned after UNIXT.M., which is one of the most highly regarded operating systems around.

## PERIPHERALS TOO-

To complete the system we offer our smart terminals, and a variety of disk systems. We have everthing from a 390 K byte floppy to a $40 \mathrm{Meg} /$ byte Winchester drive. All peripherals are compatible and so you can start with a small single terminal system and upgrade if necessary to a fully expanded system-16 terminals, 768 bytes of RAM memory and $96 \mathrm{Meg} /$ bytes of disk storage.

## GET THE WHOLE STORY-

If you are planning to install, or sell business systems you should get our information package on the most versatile and cost effective system on the market, the S/09. You can get a 128 K system (less printer) for a little over \$5,000.00.
*UNIX is a Trademark of Bell Laboratories.

## SYSTEM SOFTWARE

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| :--- | :---: |
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| FORTRAN |  |
| Pascal |  |
| PILOT | Word Processing |
|  | Word Processing Editor |
| Data Processing | Text Processor |
| General Ledger |  |
| Accounts Receivable |  |
| Accounts Payable  <br> Payroll Utilities <br> Jobcost Debug Package <br> Inventory Sort-Merge <br> Mail List Diagnostics <br>   |  |
|  | *Supplied with over 40 utilities |




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# Get the professional color display that has BASIC/FORTRAN simplicity 

## LOW-PRICED, TOO

Here's a color display that has everything: professional-level resolution, enormous color range, easy software, NTSC conformance, and low price.

Basically, this new Cromemco Model SDI* is a two-board interface that plugs into any Cromemco computer.

The SDI then maps computer display memory content onto a convenient color monitor to give high-quality, highresolution displays ( $756 \mathrm{H} \times 482 \mathrm{~V}$ pixels).

When we say the SDI results in a highquality professional display, we mean you can't get higher resolution than this system offers in an NTSC-conforming display.

The resolution surpasses that of a color TV picture.

## BASIC/FORTRAN programming

Besides its high resolution and low price, the new SDI lets you control with optional Cromemco software packages that use simple BASIC- and FORTRANlike commands.

Pick any of 16 colors (from a 4096-color palette) with instructions like DEFCLR (c, R, G, B). Or obtain a circle of specified size, location, and color with $X C \operatorname{IRC}(x, y, r, c)$.

[^0]

Model SDI High-Resolution Color Graphics interface

## HIGH RESOLUTION

The SDI's high resolution gives a professional-quality display that strictly meets NTSC requirements. You get 756 pixels on every visible line of the NTSC standard display of 482 image lines. Vertical line spacing is 1 pixel.

To achieve the high-quality display, a separate output signal is produced for each of the three component colors (red, green, blue). This yields a sharper image than is possible using an NTSC-composite video signal and color TV set. Full image quality is readily realized with our highquality RGB Monitor or any conventional red/green/blue monitor common in TV work.


Model SDI plugs into Z-2H 11-megabyte hard disk computer or any Cromemco computer

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## CONTACT YOUR REP NOW

The Model SDI has been used in scientific work, engineering, business, TV, color graphics, and other areas. It's a good example of how Cromemco keeps computers in the field up to date, since it turns any Cromemco computer into an up-to-date color display computer.

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Page 26


Page 46


Page 76


Page 126

## Foreground

26 BUILD A LOW-COST, REMOTE DATA-ENTRY TERMINAL by Steve Ciarcia<br>This terminal increases the flexibility of computer home-control systems.

46 AN 8088 PROCESSOR FOR THE S-100 BUS, Part 1 by Tom Cantrell Part 1 covers the basic design considerations of an S-100 processor board based on Intel's 8088 microprocessor.
86 PENNY PINCHER'S JOYSTICK INTERFACE by Steven Wexler For about $\$ 6$ and one night's work, you can add this interface to your system.

## 116 APL CHARACTER GENERATOR by John W Langer

This is a simple modification for any video display employing the MCM6571 character generator.

## 126 CONSTRUCTION OF A FOURTH-GENERATION VIDEO TERMINAL, Part 2 by Theron Wierenga

Part 2 helps you to complete the construction of the terminal and learn to use the built-in debugging features.
242 KHACHIYAN'S ALGORITHM, Part 2: Problems with the Algorithm by G C Berresford, A M Rockett, and J C Stevenson A practical BASIC program can be used to explore the power and limitations of this new algorithm.
270 EXPLORING BALLISTICS WITH YOUR COMPUTER by Robert $W$ Jenks This BASIC program helps the target shooter to calculate the complex path of bullets.
282 AN INTERRUPT-DRIVEN REAL-TIME CLOCK FOR THE TMS 9900 by Thomas G Morris Jr
Three selectable interrupt rates make the Texas Instruments 16-bit processor count time.
328 A BASIC FLOPPY-DISK ACCOUNTING SYSTEM by Joseph J Roehrig Here's a complete six-program package to keep your budget records in order.

## Background

76 DISSECTING THE TI SPEAK \& SPELL by Michael A Rigsby
With these notes you can move toward the eventual goal of getting this toy to talk under personalcomputer control.
102 MACHINE PROBLEM SOLVING, Part 1: Trial-and Error Search, A Mechanical Plan to Save the Missionaries by Peter W Frey Simple games help to express this method of solving problems with computers.
180 FCC REGULATION OF PERSONAL- AND HOME-COMPUTING DEVICES by Terry G Mahn
New rulings by the FCC will affect the use and manufacture of personal computers.
206 varieties of threaded code for language
IMPLEMENTATION by Terry Ritter and Gregory Walker
Some kinds of threaded code are position and system independent.
230 EDUCATION FORUM: NEW CULTURES FROM NEW TECHNOLOGIES by Seymour Papert
Children should learn to compute in the same way they learn to talk.

## Nucleus

6 Editorial: Intellectual Ethics and Software
14 Letters
66, 322, 324 Programming Quickies
304, 308, 310 Book Reviews
94. Languages Forum

96, 194, 314, 318, 321, 326 Technical Forum
114, 312 BYTE's Bits

[^1]
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## About This Issue

BYTE is five years old this month, and we're taking the opportunity to discuss one of our favorite subjects: homebrewing. Much of the personal computer hardware sold today is already assembled; even so, many of our readers like to build or modify their own equipment, and even "homebrew" it from scratch. The cover photograph by Raoul Hackel, Stock Boston, shows some colorful wiring harnesses inside a computer chassis, a familiar sight to the intrepid do-it-yourselfer.

Theme articles in this issue include a build-it-yourself, low-cost, remote data-entry terminal (from Steve Ciarcia); exploring the TI Speak \& Spell; a pennypincher's joystick interface; and the beginning of a multipart article on building an 8088 processor for the S-100 bus. Along with these are features on threaded code; FCC regulations and your personal computer; machine problem-solving; some tax hints for personal computer owners; and much more.

You've probably noticed that this issue of BYTE is on the large side. In fact, it's the biggest issue we've ever printed. The extra space allows us to bring you even more articles and features in this issue and in the coming months. . . . CM

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# Intellectual Ethics and Software An Inquiry Into the Nature of Ideas, Academia, and Commerce 

## Carl Helmers

Recently, I encountered an old problem again. A problem in this sense is a body of questions and my tentative answers. An old problem is like an old jacket. You get familiar with the intricacies of its individual creases, wrinkles, and holes. It may not be currently stylish, or even in the best of conditions. Yet it is hardly worth throwing out because of a shared body of experience. So, I had long ago packed this problem away in my mental baggage.

The problem I refer to is ethical in nature; it has epistemological attributes as well. It is the problem of interfacing the world of ideas with the world of commerce. In its simplest form it is a two-part question: "who originated an idea?" and "what is the value of that idea?" The problem, which has great practical implications in our technological civilization, is that of encouraging innovation by means of rewards in the worlds of ideas and commerce. The ethical position implicit in my viewpoint is simple honesty. Its intellectual expression is that credit should be given where credit is due in a freely operating world of ideas. In a laissez-faire world of commerce, its expression is that value in the marketplace should be given where value is due, in a framework of freely chosen relationships.

We humans have two worlds of activity: the intellectual world and the world of commerce. Each has its own characteristics. One deals with ideas and thoughts freely expressed. The other deals with material goods freely traded in the marketplace. We can engage in both of these very natural human pursuits to the extent that we are politically free of arbitrary laws and interference.

What, you might ask, brings about a discussion of ethics in the marketplace at this time? The particular impetus to this discussion is an incident that came to my knowledge at a recent trade show. Inasmuch as the incident is far from closed, I will not disclose the names of the parties involved. But the situation in its abstract form is worth using to explore some of the ethical problems of commerce in ideas, particularly software for small computers.

Several years ago, a small group of academics began pursuing a particular line of inquiry that related to the nature of computer design for human interaction. The charter of this group of researchers might have been expressed as: "Find the problems of human interaction with computers, and experiment with any solutions you may find." As in any academic pursuit, the inquiry generated many published papers over more than a decade. The fact that these papers also generated some exciting hardware and systems software entered the picture along the way.

Both the software and hardware developments of this group's research have been and are generously underwritten by the sponsoring organization where the activity takes place. In fact, the sponsoring organization did not expect the research to have any immediate practical expression in the marketplace, because it was basic research.


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Naturally, the members of the group communicated with others at similar academic and industrial research laboratories of the land, by means of conversations at conferences and meetings, as well as written communications of academic professional organizations. This type of communications between peers is an essential part of any productive research field. In short, word of their ideas got out.

Enter the publicist.
Now, intellectually and ethically we cannot argue with the following thought: when an opportunity is available to pursue some perceived value, we should go ahead and pursue it. There is no way one could complain about this kind of action since it is the essence of human activities. This attitude is a prelude to all research and innovation.

The publicist had all the right words. He was fluent in the jargon of computers. He perceived the enthusiasm with which the researchers described their activities personally and in print. He thought it would be good to tell the world about what was going on. And that is what he proceeded to do by means of a selfpublished work which was indeed ahead of the technology of practical general-purpose microcomputers.
Up to this point, our publicist had done nothing to which we could object. He was taking published works, analyzing them and pointing out the implications that these works have. But having caught the enthusiasm, he was beginning to grow impatient. After all, our researcher friends are involved in research, not in entrepreneurial activities. What our publicist had done, however, was create among people stimulated by small computers an intellectual and commercial demand for an excellent concept.

Enter the entrepreneurial programmer. He is the archetypal programmer who, given a challenge, immediately proceeds to code. Probably as a result of the ballyhoo created by the publicist, the entrepreneurial programmer proceeded to dig up the published works of our thinker friends.

These works were indeed complete, and can be found in the technical journals published during the 1970s. They even include all the information necessary for the entrepreneurial pro-
grammer to implement a version of one of the crude, early approaches our researcher friends investigated in their pursuit of the problem. Now, as a published work, these documents were intended for use by other researchers and anyone else with a programming problem.
The problem arises when we examine the manner in the which the publicist was going to use the published works of our researchers. It is one thing to implement a version of a program and sell the particular example as a toy. But it is quite another thing to name it the same as our researchers' ongoing project, imply in advertising that it is the same (when it is not), and generally imply that its use is sanctioned by its original authors at the research establishment. This is not the same as simply crediting the source in a published work and proceeding to implement a version under a different name and with particular variations.
Here, we find the complicity of the publicist and the entrepreneurial programmer as a pair. The publicist now had an opportunity to reach for the brass ring of the software that our research friends had not yet made available to him. He found the ring in the entrepreneurial programmer's product. So, the publicist has recently been pushing the entrepreneurial programmer's product at whatever forum he can find. This situation had been fermenting for some time when all parties showed up at a recent convention.

The situation came to a head at the convention when our researcher friends arrived on the scene. I became involved to the extent of providing a sympathetic ear in conversation with one of my friends from the laboratory in question. By all reports, the entrepreneurial programmer later became involved in some heated discussion of these points with the publicist, my research friends, and several individuals well aware of the issues involved (not including myself).
As of this writing, the matter remains unresolved. The entrepreneur still has not decided whether to change the name of his program or not, but I hope that, through the mediation of several individuals who know the facts of the matter, he will recognize the error of his ways and, in so doing, learn a bit about the in-

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tellectual versus commercial realms of endeavor. I have learned that some sort of decision will probably have been made by the time you read this.

As for the publicist, he continues in his inimitable style to spin wheels of fancy.
$\ln$ the intellectual marketplace of ideas, the coin of the realm is thought. He or she who owns a reputation as a result of careful thought has a purse full of golden coins ready for the bazaar of ideas. A marketplace of ideas or commerce is a human activity where all parties benefit as a part of trade. One cannot expect willing and bountiful trading when one party plays by a set of rules different and incompatible from the other's set.

The productive results of innovation and thought carry a requirement for the respect of the rules of the game. One of these rules in the intellectual world could be stated "thou shalt not take thy neighbor's reputation as thine own." When you use an idea, credit its source where appropriate, but do not pretend to imply that your version of the thought is the same.

It is perfectly fine to use an inspiration from someone's published thought in a commercial product of your own. But be sure that you make clear that the product is your own! Credit the inspiration to be sure. However, if you do not have an endorsement from the source of the inspiration, do not attempt to advertise that thought in any way as a product endorsed by the source of the inspiration.
Naturally, the ideal state is that in which the researcher is also able to capitalize directly on the results of his or her innovation. By being the first to it and the best able to understand the problem, an inestimable advantage is gained over the nonoriginal machinations of those who merely implement the published designs.

The main rewards of research must be understood for what they are: an appreciation of difficult problems and the satisfaction of seeing them through to a better understanding.

Occasionally in research a commercial gold mine is found that exudes some of its wealth on the innovator. But this is a small part of motivation for a life of ideas. The innovator's reputation is based on a mutual trust and fascination with
ideas. Entrepreneurs with a long-term point of view respect this trust by avoiding any semblance of potential violation of that trust. End of commentary.

## A Note

The lives of individuals are marked by a series of changes through growth. Enterprises evolve in much the same way. BYTE has gone through many such changes. It began as an idea in the minds of my associates and me five years ago. After much hard work it matured to the point where it now has a circulation in excess of 160,000 and an assured future as a member of the family of magazines published by McGraw-Hill. This issue marks the fifth anniversary of BYTE's first issue, published in September 1975.

Since BYTE has matured to the point where a founder's day-to-day input is no longer a requisite to the continued health of the venture, I am now in the fortunate position of being able to indulge in my other interests and goals. While continuing with many of the functions at BYTE that have occupied me over the last five years, I will be able to engage in consulting activities related to the technology of, and markets for, small computer systems. Such activities have always been of great interest to me. Only with the evident maturity of BYTE and the cooperation of McGraw-Hill am I now able to spend about half of my time on such ventures.
The day-to-day operations of the magazine will be in the very capable hands of my successors, Chris Morgan and the technical editors of BYTE's staff. My new relationship with BYTE is reflected in a new title on the masthead: "Founding Editor." With my continued intimate involvement with BYTE, I shall truly have the best of both worlds. . . .CH.

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## andelulankce.

The RS-232 standard assures maximum compatibility with a variety of serial devices. Apple Ap an an ample, with the AlO you can 80 characters per line to a video terminal modem to use time-sharing instead of 40 , a moder for hard copy. The services, or a printer for harogrammable. serial interiace is sondshaking lines, and
features three hand features a rotary switch to select from 7 standard baud rates. On-board firmware provides a powerful driver routine so you won't need to write any
software to utilize the interface

Daraled Interrace. This interface can be used to connect your Apple* to a variety of parallel printers. The programmable $I / O$ ports have enough lines to handle two printers simultaneously with handshaking control. The for controlling includes a software listing for co y a prefer, parparallel printers or, if you pref is firmallel driver routine is And printing is ware as an option. Applicat for this general
 only one applillel interface.

## Two boardsinone.

 The AIO is the only board on the market that can do both at the same to both serial and parallel devices. Idesign and solid value that's computing. time. That's the kind of ins since the beginning of persorial and parallel going into SSM products complete with serial PROMS, sof sof ware listings. The AlO comes comple documentation including sore store contact cables, and comple AIO at your local computer store or See the AIO at your location.us for more information

## Maybe we can save you a call.

Many people have called with the same questions about the AIO. We'll answer those and a few more here.

Q: Does the AIO have hardware handshaking?
A: Yes. The serial port accommodates 3 types-RTS.
CTS, and DCD. The parallel port handles ACK, $\overline{\mathrm{ACK}}$, BSY, STB, and STB.
Q: What equipment can be used with the AIO?
A: A partial list of devices that have actually been tested with the AIO includes: IDS 440 Paper Tiger, Centronics 779, Qume Sprint 5, NEC Spinwriter, Comprint. Heathkit H14, IDS 125, IDS 225, Hazeltine 1500, Lear Siegler ADM-3, DTC 300, AJ 84I.
Q: Does the AIO work with Pascal?
A: Yes. The current AIO serial firmware works great with Pascal. If you want to run the parallel port, or both the serial and parallel ports with Pascal. order our "Pascal Patcher Disk:"
Q: What kind of firmware option is available for the parallel interface?
A: Two PROM's that the user installs on the AIO card in place of the Serial Firmware PROM's provide: Variable margins, Variable page length. Variable indentations, and Auto-line-feed on carriage return.
Q: How do I interface my new printer to my Apple using my AlO card?
A: Interconnection diagrams for many popular printers and other devices are contained in the AIO Manual. If your printer is not mentioned, please contact SSM's Technical Support Dept. and they will help you with the proper connections.
Q: I want to use my Apple as a dumb terminal with a modem on a timesharing service like The Source. Can I do that with the AIO? A: Yes. A "Dumb Terminal Routine" is listed in the AIO Manual. It provides for full and half duplex. and also checks for presence of a carrier.
Q: What length cables are provided?
A: For the serial port, a 12 inch ribbon cable with a DB-25 socket on the user end is supplied. For the parallel port, a 72 inch ribbon cable with an unterminated user end is provided. Other cables are available on special volume orders.

The AIO is just one of several boards for the Apple that SSM will be introducing over the next year. We are also receptive to developing products to meet special OEM requirements. So please contact us if you have a need and there is nothing available to meet it.

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# ht and the Apple. 

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With Apple, Edison could've written a program to determine why some filaments burned longer than others.
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If you're considering a personal computer, stop by the computer store and compare. Apple's reliability, proven performance and recognized technological leadership will help you see the light. Don't let history pass you by. Visit your nearest Apple dealer, or call 800-538-9696. In California, 800-662-9238.

## Letters

## Cromemco Lauded

After reading of the many horror stories of poor documentation and service within the microcomputer industry, I want to point out the excellent treatment I have received from Cromemco Inc.
In July, 1979, I purchased a System III with four disk drives and most of Cromemco's available software. Lately, I
have added the 3102 Terminal and the 3355A Printer. I have found the documentation very complete. The manuals for the above products form a pile 10 inches high.

When I first received the System III, I had some difficulty using the third and fourth disk drives. Because I was not too familiar with the system, and the drives worked in certain situations, I concluded that the drives were probably OK, and

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that I did not understand some detail of the system's operation. Several weeks ago I was forced to conclude that the drives were defective, and I called Cromemco. Even though the warranty on the drives had expired six months earlier, they accepted the responsibility for the defect and had the repaired drive back to me within two weeks.

In addition, I have begun receiving updated software on disks. The software has been considerably enhanced. There is no charge for the additional features. I don't even have to pay for the disks.

Finally, though I had done a lot of programming on large systems and am quite knowledgeable about electronics, I had never worked with FORTRAN or COBOL, and initially I was not up to speed on the system aspects of microcomputers, especially the use of the disk drives. My questions were always answered courteously, even when they were naive, and my telephone calls were always returned.

The equipment is conservatively designed and well constructed. The software and operating system are capable and straightforward to use.

I have never been more pleased with all aspects of a purchase than I am with my Cromemco system.

## Wil Schuemann <br> Sage Instruments <br> 501 Maple St

Parkersburg WV 26101

## Making Music

Hal Chamberlin's article on
"Advanced Real-Time Music Synthesis Techniques" (April 1980 BYTE, page 70) was timely and informative. Since I have been experimenting with similar techniques for several years, I can vouch for the viability of his procedures, but I would also like to comment on several points raised in the article.

I agree that most digital synthesizers on the market do not have sufficient control for either education or serious musical work. A recent informal poll of musicians showed that the majority desired at least four voices, and complete control over envelope, timbre, loudness, and pitch for these purposes.

While Mr Chamberlin's technique provides for the important change of timbre with time that is so often neglected, his sequence table is stepped through at a rate determined by the tempo setting, so a voice will behave differently at slow

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and fast tempi. Most musical instruments, however, vary their amplitude (tremolo), pitch (vibrato), and timbre (we need a word for this-tambolo?!) at a rate almost independent of the score tempo, but in a manner suited to the instrument and type of music played. This could be accomplished by adding one more counter for vibrato-tremolotambolo update independent of the tempo counter.

The computation of signal/noise ( $\mathrm{S} / \mathrm{N}$ ) ratios for synthesizers can be misleading. If the intent is to reproduce a musical sound, then a resolution of 60 to 80 dB is a necessity. However, if the intent is to produce music from scores, a much lower S/N ratio can be tolerated if the distortion partials are harmonic. After all, the "noise" content of flutes or harpsichords can be very high, but is considered part of the natural sound of the instrument. Eight-bit D/A (digital-toanalog) converters and 256-byte wave tables do seem adequate for musicsynthesis experimentation, at least until computer memory and power become somewhat cheaper.

Mr Chamberlin's method of generating up to 8 K bytes of waveform tables is well suited to single D/A output but requires extensive dedicated storage, plus time spent in creating the wave tables. This can be markedly reduced by noting
that the ratios of the harmonic amplitudes remain nearly constant for a considerable fraction of the note duration for many instruments. This suggests that if the envelope amplitude were provided by a separate D/A converter and its output were multiplied by a waveform multiplying D/A converter, that many fewer waveform tables would be necessary since they would contain only waveshape information, not envelope information, and they could better be reused for other voices. The additional $\$ 10$ for a multiplying D/A converter would be more than offset by the savings in memory. Incidentally the envelope "volume control" must precede the waveform $\mathrm{D} / \mathrm{A}$ converter, not follow it as implied in the text, so that the required envelope filter does not cut off the harmonics of the waveform.

Finally, there is a very serious problem with the low sampling rates
( 6.9 kHz to 8 kHz ) mentioned in the article. Suppose that the highest fundamental desired is $\mathrm{C}_{6}(\approx 2100 \mathrm{~Hz})$ and that at least four harmonics are necessary to produce the desired timbre (both of these figures are very conservative). Then the highest frequency present in the sampled waveshape is $\approx 8400$ Hz , and since a "headroom" of at least $10 \%$ is needed for the anti-aliasing lowpass filters, the filter stop-band edge can

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ten, XAF is assigned the address to which a byte of value 8 is assembled. The actual intent is to assign XAF the value 8 . The pseudo-operation EQU serves the function of an "equivalence statement."

Using mnemonic conventions such as those developed in this article, it is simpler to use $Z 80$ code on an 8080 assembler. However, the readability of the resultant programs will be poor. I would suggest the use of macroinstructions in lieu of the DW...DB sequences. If a macroassembler is not available, then a preprocessor could be created to expand the Z 80 instructions into sequences understandable to an 8080 assembler. Either way, the source code will retain readability and will probably be less error-prone.
I believe that the basic software tools make a tremendous difference to the quality of software produced. Every Z80 computer should have at least one good Z80 assembler.
Lest I seem too critical, I did enjoy this article very much.

Anthony Skjellum
1695 Shenandoah Rd
San Marino CA 91108

## Information Please

Are any of my fellow BYTE readers willing to share information with me on interfacing microcomputer systems to the IBM Models 50 or 60 electronic typewriters? I would like to use my Model 60 as an output printer, and I would appreciate some advice, if any is to be had. Thanks very much.

## Michael Pinneo <br> 3757 Vienna Dr <br> Aptos CA 95003

## Selectric Information Sought

Do any readers of BYTE know of any commercial devices that can interface a Radio Shack TRS-80 to an old model of an IBM Selectric typewriter (a Model 71)? I would also like to hear from anyone who has bought an alreadyinterfaced Selectric from McClain and Associates or from Worldwide Electronics. Thank you.

## N Vijayan <br> 1332 Notre Dame Dr <br> Davis CA 95616

## Performance Improvements

I have studied the article 'TRS-80 Performance Evaluation by Program Timing," by James Lewis (March 1980 BYTE, page 84) with interest. I am only concerned here with the Level II

## BASIC program.

The largest number a figure is divisible by without becoming redundant is its square root. If we include the statement:

$$
20 \mathrm{C}=\mathrm{INT}(\mathrm{SQR}(\mathrm{~A}))+1
$$

and change the second FOR-NEXT loop to:

## 30 FOR $B=3$ TO C STEP 2

we will find the program runs much faster. For example, in the original program 9901 goes through the inner loop roughly 4500 times. Using the modified program, the second loop is only used 50 times which is ninety times faster. I find this version will run in about 25 minutes.

Here is a listing of the modified program:

```
1 CLS:PRINT"123";
```



```
20 C=INT(SQR(A)) +1
30 FOR B = 3 TO C STEP 2
40 D = A/B
50 IF INT(D) = D THEN NEXT A
6 0 ~ N E X T ~ B ~
70 PRINT A;
8 0 ~ N E X T ~ A ~
```


## Brian Glover

POB 2102
Inuvik, Northwest Territories
XOE OTO, Canada

## More Improvements

Mr Lewis, in his article in the March 1980 BYTE, seems to compare two dissimilar computers. It was unclear to me what could be gained by this kind of comparison. The run time of a program is not only sensitive to the computer being used, as well as the programming language, but also to many other seemingly trivial factors.

For instance, Mr Lewis wanted to find all the prime numbers less than 10,000 . His method was to divide by successive odd numbers. If division occurred without a remainder, then the number being divided is not a prime. The problem was that he kept dividing until the divisor was half of the dividend. For example, to check a number that was almost 10,000 he would keep dividing by numbers until he has used up all those less than 5000 . It is easy to show that the time to stop is at the square root of the number, not half the number. He could have stopped after checking numbers up to 100 instead of 5000.

This is true because, if some number greater than 100 is divided without a remainder, the quotient would be some number less than 100 and this would have been revealed before ever reaching 100.

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I wrote the following short program, PRIME, and ran it on my North Star computer in about 24 minutes:

```
10 REM PRIME
20 FOR K=5 TO 10000 STEP 2
30 I=3
40 IF INT(KI) = KII THEN }8
50 I= I+2
60 IF | i 2< = K THEN }4
70 !K
80 NEXT
90 END
```

Division for a conventional microcomputer for which double precision is necessary is slow, and the fewer occurrences in a program the quicker the program will run. When I eliminated one division in my program to produce PRIME 2, the running time was reduced to 17 minutes:

10 REM PRIME 2
20 FOR K $=5$ TO 10000 STEP 2
$30 \quad \mathrm{I}=3$
$40 \quad X=K I$
50 IF INT $(X)=X$ THEN 90
$60 \quad I=1+2$
70 IF I $12<=$ K THEN 40
80 ! K
90 NEXT
100 END
But the most important consideration is how the translator works; an inter-
preter is devilishly slow. A computer will run considerably faster because machine code is actually executed. I wrote a short Pascal program for my North Star, primes, and was surprised to find that it executed in 1 minute and 46 seconds. (See listing 1.)

Mr Lewis' results for the large IBM computer was 1 minute and 19 seconds using a PL/I compiler. Does this mean that my microcomputer is almost equivalent to this huge IBM machine? I think not.

Comparisons of this sort do not prove much; they just show how many variables are involved in determining the time it takes to run a program!

Ivan Flores
Flores Associates Computer Consultants 108 8th Ave
Brooklyn NY 11215

Comparisons of this sort may not prove much, but you (and many other readers) found the idea interesting enough to experiment with. Evaluation of performance encourages programmers and designers to work their crafts with efficiency, and to search for the elegantly simple solutions that improve .... CPF

Listing 1

```
program primes; \{writes out a number of primes \}
vari, j,k, \(n\) : integer;
begin
    \(k:=2\);
    while \(k<=5000\) do begin
        \(n:=2^{*} k+1 ;\)
            \(\mathrm{j}:=1 ; \mathrm{i}:=3\);
            while ( \(i\) * \(i<=n\) ) and \((j=1)\) do
            begin
            if \(n \bmod i=0\) then \(j:=0\) else \(i:=i+2\);
            end;
            if \(\mathrm{j}=1\) then write( \(\mathrm{n}, \quad{ }^{\prime}\) ');
            \(k:=k+1\);
        end;
end
```


## Pascal Precision

The letter from Martin Berman concerning numerical precision in UCSD Pascal (BYTE, June 1980, page 17) struck one of my current concerns. The actual precision available in UCSD Pascal is 7.2 decimal digits; ie: the data type real will accommodate integer values as large as $16,777,216\left(2^{24}\right)$ exactly. However, the output routine is limited to six significant digits. To print the remaining available 1.2 digits will require either a revision to the systemoutput routine or an output routine custom-made for the application.

I am not privy to the design process at UCSD, but suspect that this is an attempt to "protect" the user from roundoff error. I, for one, deplore such at-
tempts at protection since the user who actually knows what he is doing is forced to "program around" the system. A reasonable precaution is to give no more precision than the system has (eight digits in the case of UCSD Pascal), although even this is open to question-a fellow programmer was once caught by this type of "protection" even though he was using only powers of two which are exactly represented throughout the range of the system.
Incidentally, there is a routine available for determining the actual precision of floating-point routines. It may be found in Pascal News, number 13 (December 1978). I enclose a copy of the code as I ran it on my UCSD Pascal system, along with the output it generated.

Industrial quality components for S-100 system builders, from California Computer Systems.
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Listing 1
program representation;

```
var base, numberofdigits, i : integer;
    rounding : boolean;
    epsilon : real;
procedúre enquiry (var radix, digits : integer;
                var rounds : boolean);
    var number, increment : real;
begin
            (*find large integral value just beyond integer limits*)
    number := 2;
    while (((number + 1)-number) = 1) do
    number := number * 2;
    (*end while*)
            (*manufacture the next largest real value*)
    increment := 2;
    while ((number + increment) = number) do
    increment := increment* 2;
    (*end while*)
            (*subtract these to give radix of representation*)
    radix := trunc((number + increment) - number);
            (*see if it rounds or truncates by adding (radix - 1)*)
    rounds:= ((number + (radix - l)) NEQ number);
            (*work out how many digits in mantissa*)
    digits:=0;
    number:= l;
    while (((number +1)-number) = 1) do begin
        digits:= digits +l;
        number:= number * radix;
    end; (*while*)
end; (*enquiry*)
begin
            (*find out basic properties*)
    enquiry(base,numberofdigits,rounding);
    writeln(' Base = ',base);
    writeln(' Number of digits = ',numberofdigits);
    if rounding then
    writeln(''Rounded')
    else
    writeln(` Truncated');
    (*end if*)
            (*compare the precision bounds")
    epsilon:= l;
    for i := l to numberofdigits do
        epsilon : = epsilon/base;
    (*end for*)
    if rounding then epsilon := epsilon/base;
            ("print the best and worst precision")
    writeln(' Best and worst precisions are ',
        epsilon,(epsilon * base));
end.
```

My hard-copy terminal does not have greater-than or less-than symbols. Thus ' NEQ " is inserted for the Pascal "not equal" symbol.
Base $=2$
Number of digits $=24$
Rounded
Best and worst precisions are
2.98023E-8 5.96046E-8

Fred Crary
7750 31st Ave NE
Seattle WA 98115

May We Suggest a Gasp Mask?
Philip K Hooper is not alone. I too noticed the foul odor of the magazine.
(See Letters, April 1980 BYTE, page 16.) Not only do I love computer science, but I love my body, and my health is paramount. I therefore abstain from the inhalation of foul vapors and fumes.

## A Healthy Minority

## Jon Dattorro

1379 Kingstown Rd Apt 1A
Kingston RI 02881
I am told that our printer used an improper glue to bind the pages together, causing the unusual smell. The printer has promised to henceforth use a different glue, and we expect that the odor problem will not recur....RSS ■

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## PERSONAL COMPUTERS

## Ciapcigs Aipguit Bellap <br> Copyicht © 1980 by steven $A$ Clarcia. All nights reserved

# Build a Low-Cost, Remote Data-Entry Terminal 

Steve Ciarcia

POB 582
Glastonbury CT 06033

Remote data-entry terminals are not something new. They are devices which provide a means of direct, specialized communication with a computer. In July's Circuit Cellar I said that a pushbutton switch on the end of a long cable is probably the least expensive and most secure form of remote data entry. This is still true, but now it is time to look at more sophisticated forms of remote data entry.

There is no formal definition of what constitutes a remote data-entry terminal. The application defines the classification. While a regular videodisplay terminal can be used for data entry, remote data-entry terminals are usually specially fabricated to fit the application and environment. Remote data-entry terminals almost always communicate in duplex mode, and are capable of displaying computer directives to the operator as well as sending operator input to the computer.

A further refinement is that the buttons on the panel frequently have function/numeric nomenclature
rather than the character set we normally associate with keyboards. A key bearing the label "START" may in fact transmit an ASCII (American Standard Code for Information Interchange) " A " when pressed. Application software running on the control computer is used to recognize that a letter " A " means "initiate the process." The transmission length and protocol should be preset to reduce operator error and entry-panel complexity.

> Remote data-entry terminals are usually specially fabricated to fit the application and the environment.

For example, an entry terminal associated with a dip-plating line in a factory would probably have a panel with a numeric keypad and function buttons labeled "Bath 1", "Bath 2",
"Anode Current", 'Voltage", 'Time", and "Temperature". If the operator has to set the anode current in the plating tank, he presses the "Anode Current" button and then enters a four-digit value on the numeric keypad. When the control computer detects the anode-current function button being pressed, it reads the next four characters as numeric information pertaining to the anode-current function. Other function keys could have entirely different entry sequences.
To minimize error, most industrial data-entry terminals rely on considerable handshaking. At the very least, they include an accept/reject indicator for the operator. If the numeric portion of the anode-current entry did not fit within the limits prescribed for the process, a reject signal must be given to the operator so the data can be reentered.
In the more sophisticated units, the data-entry panel often incorporates an alphanumeric display. Usually, it is unnecessary to display textual material to the operator, and these

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Photo 1: Deluxe remote data-entry terminals, intended for industrial use, often contain specialized equipment to read card-badges, or control unusual functions. Many are constructed with a hazardous environment in mind, and are waterproof or blast-proof. This particular unit is a function/numeric panel (FNP) manufactured by General Digital Corporation in East Hartford CT.
displays are generally limited to a single line of sixteen to eighty characters. Gas-plasma displays or alphanumeric LED (light-emitting diode) matrices work well and are cost-effective in these applications.
Since the panel can communicate in both directions, it is possible for the operator to interrogate the process data base in the computer for specific information. Pressing the "Bath 1 " and "Temperature" buttons could result in the appearance of "\#1 TEMP $=192 \mathrm{C}^{\prime \prime}$ on the sixteen-character display for example.

## The entire remote dataentry terminal can be constructed with only two integrated circuits.

## Entry Panels for <br> Personal Computers

Deluxe industrial data-entry terminals include numeric keypads, function buttons, badge readers for operator identification, Hollerithcard readers for part identification, alphanumeric displays, and elaborate self-test features. A typical unit is
shown in photo 1 . They can be made waterproof, blast-proof, and idiotproof as required by the application. These are hardly attributes that suggest their use in the home. However, the concept of remote data-entry panels connected to a personal computer is not as alien as it once seemed.
In the past few months I have been presenting articles on various aspects of home control. If you have attached any control devices to your computer and have it controlling the lights and appliances around your home, you undoubtedly are using a program which manipulates logic outputs based on time, status of input sensors, and operator commands. What you have is in fact a practical, even if rudimentary, process-control system. It has fundamental similarities to the dip-plating system previously discussed.
There seems to be considerable interest in home control these days. Many new systems and peripheral devices have been introduced to meet the demand. In my opinion, however, they address only half the problem. They all seem to be limited to central-system use with no facility for remote data entry or effective human engineering.
The handheld remote-control devices I detailed in my July article

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## OT SYSTEM +



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Photo 2: The remote data-entry terminal. Using a new serial keyboard-interface integrated circuit, construction is simple and inexpensive.
were only one part of the solution. They facilitate operator feedback, to a limited degree, but like any transmit-only wireless device, they cannot be relied upon in critical applications. Consistent success in control can be obtained only with closedloop communications hard-wired directly between the operator and the control computer. If you press a button on the entry panel, the computer signifies acceptance of the command by flashing an LED or displaying "HEY, GOOD BUDDY."

The easiest way to satisfy the requirements of direct communication is to use a standard video-display terminal at each remote location. The environment in the average home is not as hazardous as a factory plating line. With video terminals at $\$ 700$ each, it is at least worth thinking about.

## Limitations of Video Terminals

However, one problem is that most video-display terminals have an RS232C serial output which is not supposed to be used for communication line lengths over 50 feet. Before you throw out the terminal you were saving to put in the bedroom at the end of the hall ( 51 feet from the computer), I should point out that this specified limitation becomes significant only at a data rate of $19,200 \mathrm{bps}$
(bits per second). At 300 bps , the problem is of less concern. I have personally driven 1000 feet of transmission line at 300 bps through an RS-232C port. This is a little unorthodox so don't tell anyone I told you.

There are many computer owners like me who don't particularly care to put a $\$ 700$ terminal in the garage. If your garage is anything like mine, you'd either have to keep it wrapped in plastic or periodically wipe the oil off, and dump the leaves and the dirt out of it. The average open-chassis video terminal would last about a week. Terminals specifically designed for these extremes would be very expensive and probably come in NEMA 4 or NEMA 10 (National Electrical Manufacturers Association specifications) oil- and water-tight enclosures.

## Build a Low-Cost <br> Data-Entry Terminal

The personal computer applications which would warrant using a $\$ 5000$ submersible data-entry panel are limited in number. I prefer instead to build something that is less expensive. A remote-entry panel, in the garage for instance, might only require functions such as lights on and off, alarm on and off, and maybe a few heating-system functions. A unit installed in the bedroom might have a couple additional functions.

For my own use, I felt I could be satisfied with a combination of ten numeric digit codes ( 0 thru 9 ) and ten function inputs. Control-system response could be handled adequately with an 8-bit display. Proper choice of components used in construction (with regard to temperature and voltage ranges, etc) would allow use of the panel in a slightly heated garage as well as the bedroom, and make it inexpensive enough to almost be considered disposable.

Thanks to a new serial keyboardinterface integrated circuit from Na tional Semiconductor, the entire remote data-entry terminal, shown in photo 2 , can be constructed with only two integrated circuits. The entry panel, which communicates with the host computer in standard 1200 bps serial format, can be placed as far away as 2 miles from the control computer with the addition of a line driver and receiver. With the exception of the hexadecimal display shown on the prototype, the entire

terminal can be built for under $\$ 50$.
The heart of my entry panel is the MM57499 serial keyboard-encoder circuit. This device bears some similarity to other scanning keyboard-encoder read-only memories sold by many manufacturers. It scans a 12 by 8 key matrix and produces the ASCII code for each key. However, using an inexpensive
color-burst ( 3.579 MHz ) crystal and an internal data-rate generator, it transmits the characters serially at 1200 bps. In addition, it has the capability to receive serial data ( 1200 bps) as well. This information can be displayed 1 byte at a time using a single 8 -bit shift register. The communications protocol in either case is fixed at 1 start bit, 8 data bits, 1 stop


Photo 3: It is amazing what can be done with so few parts. Most of the components shown here are quite common and easily available. The use of such materials as a colorburst crystal and a standard hexadecimal keypad make this project reliable and nearly bulletproof.


Photo 4: This twenty-eight-pin integrated circuit keeps things simple by performing the keyboard encoding and transmitting resulting data serially. It also takes care of display functions, with the addition, in figure $2 a$, of a single shift register.
bit, and no parity bit. The data rate can be changed by selecting a different crystal or injecting a TTL (transistor-transistor logic)-level clock signal into pin 2 of the MM57499.
A block diagram of the interface is shown in figure 1, and the schematic diagram is illustrated in figure 2. The keyboard I used is a standard twentykey hexadecimal pad. The keys are individually connected across the $X$ and $Y$ matrix inputs as shown. When the A key is pressed, it will short $Y_{B}$ and $X_{1}$ together sending out the ASCII code for lowercase " $a$ ". Pressing the shift key and the A key together will send an uppercase " A ". The ten letters A thru E and a thru e constitute our primary function keys. The numeric-digit keys 0 thru 9 are wired into the matrix in a similar manner. Pressing the shift key and a digit can provide ten more ASCII symbols as function indicators if needed. The key codes corresponding to the cross points of the matrix are outlined in figure 3. To change a particular key, simply determine which scan and strobe lines produce the desired code and wire the key between those points.

Three keys, F, H, and L in my unit, are given operations that are different from what their nomenclature might indicate. The F key is wired as a semicolon " $;$ ", the L key is wired as a Control "CTL" key and, the H key is now an Escape "ESC". These three keys facilitate using the programmable phrase feature of the MM57499.

During normal use, pressing the A key will send an "a". This could be interpreted by the host computer as the set-alarm signal to the home security system. To reduce potential problems, a numeric code or password could be required with all entries. Fortunately, frequent transmission of a lengthy password is not a problem.

The MM57499 contains a fourteencharacter programmable memory. Pressing a Control-Escape enables this function and automatically transmits a hexadecimal FA to tell the control computer that the panel is in the program mode. The next one to fourteen keystrokes (character or control) will be stored in memory. To halt the entry process, for instance after entering a password of "abAB", we just type a Control-semicolon. This will transmit the stored message

# COMPUSTAR 

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The system architecture of the Compustar is based on four types of video display terminals, each of which can be connected into an auxiliary hard disk storage system. Up to 255 terminals can be connected into a single network! Each terminal (called a Video Processing Unit) contains its own microprocessor and 64K of dynamic RAM. The result? Lightning fast program execution! Even when all users are on-line performing different tasks! A special "multiplexor" in the Compustar
or inquiry/response application. And if your terminal needs are more sophisticated, select either the CompuStar Model 20, 30 or 40 . Each can be used as either a standalone workstation or tied into a multi-user network. The Model 20 incorporates all of the features of the Model 10 with the addition of two, double-density mini-floppies built right in. And it boasts over 350,000 bytes of local, off-line user storage. The Model 30 also features a dual drive system but offers over 700,000 bytes of disk storage. And, the Model 40 boasts nearly $11 / 2$ million bytes of dual disk storage. But no matter which model you select, you'll enjoy unparalleled versatility in contiguring your multi-user network. Disk Storage System ties all external users together to "share" the system's disk resources. So, no single user ever need wait on another. An exciting concept ... with some awesome application possibilities!

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Figure 1: Block diagram of a minimal-component remote-entry panel, capable of serial communication with most host computers.
to the computer. The first time it is transmitted, a hexadecimal F9 is affixed to the beginning of the message to tell the computer that the terminal is no longer in the programming mode. At any time after this point, whenever a Control-semicolon is
pressed, the stored password will be transmitted. Reprogramming this phrase is accomplished by simply pressing Control-Escape again and repeating the sequence.

Receiving data from the control computer in response to an operator
input is where the real power of this interface becomes apparent. The computer can signify the acceptance or rejection of a command input, or the completion of a task by turning on one of the LEDs connected to IC2.

Text continued on page 42



## Multi-User

UniFLEX is the first full capability multi-user operating system available for microprocessors. Designed for the 6809 and 68000 , it offers its users a very friendly computing environment. After a user 'logs-in' with his user name and password, any of the system programs may be run at will. One user may run the text editor while another runs BASIC and still another runs the C compiler. Each user operates in his own system environment, unaware of other user activity. The total number of users is only restricted by the resources and efficiency of the hardware in use.

## Support

The design of UnifLEX, with its hierarchical file system and device independent I/O, allows the creation of a variety of complex support programs. There is currently a wide variety of sottware available and under development. Included in this list is a Text Processing System for word processing functions, BASIC interpreter and precompiler for general programming and educational use, native C and Pascal compilers for more advanced programming, sort/merge for business applications, and a variety of debug packages. The standard system includes a text editor, assembler, and about forty utility programs. UniFLEX for 6809 is sold with a single CPU license and one years maintenance for $\$ 450.00$. Additional vearly maintenance is available for $\$ 100.00$. OEM licenses are also available.

Fㅐ파눙
UniFLEX is offered for the advanced microprocessor systems. FLEX, the industry standard for 6800 and 6809 systems, is offered for smaller, single user systems. A full line of FLEX support software and OEM licenses are also available.


Box 2570. West Lafayette, IN 47906
(317) 463-2502 Telex 276143

[^3]Vのज』

## F

| 80 | 80 | 80 | 80 | FN1 |
| :---: | :---: | :---: | :---: | :---: |
| 81 | 81 | 81 | 81 | FN2 |
| 82 | 82 | 82 | 82 | FN3 |
| 83 | 83 | 83 | 83 | FN4 |
| 84 | 84 | 84 | 84 | FN5 |
| 85 | 85 | 85 | 85 | FN6 |
| 86 | 86 | 86 | 86 | FN7 |
| 87 | 87 | 87 | 87 | LS |
| 88 | 88 | 88 | 88 | IC |
| 89 | 89 | 89 | 89 | ADM |
| 8A | 8A | 8A | 8A | DE |
| 8B | 8B | 8B | 8B | BTAB |
| 8C | 8C | 8C | 8C | SC |
| 8D | 8D | 8D | 8D | CLEAR |
| 8E | 8E | 8E | 8E | EOS |
| 8F | 8F | 8F | 8F | EOL |
| 90 | 90 | 90 | 90 | BS |
| 91 | 91 | 91 | 91 | DL |
| 92 | 92 | 92 | 92 | DC |
| 93 | 93 | 93 | 93 | IL |
| 94 | 94 | 94 | 94 | FMT |
| 95 | 95 | 95 | 95 | $\dagger$ |
| 96 | 96 | 96 | 96 | + |
| 97 | 97 | 97 | 97 | $\rightarrow$ |
| 98 | 98 | 98 | 98 | - |
| 09 | 09 | 09 | 09 | TAB |
| 08 | 08 | 08 | 08 | BS |
| 7B | 7B | 7B | 7B | \{ |
| 7C | 7C | 7C | 7C | : |
| 7D | 7 D | 7D | 7D | \} |
| 7E | 7E | 7E | 7E | $\sim$ |
| 5F | 1F | 1 F | 1F | - |
| 30 | 30 | 30 | 30 | 0 |
| 31 | 31 | 31 | 31 | 1 |
| 32 | 32 | 32 | 32 | 2 |
| 33 | 33 | 33 | 33 | 3 |
| 34 | 34 | 34 | 34 | 4 |
| 35 | 35 | 35 | 35 | 5 |
| 36 | 36 | 36 | 36 | 6 |
| 37 | 37 | 37 | 37 | 7 |
| 38 | 38 | 38 | 38 | 8 |
| 39 | 39 | 39 | 39 | 9 |
| OA | OA | OA | OA | LF |
| 1B | 1B | 1B | 1B | ESC |
| 20 | 20 | 20 | 20 | SP |
| OD | OD | OD | OD | RTN |
| 2E | 2E | 2E | 2E |  |
| FF | FF | FF | FF | BREAK |




| 30 | 30 |
| :---: | :---: |
| 31 | 31 |
| 32 | 32 |
| 33 | 33 |
| 34 | 34 |
| 35 | 35 |
| 36 | 36 |
| 37 | 37 |
| 38 | 38 |
| 39 | 39 |
| 3A | 3A |
| 3B | 3B |
| 2C | 2 C |
| 2D | 2 D |
| 2E | 2E |
| 2 F | 2 F |
| 40 | 00 |
| 61 | 01 |
| 62 | 02 |
| 63 | 03 |
| 64 | 04 |
| 65 | 05 |
| 66 | 06 |
| 67 | 07 |
| 68 | 08 |
| 69 | 09 |
| 6A | OA |
| 6 B | OB |
| 6C | OC |
| 6D | OD |
| 6 E | OE |
| 6 F | OF |
| 70 | 10 |
| 71 | 11 |
| 72 | 12 |
| 73 | 13 |
| 74 | 14 |
| 75 | 15 |
| 76 | 16 |
| 77 | 17 |
| 78 | 18 |
| 79 | 19 |
| 7A | 1 A |
|  | $\mathrm{ON} \rightarrow \mathrm{FC}$ |
|  | $\mathrm{ON} \rightarrow \mathrm{FE}$ |
|  | No Code |



Table 1: Hexadecimal key-code assignments. Using this set of assignments, the computer can reply to data entered at the terminal. The data received at the remote terminal is displayed on eight LED indicators or an optional two-digit hexadecimal readout.


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Figure 2a: Schematic of the remote data-entry terminal. Use of the MM57499 serial keyboard-interface circuit allows for simple construction. Data is entered via a standard keypad, and encoded by the interface circuit. Data may then be sent serially at 1200 bps to the computer over any of a number of types of transmission line.

In this circuit, all diodes are 1N914s, and not all $Y$ n lines are used since a hexadecimal keypad does not require them. Holding any key down causes a $15-\mathrm{cps}$ automatic repeat.

## UCSD* System for TRS-80 Model II ${ }^{\dagger}$

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- Configuration program for serial I/O.
- Disk-set program to permit separate assignment of density and format characteristics for each disk drive.


## DOCUMENTATION

- UCSD System Manual (400 pages).
- Beginner's Guide To UCSD Pascal.
- Pascal User Manual \& Report.
- Fortran User's Manual with Fortran systems.


## PRICES

- UCSD System with Pascal Compiler $\$ 350$ with Pascal and Fortran Compilers $\$ 500$
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- P-Code Interpreter alone (either LSI-11 or Z-80)
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- UCSD System for MINC or PDT: - Z-80 Adaptable System (you write BIOS). - UCSD System for CP/M environments.

PCD Systems is a licensed distributor of the UCSD System for Pascal and Fortran. Dealer inquiries are invited.
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592 Weddell Dr., Sunnyvale, CA 94086. If your favorite dealer doesn't already carry Personal Software products, ask him to give us a call.


Figure 2b: Transmission-line drivers for the terminal are capable of transmitting over 10,000 feet of 100 -ohm line. The capacitors at pin 4 of IC4 and IC5 help to reduce noise pick-up by decreasing the frequency response of the receiver.


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Figure 2c: The MM57499 serial keyboardencoder integrated circuit, which scans a 12 by 8 matrix and produces the appropriate ASCII code for each key.

## Text continued from page 34:

This is accomplished by sending an ASCII character to the entry receiver that has a key code corresponding to the bits we wish to light. These codes are listed in table 1.

For example, to light the LSB (leastsignificant bit) of the display, a hexadecimal 01 is sent. This corresponds to a "Control-shift-A". The Break key code FF would turn on all the indicators. To successfully use these LEDs, a lookup table and bit map should be included in any software driver for the terminal. My prototype included both an 8-bit LED display and a two-digit hexadecimal display. They are wired in parallel and display the same information.

## Long Distance Transmission

No one bothers to construct a remote-entry terminal for placement next to the control computer. In most cases you will not have to resort to extraordinary means to communicate a couple hundred feet. Should you need to communicate long distances, such as 3000 feet to the barn, the linedriver circuitry of figure 2 should be used. It is capable of driving 10,000 feet of 100 -ohm transmission line. For short distances it isn't absolutely necessary to use this wire or circuit. A

| $\begin{aligned} & \text { SHIFT } \\ & \text { KEY } \end{aligned}$ | CONTROL | REPEAT | $\begin{aligned} & \text { CAP } \\ & \text { LOCK } \end{aligned}$ | $\begin{aligned} & \text { SHIFT } \\ & \text { LOCK } \end{aligned}$ | $2^{2}$ | $y^{Y}$ | $\times$ | $\mathrm{r}_{11}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $w^{w}$ | $v^{v}$ | $4^{u}$ |  | ${ }_{5}{ }^{\text {S }}$ | ${ }^{1} \mathrm{R}$ | $q^{\text {Q }}$ | $p$ P | $Y_{10}$ |  |
| $\bigcirc$ | $n^{N}$ | $m^{M}$ |  | ${ }^{k}$ |  |  | ${ }^{\text {H }}$ | $\mathrm{Y}_{9}$ |  |
| $\mathrm{g}^{\text {G }}$ | $\mathrm{f}^{\mathrm{F}}$ | $e^{E}$ | $\mathrm{d}^{\text {D }}$ | $c^{\text {c }}$ | $\square^{\text {B }}$ | $0^{\text {A }}$ | @ | $\mathrm{Y}_{8}$ |  |
| , ? |  | _ $=$ | , < | ; ${ }^{+}$ |  | 91 | 81 | $\mathrm{r}_{7}$ |  |
|  | $6^{8}$ | $5^{\%}$ | $4^{\text {\$ }}$ | $3^{*}$ | $2{ }^{\prime \prime}$ |  | $\begin{array}{ll} 0 \\ 0 & 0 \end{array}$ | $r_{6}$ |  |
| BREAK | . | RTN | SP | ESC | LF | 9 | 8 | $r_{5}$ |  |
| 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | $r_{4}$ |  |
| - DEL | $\sim \wedge$ | $\}^{\mathrm{J}}$ | $11$ | $\{1$ | BS | TAB | - | $Y_{3}$ |  |
| $\rightarrow$ | $\downarrow$ | 1 | FMT | IL | DC | DL | FS | $Y_{2}$ |  |
| EOL | EOS | CLEAR | sc | B TAB | DE | ADM | IC | $Y_{1}$ |  |
| LS | FN7 | FN6 | FN5 | FN4 | FN3 | FN2 | FN1 | $\mathrm{r}_{0}$ |  |
| $\mathrm{x}_{7}$ | $x_{6}$ | $\mathrm{x}_{5}$ | $\mathrm{X}_{4}$ | $\mathrm{x}_{3}$ | $x_{2}$ | $\mathrm{X}_{1}$ | $x_{0}$ |  |  |

Figure 3: Key function chart. Although not all scan lines are used for the hexadecimal keypad, the MM57499 circuit is capable of encoding the full ASCII character set. In the unit described, shorting $X_{3}$ and $Y_{5}$ produces an ESC (Escape) code, while shorting $X_{5}$ and $Y_{5}$ gives the code for 5 .
pair of MC1488 and MC1489 RS232C drivers can be substituted for short runs and twisted-pair wiring used instead of 100 -ohm cable. The degree of leeway allowed depends upon the electrical noise between the terminal and the computer. If in doubt, use the heavy-duty driver I've outlined.
Whether you build this interface or not is immaterial so long as you recognize the advantages it presents for those readers interested in control applications. I've only scratched the surface concerning the capabilities of the MM57499. We could also have used it as a single-chip remote-status transmitter, or we could have expanded the receiver section for full message displays. Trying to cover all
potential applications is impossible in a single article. I assure you that I am not through with this device, and I'll think up a few more gadgets that use it. If in the meantime you have any brainstorms concerning home control, I'd appreciate hearing about them.
For information on the MM57499 write to:

## Mike Van Slack <br> Product Marketing Engineer <br> National Semiconductor <br> 2900 Semiconductor Dr <br> Santa Clara CA 95051

Next Month: We will explore some ways to use LCDs (liquid-crystal displays).

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## Personal Computer Features

The C8P DF's specs beat all personal computers hands down. It executes instructions two to three times faster, and displays more alphabetic characters on its screen than other

# puter explorations. 

## Business Computer Features

The C8P DF utilizes dual $8^{\prime \prime}$ floppy disk drives which store up to eight times as much information as personal computer mini-floppies, and an available double-sided option expands capacity to 1.2 megabytes of on-line storage. The C8P DF is compatible with Ohio Scientific's business computer software, including OS-65U an advanced operating system, and an Information Management System (OS-DMS) with supplementary inventory, accounting, A/R-A/P, payroll, purchasing, estimation, educational grading and financial modeling packages. The system also supports word processing (WP-3) and a fully integrated small business accounting system (OS-AMCAP V1.6). The C8P DF's standard modem and printer ports accept high-speed matrix printers and word-processing printers directly.

## Home Control and Industrial Control

The C8P DF has the most advanced home monitoring and control capabilities ever offered in a computer system. It incorporates a real time clock and a unique FOREGROUND/
BACKGROUND operating system which allows the computer to function with normal BASIC programs, at the same time it is monitoring external devices. The C8P DF comes standard with an AC remote control interface, which
allows it to control a wide range of AC appliances and lights remotely, without wiring, and an interface for home security systems which monitors fire, intrusion, car theft, water levels and freezer temperature, all without messy wiring. In addition, the C8P DF can accept Ohio Scientific's Votrax voice I/O board and/or Ohio Scientific's new universal telephone interface (UTI). The telephone interface connects the computer to any telephone line. The computer system is able to answer calls, initiate calls and communicate via touch-tone signals, voice output or 300 baud modem signals. It can accept and decode touch-tone signals, 300 baud modem signals and record incoming voice messages. These features collectively give the C8P DF capabilities to monitor and control home functions with almost human-like capabilities.

For process control applications, a battery back up calendar clock with automatic computer restart capabilities is available. Ohio Scientific's unique accessory ports allow the connection of a nearly unlimited number of 48 line parallel I/O cards and 12 -bit high speed instrumentation quality analog I/O modules to the computer by inexpensive 16 -pin ribbon cables.

## Exploring New Frontiers

Ohio Scientific's vocalizer software processes normal BASIC print statements with conventional spellings and speaks them clearly in real-time
on computers equipped with the UTI (CA-15B or CA-14A). This voice output capability, combined with the C8P's remote control, remote sensing, telephone interface capabilities and reasonable cost open up new frontiers for computer applications.

## Documentation

The C8P DF is not a beginner's computer and doesn't come with beginner's documentation. However, Ohio Scientific does offer detailed documentation on the computer which is meaningful for experts, including a Howard Sams produced hardware service manual that includes detailed block diagrams, schematics, parts placement diagrams and parts lists. Ohio Scientific is now also offering fully documented Source Code in machine readable form for OS-65D, the Challenger 8P's operating system allowing experimenters and industrial users to customize the system to their specific applications.

## What's Next?

Ohio Scientific is working on a speech recognizer to complement the UTI system, with a several hundred word vocabulary. The company is also developing an 8 megabyte low-cost, add-on hard disk for use in conjunction with natural language parsing to further advance the state-of-the-art in small computers. The modular bus architecture of the C8P assures system owners of being able to make use of these new developments as they become available just as the owner of a 1976 vintage Challenger can directly plug in voice output, the UTI and other current state-of-the-art OSI products.
The C8P DF with dual $8^{\prime \prime}$ floppies, BASIC and two operating systems costs about $\$ 3000$, only slightly more than you would pay for a dual mini-floppy equipped personal computer with only a fraction of the capabilities of the C8P.
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# An 8088 Processor for the S-100 Bus <br> <br> Part 1 

 <br> <br> Part 1}

## Thomas Woodward Cantrell <br> 2475 Borax Dr Santa Clara CA 95051

The 16 -bit microprocessor has definitely arrived. No one doubts that this new wave of high-performance processors will soon be operating on the familiar S-100 bus. In fact, Seattle Computer Products is already shipping its Intel 8086-based processor card, along with a support card that includes vectored-interrupt control, hardware mathematical operations, and miscellaneous timer/counters.

Godbout Electronics has designed a card containing two microprocessors and the logic allowing transfer of control between them by software. One of the processors on this board is an Intel $8085 \mathrm{~A}-2$, which allows
the board to be placed in 8080A/8085A/Z80A-based S-100 systems with a minimum amount of hassles.

Using various existing or soon-to-be-developed cross-software products, programs can be developed for the other processor on the board, the Intel 8088. When the new software is developed and loaded, control can be transferred from one microprocessor circuit to the other for checkout and debugging. This is a novel solution to the problem of bootstrapping a system consisting of both new hardware and new software.

Microsoft and Digital Research,
both highly renowned producers of quality software, are making their contributions to the processor revolution. Microsoft is already shipping an 8086/8088 version of its popular BASIC interpreter as well as an 8086/8088 cross-macroassembler which runs under Digital Research's CP/M. A disk operating system and other system software are to follow.

Digital Research has an $8086 / 8088$ based version of CP/M in the works. Expect this to be followed with new versions of MP/M and PL/I. The multitude of vendors who supply software to run under CP/M should already be converting their software for use with the new $C P / M$.

## Problems Remain

Be that as it may, the S-100/16-bit processor picture is not as bright as it may seem. The fundamental problem is that the $\mathrm{S}-100$ bus was originally designed by MITS (of Altair fame) for the Intel 8080, an 8 -bit microprocessor. To "upgrade" the S-100 bus to the higher levels of performance offered by the new machines, certain problems must be addressed. The IEEE (Institute of Electrical and Electronics Engineers) Standards Committee, through its S-100 bus standard definition, has assured a future for the $\mathrm{S}-100$ bus in two ways.

First, the problem of incompatibility between different "S-100" modules will be laid to rest. Woe be unto today's computerist who attempts to use a Brand X DMA color video-display board with a Brand $Y$


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Z80 processor card and a Brand Z dynamic memory board. If they all work together it's a miracle! By more clearly defining things like DMA (direct-memory access) protocols and timing, as well as eliminating some of the archaic or abused S-100 signals, board compatibility can more likely be assured.

Second, provisions have been made to ease the adaptation of new, higher-performance processors to the bus. This expandability has been achieved in three distinct ways:

- 16-Bit Data Transfers - MITS chose to split the 8080's bidirectional 8 -bit data bus into separate input and output data buses. While the wisdom of this was often questioned, it has proven to be a saving grace. The IEEE S-100 standard adds two signals (SXTRQ*, Sixteen Request, and SIXTN*, Sixteen Acknowledge) to allow 16-bit data transfers by ganging the input and output data bus.(Note that throughout this article I will use the "*" notation to designate active low signals; this is the accepted usage in the IEEE standard.)
- Extended Memory Addressing - Eight of the unused S-100 bus lines have been designated as address lines A16 thru A23. With 24 address bits (A0 thru A23), 16 megabytes of memory can be addressed directly.
- Extended I/O (input/output) Addressing - The 8080 was capable of addressing 256 I/O ports. The 8 -bit I/O port address was placed on both the low byte (A0 thru A7) and high byte (A8 thru A15) of the 16-bit address bus. The IEEE standard allows this echoing of the port address on both halves of the address bus, but recommends that A0 thru A15 be used for I/O addressing. The 16-bit I/O address gives $\mathrm{S}-100$ systems the ability to directly utilize up to 64 K I/O ports.

These standardization efforts will allow a controlled evolution of the S-100 bus. However, I realize that of the dozens of S-100 boards I have (including some of very recent vintage), probably none meets the IEEE standard. I cannot afford to replace them

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Figure 1: The internal architecture of the 8088. By combining a 16-bit execution unit with an 8-bit bus-interface unit, the 8088 can use a powerful instruction set and still remain compatible with most existing hardware. The functional division of processing allows the 8088 a speed advantage by performing fetch and execute concurrently.
all. In fact, my IMSAI computer's front panel does not meet the standard either.

## A Solution

Intel's 8088 microprocessor is a remarkable machine. By combining a 16-bit execution unit with an 8 -bit bus interface, the 8088 can represent the best of both worlds for many users. (See figure 1.) In particular, the 8088 allows you to reap the benefits of a powerful new architecture while preserving your investment in 8-bit hardware. In addition, many data-handling-oriented applications (such as intelligent terminals, data concentrators, and small business computers) are more naturally implemented with a machine that communicates using 8 -bit characters.

## New Architectures

The microprocessor revolution is fascinating because it represents a microcosm of the computer revolution. In the last 5 years we have seen computers on silicon follow the footsteps of 30 years of computing history. The effort of the computing pioneers has not been in vain, for it has served to chart our course.

Consider current VLSI (very large
scale integration) processing technologies. Semiconductor manufacturers have the capability of placing 30,000 transistors on a chip of silicon today, with as many as 100,000 in the near future. Now imagine a second-generation mainframe computer of the 1960s. It fills an airconditioned room and consists of large metal boxes and massive power supplies. Inside some of the metal boxes are large racks filled with circuit cards. These circuit cards are covered with transistors, resistors, and capacitors. Today, the computing equivalent of these metal boxes is a small group of integrated circuits.

The user may be initially impressed by the complexity of the computer being used, but he will ultimately judge the machine on the basis of its power and ease of use; therefore, the challenge for the manufacturers is not as simple as maximizing the number of devices. The problem is designing microprocessors that respond to the needs of the user.

The high-performance solution is to implement mainframe architectures that contain tried and proven virtues. Concepts like attached coprocessors, concurrent I/O process-
ing, pipelining, memory segmentation and hardware mathematical operations are being adopted and put on silicon. When I say the architecture of the 8088 is "new and revolutionary," I am really saying that the day of the "mainframe-on-a-chip" has arrived.

## The Best of Both Worlds

The 8088 contains two processors in its 40 -pin package. One is called the EU (execution unit) and the other is the BIU (bus-interface unit). The BIU is optimized for communicating with the rest of the computer system, while the EU is optimized for executing programs.

The EU most closely resembles what is conventionally considered the processor; it contains the working registers, the status flags, and the ALU (arithmetic/logic unit). As its name implies, this is where programs are executed.

The EU of the 8088 is the same as the one in the 16 -bit 8086 processor. All the registers (twelve of them) are 16 bits wide, though some of them can be treated as two separate 8 -bit registers by the programmer. In addition, all math operations (addition, subtraction, multiplication and division) can utilize 16-bit operands.

The 8 -bit BIU manages much of the work associated with the address, data, status, and control bus interfaces. The BIU of the 8088 uses an 8 -bit data bus for receiving and transmitting data, as opposed to the BIU of the 8086, which uses a 16 -bit data bus. An example of the bushandling optimization of the BIU is that the speed requirements placed on the rest of the system (ie: memory and $1 / O$ devices) are very easy to deal with. An 8088 running at 5 MHz can use relatively slow memories (ie: 450 ns access time) with no wait states. Save those old, slow memory boards.

The connection between the BIU (which fetches and stores data) and the EU (which processes the data) is the queue or pipeline. The BIU keeps the pipeline filled with instructions fetched from memory, while the EU draws instructions from the queue as it needs them.

In less sophisticated computers, the rest of the system (especially memory) might sit idle, waiting for the processor to finish a long instruction. To eliminate this waste of

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system resources, the BIU of the 8088 will fetch more information and put it in the queue for later use by the EU. Similarly, when the BIU tries to read some extra-slow memory and encounters a wait state, the EU can continue reading instructions from the queue and executing them. All the EU ever "sees" is the queue, regardless of differences in the BIU that feeds it.

This powerful internal architecture, combined with the simple 8 -bit I/O, makes the 8088 a natural for the S-100 and other 8-bit buses.

## Design and Interfacing

My S-100/8088 board is designed as a simple, yet powerful, base com-
puter with the support logic necessary to interface to the S-100 bus. I will explain the design accordingly by first discussing the design of the minimal system, and then the techniques for interfacing to the S-100 bus.

Several years ago it would not have been uncommon to overhear: "My computer's got a microprocessor, 2 K bytes of EPROM, 1 K bytes of programmable memory, and a couple of I/O ports." Today, the same machine can be created using four integrated circuits. In fact, such a system is shown in figure 2.

This system uses a 5 MHz 8088 processor, driven by an 8284 clock generator, with an 8185-2 1-K-by-8-
bit static memory circuit and an 8755A-2 2-K-by-8-bit EPROM (erasable programmable read-only memory). The 8755A-2 also includes two 8-bit parallel ports.

Notice how simple the basic system is. Each part was designed with compatibility in mind, so the interfacing task is essentially "connect the dots."

## The 8088 Microprocessor

In the following section, detailed hardware aspects of these key components will be discussed. My reference is Intel's 8086 Family User's Manual, which contains a wealth of information on the 8088,8086 , and other high-performance members of


Figure 2: A minimum system is possible with the 8088 family using only four dual-in-line packages. This system uses a 5 MHz 8088 central processor, driven by an 8284 clock generator. An 8185-2 1-K-by-8-bit programmable memory and an 8755A-2 2-K-by-8-bit EPROM provide system memory and two 8 -bit parallel I/O ports. Active-low signals are shown in the figures using the overbar notation, rather than asterisks.



Figure 3: The pinouts assigned to the 8088 microprocessor package. Notice that many pins serve dual functions depending on the mode selected (either minimum or maximum). Maximum mode is designed to facilitate concurrent processing, using the I/O processor and arithmetic processor also available in the 8088 family.
the family. See figure 3 for the 8088 pinouts.

The following paragraphs describe the function of the 8088 pins:

AD0 thru AD7: These form the time-multiplexed address and bidirectional data bus. In other words, they sometimes contain address information (A0 thru A7) and other times contain data ( D 0 thru D 7 ). The obvious benefit of multiplexing is that eight fewer pins are needed on the package.

ALE (Address Latch Enable): The 8088 asserts ALE whenever the multiplexed address/data bus contains valid address information. ALE serves two fundamental purposes.

- When connected to other multiplexed-bus components (as in figure 2), ALE is a signal to them that the processor has address information on its address/data bus.
- We may want to demultiplex
the bus-in other words, the rest of the system may want to see a separate address bus and a separate data bus (the S-100 standard requires two separate buses). ALE can be used to strobe address information into a latch (hence the "latch enable" part of its name) (see figure 4).
A8 thru A15: These are address lines; they are not multiplexed.

You may note that the multiplexed bus and many of the following hardware-interface facets of the 8088 are the same as those of the popular 8085 A . The 8088 is upward compatible with many existing 8085A designs, and the 8088 can easily use all the peripheral components designed to support both the 8080A and the 8085A

A16/S3 thru A19/S6: The upper four address lines (A16 thru A19, also known as S3 thru S6) extend the addressing capability of the 8088 to 1 megabyte. This is a very real perfor-


Figure 4: The ALE signal from the 8088 microprocessor is used to latch address information into an 8282 buffer. The buffer output is demultiplexed address information which has been separated from data that appears on the same pins.

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mance improvement over most 8-bit processors (usually limited to a 64 K byte address space). These address lines are multiplexed with status information. During the early part of a bus cycle (T1, the first clock period of the four-clock bus cycle), a valid address is present. Then from clock cycles T2 to T4, each of these pins contains status information as follows:

- S6: This signal is always low.
- S5: This signal reflects the state of the EU's interrupt-enable flipflop. If this signal is high, it in-
dicates that the processor can accept interrupts. If it is low, interrupts are currently disabled.
- S3 and S4: These two pins can encode four possible states. These states reflect the segment register used in forming the address for the current bus cycle. (See table 1.) This information can be used for monitoring program execution or for analyzing program performance. There is also the potential for implementing a memory bankswitching scheme, where the two lines are used to choose one


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of four areas (banks) of memory.
MN/MX*: Reflecting the needs of different users, the 8088 can be operated in two different modes. If $\mathrm{MN} / \mathrm{MX}^{*}$ is high, the processor is in minimum mode; if this input is low, the processor is in maximum mode. Depending on the mode ( min or $\max$ ), certain pins on the processor will serve different purposes. In $\min$ mode the processor is responsible for generating all bus-control signals. In max mode, control signals are generated by an 8288 bus controller.

The control signals put out by the 8088 in min mode are then replaced with other signals that facilitate the design of higher-performance (and generally more expensive) systems. These max mode signals include a hardware bus lock, queue status information and the implementation of a memory access request/grant protocol used in multiprocessing.

The max mode gives a computer the ability to use multiple processors (eg: an 8088 processor with an 8089 concurrent-I/O processor and an 8087 ultra-high-performance numeric-data processor). Note: both min and max modes allow the 8088 to address the full megabyte of memory.

My S-100/8088 board is implemented in min mode, so when a signal that differs for $\min$ or $\max$ mode is defined, the min mode definition will be used.

RD*: This is the general-purpose read signal that latches data from memory or an I/O device (the device involved depends on the state of $10 / \mathrm{M}^{*}$ ) into the 8088.

WR*: This is the general-purpose write signal. The 8088 uses $W R^{*}$ to output information to memory or I/O devices.
$I O / M^{*}$ : This line indicates whether the processor is communicating with I/O devices or

|  |  |  |
| :---: | :---: | :--- |
| S4 | S3 | Segment |
| 0 | 0 | EXTRA |
| 0 | 1 | STACK |
| 1 | 0 | CODE or none (ie: I/O) |
| 1 | 1 | DATA |
| Table 1: Possible interpretations of in- |  |  |
| formation on pins S3 and S4 of the |  |  |
| 8088. Each of the four states is |  |  |
| associated with the segment register |  |  |
| that helped form the current address. |  |  |

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memory.
DEN* and DT/R*: The data enable (DEN*) and datatransmit/receive (DT/R*) signals are primarily for use with the 8286 and 8287 bus transceivers. These devices serve to buffer the information going to or from the 8088 processor. DT/R* configures the transceiver for either the transmission or reception of data. DEN* is used to enable the 8286 or 8287 at the correct time. Since my system does not use these transceivers, DEN* and DT/R* are not used.

INTR: This interrupt-request line is the general-purpose interrupt input. The ability to receive interrupts can be masked via software using the clear interrupts (CLI) instruction, (similar to the 8080A DI instruction). If interrupts are not disabled, the processor will vector (ie: jump) to an appropriate interrupt-handling routine (see INTA*, below).

INTA*: Upon receipt of an INTR instruction, the 8088 will begin an (INTA*) interrupt-acknowledge sequence. The INTA* signal is used to read an interrupt type vector. Without going into details, this type
vector is used by the 8088 to determine the actual address of the appropriate interrupt routine. Commonly, INTA* and INTR are connected directly to an 8259 A programmable priority-interrupt controller, allowing an easy implementation of powerful and flexible interrupt-driven systems.

NMI: The nonmaskable interrupt line NMI is an input similar to the more general INTR except for two fundamental differences:

- Receipt of NMI does not generate an INTA* sequence; rather, a fixed location (stored at hexadecimal address 08) is immediately vectored to.
- NMI interrupts cannot be masked (ie: via the CLI instruction, as for INTR); NMI interrupts are usually reserved for catastrophic events such as imminent power failure or recurrent bus errors.

READY: READY is an input to the 8088 which indicates that an addressed memory or I/O device is currently capable of completing an input
or output data transfer. The 8088 will enter and execute wait states (idle clock cycles with all control and address lines valid) until READY is brought high. This signal is normally used to allow operation with slow memories or I/O devices. It is also handy for implementing hardware single-step capability via a front panel switch.

TEST*: This unique input line, in combination with an associated software instruction, yields a powerful hardware/software debugging capability. It works like this: when the 8088 executes a WAIT (wait for TEST*) instruction, it immediately examines the state of the TEST* input line. If TEST* is low, execution simply continues with the next instruction; however, if TEST* is high, the processor waits in an idle state. TEST*, combined with the above mentioned READY-signal-based single-stepping capability, provides a powerful debugging aid that I have exploited in my design.

Another use for TEST* is the synchronization of concurrent processing. An example will serve to explain this more fully.

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Imagine a max-mode 8088 system that also utilizes an independent 8089 I/O processor. A common occurrence will be the 8088 issuing a "command" to the 8089 to perform some I/O function (such as reading from or writing to a disk, or printing on a printer). While the 8089 is doing this, the 8088 can continue executing the user's program (resulting in concurrent or simultaneous processing).

However, in some cases, the 8088 must wait for the 8089 to finish its I/O task. For example, the user's program may not be able to continue processing until data is retrieved from a disk. In this case, the 8088 will command the 8089 to perform the read operation and will then execute a WAIT instruction. Meanwhile, the 8089 pulls the 8088's TEST* input high until the I/O operation is complete. When the operation is finished, the 8089 will bring TEST* low and the 8088 can continue executing.

SSO*: This is a status output line which, combined with $10 / \mathrm{M}^{*}$ and $D T / R^{*}$, allows complete decoding of the current 8088 status. (See table 2.)

RESET: A high-logic state on this input causes the 8088 to terminate its present activity and restart execution. The CS (code-segment) register is set to hexadecimal OFFFF and the IP (instruction pointer) is reset to 0 , resulting in an absolute restart address of hexadecimal OFFFFO. (See figure 5.)

CLK: This is the clock input to the processor and is normally driven by the 8284 clock generator. It is a 5 $\mathrm{MHz}, 33 \%$ duty-cycle signal.

## The 8284

The 8284 clock generator is used to generate an optimal clock signal for the 8088 and condition some of the basic processor-control signals. (See figure 6.) Some of its functions are more directed towards max-mode
multiprocessing bus control and will not be discussed here.

The following paragraphs describe the functions of the 8284 pins.

X1 and X2: These pins are attached to the crystal that generates the fundamental clock frequency. Note that the crystal frequency is three times the desired operating frequency (ie: 15 MHz for a 5 MHz 8088 ). It is also recommended that a 3 pF to 10 pF capacitor be connected in series with X2.

CLK: This is the optimized clock output that is directly connected to the 8088 CLK input.

PCLK and OSC: The peripheral clock line (PCLK) is a TTL (transistor-transistor logic)-level, $50 \%$ duty-cycle clock output of the 8284 with a frequency of half that of the CLK output. The OSC line is similar but operates at the crystal frequency (eg: a 15 MHz crystal gives a 15 MHz OSC signal, which drives a 5 MHz 8088 CLK signal and a 2.5 MHz PCLK signal).
F/C*: The frequency/crystal select line allows generation of a clock signal using either a crystal or an external frequency input (see EFI below). Since I use a crystal, F/C* is tied low in my system.

EFI (External Frequency In): If F/C* is high, the 8284 will use the EFI input line to generate the CLK and PCLK signals. Once again, the CLK output will be one-third the frequency present on EFI (OSC and PCLK act the same too).
AEN1*,AEN2*, RDY1, and RDY2: These signals are primarily used in multiprocessor systems; however, I do use RDY2 to condition the system READY signal for use by the 8088. AEN1*, AEN2* and RDY1 are not used in my system.
READY: As mentioned previously, this 8284 output line is a conditioned and synchronized reflection of the in-

| IO/M* | DT/R* | SSO* | Status |
| :---: | :---: | :---: | :--- |
|  |  |  |  |
| 1 | 0 | 0 | Interrupt Acknowledge |
| 1 | 0 | 1 | Read I/O port |
| 1 | 1 | 0 | Write I/O port |
| 1 | 1 | 1 | Halt |
| 0 | 0 | 0 | Code Access |
| 0 | 0 | 1 | Read Memory |
| 0 | 1 | 0 | Write Memory |
| 0 | 1 | 1 | Passive (idle) |

Table 2: The status of the 8088 processor is completely encoded by the three signals above.


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puts at RDY1 and RDY2 and is tied directly to the 8088 READY input.
RES*: The reset-in signal (RES*) is an 8284 input line that is connected to the system RESET line (through a front-panel switch). Power-on-reset as well as proper input conditioning are obtained by the use of an appropriate resistor/capacitor timing network.
RESET: This is the conditioned reset output of the 8284 (based on the RES* input) and is tied directly to the 8088 RESET input line.

## The 8755A-2 and the 8185-2

Looking at the 8755A-2 and 8185-2 pinouts (see figures 7a and 7c), we immediately notice that a lot of the signals are common to the 8088 and
have already been discussed. ADO thru AD7 (and other address lines), ALE, $10 / \mathrm{M}^{*}, \mathrm{RD}^{*}, \mathrm{WR}^{*}$ and RESET are all used. This illustrates what I said earlier about the "connect the dots" ease of design using these multiplexed-bus components. Simply connect the 8088 pins AD0 thru AD7 to 8755A-2 pins AD0 thru AD7 and the 8185-2 pins AD0 thru AD7. Then connect the 8088 ALE pin to the 8755A-2 ALE pin and the 8185-2 ALE pin, etc.

The $8755 \mathrm{~A}-2$ is a 2 -K-by-8-bit EPROM (erasable programmable read-only memory) much like the familiar 2716 . The " -2 " suffix means that it can run reliably at 5 MHz , compared to the 3 MHz rating of the standard 8755A. Two useful


Figure 5: Calculation of the reset address on the 8088 . The 8088 reset address is derived from the code-segment register, which is set to hexadecimal OFFFF, and the value in the instruction pointer, which is reset to 0 .
enhancements are the addition of two independent 8 -bit bidirectional parallel I/O ports and the use of the multiplexed bus; these make the system-design task much easier. The 8755A-2 is programmed in much the same way as the 2708 and the 2716 , but differences do exist. Also, most EPROM programmers do not have $40-\mathrm{pin}$ sockets. I hope some enterprising experimenter will develop an 8755-2 "byteburner" for the S-100 bus. This might be as simple as a "pin-scrambler" adapter (with a little extra circuitry) for existing EPROM programmers.

The $8185-2$ is a $1-\mathrm{K}-$ by-8-bit static memory circuit that is quite easily interfaced to the multiplexed bus. The byte-wide organization, low power and small physical size (only eighteen pins) make this a natural for minimal systems.

## A Base on Which to Build

The front panel on my IMSAI computer has many functions that are irretrievably tied to the 8080A instruction set. As an example, when I enter an address on the front panel address switches and push the Examine switch, the front panel "jams" an 8080 JMP (jump, hexadecimal C3) instruction onto the processor's data bus; allows the processor to execute the jump while jamming the address I entered on the switches onto the data


Figure 6: The 8284 clock generator. This device provides an optimum clock signal and serves to buffer and condition some of the basic processor signals. Figure $6 a$ shows the pin labeling for the device, while figure $6 b$ shows a block diagram of its intemal structure.


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Figure 7: Pinouts and block diagrams of the 8755A-2 EPROM (figures 7a and 7b) and the 8185-2 user programmable memory (figures 7c and 7d). These circuits were designed specifically to work with the 8088 multiplexed bus lines; they provide two 8 -bit parallel I/O ports without any additional hardware.
bus; and finally stops the processor once the jump is completed.

Similarly, for Examine-Next and Deposit-Next functions, the front panel jams and executes a NOP (nooperation, hexadecimal 00) instruction to move on to the next location.

The JMP and NOP instructions for these switch functions are hardwired into the front-panel circuitry; circuit traces must be cut to change them. Since the operation codes for the 8088 are completely different, every attempt at front-panel operation would produce bizarre results. Other difficulties include the two's-complement representation of 8088 JMP addresses and the IMSAI's use of S-100 control signals that have been outlawed by the IEEE standard.

Because of these difficulties, I decided to base my 8088 project on a different S-100 system. Fortunately, I was able to scrounge a vintage BYT-8 S-100 box at the local electronic flea market for a good price. The box did not contain any circuit boards, but the metal panel on the front did have cutouts for various LEDs (lightemitting diodes) and switches, which I used to implement a minimal front panel (see photo 1). While I agree with the principle of turnkey systems, which have only power and reset switches, a front panel is a useful tool for debugging any new hardware design. The front panel is a "window" into the machine, one that is needed in case the system does not work perfectly the first time.

## Next Month

Next month's installment will cover some of the more interesting aspects of interfacing to the S-100 bus, including the amount of TTL "glue" necessary to emulate the control and status signals of the S-100 standard and the construction of the actual processor board.

## References

Both the 8086 and 8088 microprocessors have been discussed by Steve Ciarcia in "Ciarcia's Circuit Cellar" articles in BYTE, as follows:

1. 'The Intel 8086', November 1979 BYTE, page 14.
2. "Ease Into 16 -Bit Computing: Get 16 -Bit Performance from an 8-Bit Computer", March 1980 BYTE, page 17.
3. "Ease Into 16-Bit Computing, Part 2 : Examining a Small Multi-User System", April 1980 BYTE, page 40.

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## Time Your Tape

John O'Flaherty, St Louis Veterans Administration Medical Center, St Louis MO 63125

Recently I was involved in a research program that required long-term recording (eight hours) of physiological data on an analog instrumentation recorder. We needed a quick method of searching the tape for information occurring at certain times. Although a time marker was recorded on one channel, it could not be played back during fast-forward operation. Unfortunately, although the take-up-reel turns counter indexed unique locations on the tape, the readings obtained did not correlate simply with time. Obviously, one turn on a fully wound reel contains at least twice as much tape as one turn on a bare hub.

I developed a computer solution to the problem. Given the diameter of the take-up-reel hub, the length of the tape, and the turns-counter reading at the end of the tape, the program of listing 1 prints a table relating turnscounter reading, elapsed time, remaining time, footage used, and footage remaining.
The method used is simple (now!): the single datum needed is an accurate value for tape thickness as wound, and it is found by considering the side of the tape first as a very long, very thin rectangle, and then as a circle. The area of the side of the tape (ie: what is seen as you face the reel on its axis) can be approximated by a linear function of tape thickness:

## Area $=$ Tape Thickness $\times$ Tape Length

or by a nonlinear function of tape thickness:
Radius $=$ Tape Thickness $\times$ Turns Count + Hub Radius Area $=\pi \times(\text { Radius })^{2}-$ Hub Area


Figure 1: Area occupied by the side of a given length of tape as tape thickness is changed. The $X$-axis value at the nonzero intersection of area calculated by two different methods must be the actual tape thickness.


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Lines 30 thru 80 of listing 1 find the intersection of these two functions by iteration for a fully wound reel of tape. (See figure 1.) Then lines 95 thru 230 generate a table by finding area through radius, and length and time from area for turns-counter increments of ten.
It has not been possible to test the routine on the instrumentation recorder yet, but I have applied the method to my own cassette recorder with very good results. For a C-60 cassette, which actually runs 32 minutes, 23 seconds per side, the tape length was precalculated to be ( $1943 \mathrm{~s} \times 1 / \mathrm{ips} / 12$ ) $=303.6$ feet. By carefully disassembling the cassette, the hub diameter was found to be 0.8525 inches (five cassettes from different manufacturers were found to be identical in this respect). The ratio of indicated to actual turns of the takeup reel was found by turning the reel one hundred turns by hand (an index mark helps), and noting the turnscounter reading.
Then the program was run and table 1 (see page 74) was printed, and its accuracy was tested by actually running the tape and noting the times for turns-counter increments of ten.
The test results are printed as the last two columns in the table. As can be seen, the worst case error is 5 seconds, or $0.3 \%$ of the total time, which is surprisingly good, in view of tape counters' reputed inaccuracy, and the fact that no empirical trimming was done-the algorithms simply try to represent the physical realities of the situation.
One might also use the formulas above to program a portable calculator to find time for turns count or vice versa, without consulting a table.

Listing 1: An Applesoft BASIC program for correlating turnscounter readings with time. All documentation statement line numbers end in 5, and they may be ignored when keying in the program.

```
5 REM SET CONSTANTS & MENTIDN YA
        RIAELES FOR EFFICIENCY
10 FI = 3.141592654:T,! = 2:, = 1:
    TW = 12:00 = 000001:HS = 0:
    TT = 0:ITC = 0:HH=3600:MM =
        60:HF = 0.5:TC = 0
E0 HDME : GOTD 1000
2.5 REM FINI ACTUAL TAPE THICKME
        SS
30 PRINT "CALCULATING TAPE THICK
        HESS AS UILIND..."
35 FEM FREA EY FI (R^Z) MIST EDUIA
        L
40 H1 = FI & \MTC TT + H5` A T
        W - HS ^ TW)
4.5 REM FFEF FY L*S
50 H2 = ML * TT * TV
GOLR = F1 < RE:TT = TT < OR
G FEM SD TR'Y HE|N TT TILL IT IID
PO IF AES (0 - ER) > DO THEN 40
80 FFINT : RETURN
95 REM GEMEFHTE TABLE
100 FDF ITC: = 0 TD MIT STEP 10

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}

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Listing 1 continued：
\(110 \mathrm{TE}=\mathrm{ITE}\)－IR
\(12 \mathrm{~F}=T \mathrm{~T} \cdot \mathrm{TT}+\mathrm{HS}\)
\(13 \mathrm{H}=\mathrm{FI}\)－ \(\mathrm{F} \times \mathrm{TW}-\mathrm{HS} \times \mathrm{TW}\)
\(140 \mathrm{~L}=\mathrm{H} / \mathrm{TT}\)
\(150 \mathrm{~T} 1=L<\triangle \mathrm{SH}\)
1E0TE \(=M \times T-T 1\)
\(170 F 1=I M T \& L<T V+H F)\)
\(18 \mathrm{FE}=\mathrm{INT}\)（ML \(-F 1+\mathrm{HF}\) ）
190 FRIMT ITE：THE © B：TM＝T1： GDSUE 300
こロヘ FFINT THE（ 18）：TM＝Tこ：GDSUE 300

ここロ IF CL＝！THEN E6！
ESO NEXT
235 PEM 220．240，250 TD ELDSE TH BLE MEATLY
E40 IF INT MIT \(1010=M I T<1\) －1 THEN EGO
E50 CLDSE＝1：ITC＝MIT：GロTD 110
EEO ENI
EЭS REM EQNWERT SEC TD HR：MIN：S EI： HNI FRINT
\(300 T M=I N T \& T M+H F)\)
\(310 H=I M T<T M / H H: T M=T M-\) \(\mathrm{CH}+\mathrm{HH})\)
 \(M-M M\)

\section*{the electric pencil II \({ }^{\prime \prime}\)}


3

\(34010=M:\) GDSLIE 3 O！：FRINT＂：＂：
\(350 \mathrm{O}=\mathrm{S}\) 日DSUE 3E0
SEI RETIIFN
EO IF \(0<10\) THEN PRINT＂0＂；
3GO FFINT Q：FETISRN
G95 FEM IMPUT NECESSHF＇Y INFDFMA TIDN
1 亿ロロ INFUTT＂HIIE IIAMETER（INEH）？ ＂ HE ：HE \(=H E>E\) IHFIIT＂TAPE LENIETH LESS LEA IEF：\＆FEET）？＂：ML
1 OEO IHFUIT＂TIIFNE EDINT HT ENI D F TAFE？＂：MIT FFIIMT＂TIIENS EDIINT FEEHIING FDF：＂
103 FEM MTE UILL EE AI：TIHL TIFEME E：DUNT
1040 IHFLIT＂ 1001 RETIAFL THKE－IIF T IFHET＂：IF：IF：＝ 100 ，IF：MTE \(=M I T\) IF
1 O5 FFINT＂1．．．1516 IFE＂

1070 PFINT＂3．． \(3-3 / 4\) IPS＂
1 DEO PRINT＂4．．．アー1／こ IFS＂
\(10 \%\) FFINT＂5．．．15 IFS＂
110 OHFINT ＂E．． G OI IFS＂
1110 IHFUT＂WHIEH TAPE SFEEIT＂： SFI
1115 FEM KLUGE TD FINI SFEEII FFEDM THELE EMTF＇Y＇

\(11 \Xi\) FEM FINI MFY．TIME
\(1130 \mathrm{M} \mathrm{MT}=\mathrm{ML}\)－ \(1 \Xi \%\) Y OD
1135 FEM SET STAPT WHL FOF：TT HHI FIMI FIGTIAL WHLIE
\(1140 \mathrm{TT}=\) ． \(011:\) GDSLE 30
1145 FEM FEINT CDLIMM HEAIS
1150 PFINT＂TIPMS＂THE © \({ }^{5}\)＂ELAF EI＂THE \(1 G\)＂REMEIMIHE＂THE

1150 FRIHT＂EDIMT＂THE © B ＂TIME＂ THE（ 18 ＂TIME＂THE © 29 ＂USE I＇＂TAB \％ 3 ＂LEFT＂ PF：IMT
1170
1175 EEM GENEFATE TABLE
1180 GロSUE 100
EDIS FEM TT＝TAPE THICKNESS
こO15 FEM HS＝HIIE SIZE
こロごS FEM ML＝TDTAL THPE LENGTH
こロЗ5 FEM MIT＝MAX IMIIL．TLFMS
ED45 FEM MTE：MA× FETIIHL TIFNS
こOS5 FEM ITE＝INI．EIIFEENT T．E．
EOES FEM TS＝FET．ELIFPEMT T． C ．
EOFS FEM IP＝AE：T．\(\angle M I\) FMTID
EOS FEM F：H．L．FARI，HREF：LEMGTH
ごき5 FEM MソT＝TDTRL TIME
3015 FEM TM，IT．TEMP WHF FDR TIME EDHV
シロ15 FEM TI：TE．TIME ISEI：LEFT BOEG FEM F1．FE．FEET IISED．LEFT \(9 \% 95 E H I\)

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JRUN
HUB DIAMETER(INCH)? . 8525
TAPE LENGTH LESS LEADER(FEET)? 303.6
TURNS COUNT AT END OF TAPE? 641
TURNS COUNT READING FOR
100 ACTUAL TAKE-UP TURNS? 77.3
\begin{tabular}{|c|c|c|}
\hline & 15/16 & IPS \\
\hline 2 & 1-7/8 & IPS \\
\hline 3 & 3-3/4 & IPS \\
\hline 4 & 7-1/2 & IPS \\
\hline 5. & 15 & IPS \\
\hline 6 & 30 & IPS \\
\hline
\end{tabular}

WHICH TAPE SPEED? 2
CALCULATING TAPE THICKNESS AS WOUND...

Table 1: A tape counter/time table (produced by the program in listing 1) for a cassette recorder using C-60 tape. The last two columns were not printed by the program, but are a check value from an actual test of the program's accuracy.

TURNS ELAPSED
COUNT TIME
o

100
110
120
130
130
140
150
150
160
170
180
190
200


250
260
270
270
290
300

340
350
360
360
370
380
390
400
400
410
420
420
450
480
490
500
50
520
530
540
550
560

590
600
620
640
641

REMAINING
TIME
00:32:23
00:32:04
00:31:45
00:31:26 00:31:06
00:30:46 00:30:26 00:29:43 00:29:22 00:29:00 00:28:37 00:28:15
00:27:28
00:27:04 00:26:40
00:26:16
\(00: 25: 5\)
\(00: 25: 25\)
00:25:00
00:24:34 00:24:07 00:23:40
00:23:13 00:22:18 00:21:50
00:21:21 00:20:52
00:19:53
00:19:23
00:18:52
00:18:
00:17:19
00:16:47
00:16:14
00:15:42
00:15:09
00:14:0
00:13:27
00:12:53
\(00: 12: 18\)
\(00: 11: 42\)
00:11:0
00:09:54 00:09:18 00:08:40 \(00: 08: 03\)
\(00: 07: 25\) 00:06:47 00:06:08 00:05:29 00:04:50 00:04:10 00:03:30 00:02:50 00:02:09 00:01:28 00:00:04 00:00:00

FEET USED

0

FEET
LEFT


304
301
298
6
9
12

TIM TEST
\begin{tabular}{l}
\(1: 00\) \\
\(0: 19\) \\
\(0: 39\) \\
\(0: 58\) \\
\(1: 18\) \\
\(1: 38\) \\
\(1: 59\) \\
\(2: 20\) \\
\(2: 42\) \\
\(3: 04\) \\
\(3: 26\) \\
\(3: 48\) \\
\(4: 11\) \\
\(4: 34\) \\
\(4: 58\) \\
\(5: 22\) \\
\(5: 46\) \\
\(6: 11\) \\
\(6: 36\) \\
\(7: 01\) \\
\(7: 27\) \\
\(7: 53\) \\
\(8: 20\) \\
\(8: 47\) \\
\(9: 14\) \\
\(9: 41\) \\
\(10: 09\) \\
\(10: 37\) \\
\(11: 06\) \\
\(11: 35\) \\
\(12: 04\) \\
\(12: 34\) \\
\(13: 05\) \\
\(13: 34\) \\
\(14: 05\) \\
\(14: 37\) \\
\(15: 08\) \\
\(15: 40\) \\
\(16: 12\) \\
\(16: 45\) \\
\(17: 18\) \\
\(17: 51\) \\
\(18: 25\) \\
\(18: 59\) \\
\(19: 33\) \\
\(20: 08\) \\
\(20: 44\) \\
\(21: 19\) \\
\(21: 54\) \\
\(22: 31\) \\
\(23: 07\) \\
\(23: 44\) \\
\(24: 21\) \\
\(24: 59\) \\
\(25: 37\) \\
\(26: 16\) \\
\(26: 54\) \\
\(27: 33\) \\
\(28: 13\) \\
\(28: 53\) \\
\(29: 33\) \\
\(30: 13\) \\
\(30: 54\) \\
\(31: 35\) \\
\(32: 17\) \\
\(32: 28\) \\
\\
\hline
\end{tabular}

ERROR (SEC)


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\hline CPU & Z80A, 4MHz. & Same \\
\hline Dynamic RAM (std/max) & \(32 / 64 \mathrm{~Kb}\). & Same \\
\hline Disk drive type & Double density & Same \\
\hline No. of drives (std/max) & \(2 / 4\) & Same \\
\hline Capacity per drive (on-line) & \(\mathbf{2 0 0} \mathbf{~ K b . ~}\) & 180 Kb. \\
\hline Direct Memory Access (DMA) & Yes & No \\
\hline CP/M \({ }^{(6)}\) disk operating system & Standard & Optional \\
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\hline
\end{tabular}
\begin{tabular}{|l|c|c|}
\hline SPECIFICATIONS & QUAY 520 & HORIZON-2-32K-Q \\
\hline Disk drive type & Quad density & Same \\
\hline Capacity per drive (on-line) & \(\mathbf{4 0 0} \mathbf{K b}\). & 360 Kb. \\
\hline Unit Price & \(\$ 3,200\). & \(\$ 3,595\). \\
\hline
\end{tabular}

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\title{
Dissecting the TI Speak \& Spell
}

\author{
Michael A Rigsby \\ 5164 Sunburst Dr \\ Norcross GA 30092
}

There is now an economical way to provide limited voice output for computer-controlled devices. TI (Texas Instruments) provides most of the hardware in its familiar toy called the "Speak \& Spell."

Because I am fascinated by toys (my system is a hand-wired 1802 processor used in a self-contained, mazesolving mouse), it was only natural that I should procure my own birthday present-a toy-and immediately tear it apart.

Speak \& Spell is an educational aid designed for children aged seven or older. It contains a vocabulary of greater than 230 words in addition to the letters of the alphabet. Asking questions and playing games with electronic speech, it expects answers to be entered on its 40 -switch keyboard. Each entry evokes an audible response, and the machine even keeps score. Plug-in modules are available to expand the vocabulary. Suggested retail price for the toy is \(\$ 65\), though I bought mine for less than \(\$ 40\) at a major Atlanta department store.

Operation of the electronic portion of the Speak \& Spell involves many unknowns. I am sure that the manufacturer would probably prefer to keep these unknowns secret, but I can provide some insight into the operation of the Speak \& Spell.

The first great obstacle encountered when opening the machine is the back cover. Removing the two Phillips-head screws is a good step, but not good enough. There are still four slots, each containing a plastic hook over a plastic ledge. Take a thin-bladed screwdriver and push the hook toward the outside edge of the case, at the same time pull the front and back of the case apart
with substantial force. Continue until all four hook slots are free. Take care not to allow any backsliding. I have done this three times, each time expecting to destroy it, but everything is still intact.

After reaching the inside, there is not much to see except the back of a double-sided printed-circuit board. To turn the board over, the matrix switch cards (figure 1) must be released from the front of the case. This involves springing delicate plastic hooks. If one of these hooks should break, the toy is lost. Somehow I slipped the cards out and turned the main board over. (See photo 1 and figure 2 on page 82.) On the opposite side of the main board are a circuit board (with a little black round thing on it) on top of the main circuit board, an 8 -character alphanumeric display, and four integrated circuits, each with a distinctive proprietary number. The small circuit board appears to be a power supply.
The toy operates from a 6 V supply (four C cells), but \(+6 \mathrm{~V},-6 \mathrm{~V}\), and -20 V may be found throughout the board. The processor has five input lines from the switches; five lines seem to interconnect most of the circuits. The five input lines from the switches are activated upon contact closure by -20 V pulses generated within the processor.

At this point I will refer to figure 1. Eight bits from any processor may be used to control each of thirty-two lines by means of the 74154 binary-to-hexadecimal decoder. Each output line must go to a PNP transistor capable of switching a -20 V signal. The drawing in figure 1 indicates which wires go with which letters,

Text continued on page 84

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Figure 1a and 1b: During normal operation, the Speak \& Spell will voice a phoneme (letter sound) after a key is pressed on one of the keyboards. The Speak \& Spell can be controlled by a microprocessor interfaced to the keyboard lines as shown in figure \(1 c\).


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Photo 1: Detailed photograph of the disassembled Speak \& Spell. The main circuit board is shown in the same position as in figure 2; the board in the upper left-hand corner is the power supply. The black box at bottom center is one of the two keyboard assemblies.


Figure 2: Layout of the Speak \& Spell main circuit board, viewed from the front of the toy.

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Figure 3: Pin assignments for the SN76477N complex-sound generator. It is suspected that this well-known device is marked TMC0271NL in the Speak \& Spell. The pins marked with asterisks are in a logical low state unless they are pulled up by an external voltage.
\begin{tabular}{|c|c|c|c|c|}
\hline \multirow[b]{2}{*}{Behavior} & \multirow[t]{2}{*}{Pin \# of TMC0271NL} & \multicolumn{3}{|c|}{Connected To} \\
\hline & & Pin & Device \# & Informal Name \\
\hline steady pattern when letters are pronounced. variable pattern for all words & \[
\begin{aligned}
& 22 \\
& 25 \\
& 27 \\
& \hline
\end{aligned}
\] & \[
\begin{gathered}
4 \\
11 \\
14 \\
\hline
\end{gathered}
\] & \begin{tabular}{l}
TMC0351NL \\
TMCO271NL TMC0271NL
\end{tabular} & \begin{tabular}{l}
Unknown \# \\
Processor \\
Processor
\end{tabular} \\
\hline variable pattern for all speech & \[
\begin{aligned}
& 26 \\
& 28
\end{aligned}
\] & \[
\begin{gathered}
6 \\
36
\end{gathered}
\] & \begin{tabular}{l}
TMC0351NL \\
TMC0271NL
\end{tabular} & Unknown \#1 Processor \\
\hline
\end{tabular}

Table 1: Experimental behavior of selected logic lines coming from the TMC0271NL device on Speak \& Spell circuit board.

Text continued from page 76:
while figure 2 shows the location of these wires in the toy. Each line must be released before the processor will accept another input command.

Returning to the operation of the device, the 40 -pin circuit is undoubtedly a processor. There are two integrated circuits which I have labeled as high-density read-only memory (however, this is only a guess). They contain the information for the 230 spoken words; the processor (TMC0271NL) appears to contain the spoken letters and a few brief words. Of the forty pins on the processor, five are input lines from the switches, seven are pulsed output lines to the switches, fifteen or more are output
lines to the display, and three are output lines to the sound generator. Three of the lines that go to the display are part of the five lines that connect the processor to unknown circuit \#1 (mentioned above as possibly being a high-density readonly memory). If the unknown circuits are memory devices, the individual byte locations are not addressed by the processor (there is an insufficient number of interconnecting lines for that purpose), but are possibly left to be sequenced by a clock and stopped by processor control.

I am reasonably certain that the sound is generated by a complex sound generator, SN76477N. This
circuit is controlled by numerous resistor-capacitor combinations and seven digital-control lines. (See figure 3 and table 1.) If this device is the chip marked TMC0271NL in the Speak \& Spell, then it is two of the seven control lines (pins 1 and 9) that are tied to ground all of the time. Five of the lines have varying signals, though three of these maintain a constant pattern when letters are being pronounced. The narrowest spike in a pulse train that is connected to a control line is 0.1 ms long. With a 230 -word vocabulary, there is a controlled speech time of well over 100 seconds. Five lines multiplied by 100 seconds multiplied by 10,000 pulses per second yields \(5,000,000\) bits of information stored somewhere in the Speak \& Spell-providing one assumes that each word is composed of individually stored pulses. There are probably subroutines that cause the production of phonetic elements. I can see no way to access these phonetic elements, because they seem to be internal and not directly addressable by normal address lines. Someone with more memory than I have ( 1 K bytes of user memory) could monitor the control lines on the sound generator (see figure 3) and perhaps determine the phonetic makeup of individual sounds.

If you don't mind listening to your computer spell everything, give it a voice and let it speak.


\title{
Penny Pincher's Joystick Interface
}

\author{
Steven Wexler 1634 Buck Hill Dr Huntingdon Valley PA 19006
}

One of the more entertaining input devices that can be operated by a human hand is the joystick. Physically, the device consists of a lever that moves in two dimensions. The lever operates two potentiometers, which translate the position of the lever into two analog resistance values. A joystick hardware interface, in conjunction with the appropriate software, can convert the resistance values into corresponding binary integer values. These integers can be used to move a cursor, alter music, or control a robot, along with a myriad of other applications.
There are several ways to interface a joystick to your computer. Each scheme has its advantages and disadvantages. The particular method I have chosen has the advantages of being inexpensive, easy to build, easy to understand, and of requiring a minimum of input/output ( \(\mathrm{I} / \mathrm{O}\) ) programming.
The disadvantages? This method is slower than some other interfaces I have seen, uses more software than do the expensive hardware-intensive schemes, and is less precise than some of the more elaborate circuit concoctions.

\section*{Operating Theory}

The key to my "penny pincher's"
joystick interface is the 556 dual timer configured as two monostable multivibrators or one-shots, as shown in figure 1. In English, this means that if you trigger the one-shot, its output will go high for a predetermined interval, after which the output will return to its normal low state.
By using a joystick potentiometer as a timing resistor, the duration of one output pulse will be proportional to the position, in one dimension, of the joystick lever. Software is used to convert the pulse duration into a binary value. Duplicating the circuit for the second timer, the other joystick potentiometer will yield a different output-pulse duration and binary value for the other dimension. Remember, joysticks operate in two or more dimensions.

\section*{Joystick Interface Circuit}

Careful study of figure 1 will reveal a most curious aspect of the interface. The trigger and reset lines for each circuit are all tied to a common processor output line. This certainly saves output lines, but how can you trigger and reset simultaneously? An explanation of the trigger requirements for the timer circuits should help to clear up this anomaly.

Normally, the timer will start to output a pulse on the high-to-low
transition (ie: negative-going edge) of the input trigger signal. For the device to work properly, it is necessary to return the trigger input to its normal high state before the timed-output pulse returns low. In other words, before the device times out, the trigger input must go high.

If the timer receives a trigger signal in the middle of an output pulse, the signal is ignored. The obvious conclusion is that we must either trigger each of the 556 timers independently, or we must reset the second timer before it is triggered. Otherwise, how are we to avoid attempting to trigger the second timer before it has timed out from the initial signal? Tying the resets and triggers to a common computer-output line avoids the timing pitfall, while simplifying both hardware and software.
When the computer-output line goes low, the timing function is reset and the device returns to its initial state. As the processor-output line returns high (ie: positive-going edge), the circuit is reset before it is triggered; this allows the timing pulse to begin normally. The I/O line used to reset and trigger the 556 can also be used to reset and trigger additional joysticks. How's that for efficiency! I have not included the values of the timing capacitors and potentiometers

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Figure 1: The key to the penny pincher's joystick interface is the 556 dual timer, configured as two monostable multivibrators. The interval of each output pulse is determined by the joystick resistance, in conjunction with a user-selected timing capacitor.

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in figure 1; these values depend on software, processor speed, and personal preference.

\section*{Software}

The software needed for the penny pincher's interface is very straightforward. The 556 timers are triggered by setting the proper computeroutput line first low, then high. After this, the processor should enter a tight, time-efficient counting loop until one circuit times out. The software should immediately store the count and then start the process over for the next timer. It is recommended that you disable interrupts during the counting process; otherwise an inaccurate count may occur.

Listing 1 presents the joystickdriving software for my KIM-1 computer ( 6502 processor). The program assumes that the reset/trigger line is tied to the KIM-1 I/O line B1. The timer's outputs are tied to B2 and B3; a second joystick may be tied to lines B4 and B5.

Utilizing consecutive I/O lines in this manner allows for efficient I/O line polling by merely shifting an I/O mask. Figure 2 is a flowchart of the

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Figure 2: The joystick-driving software consists mainly of a counting loop; this determines the stick position by timing the output pulse interval. High resolution can be attained by using a fast counting loop.

Listing 1: The software used on the author's KIM-1 system resets the interface timers with a low logic state on I/O line B1. When the same line goes high, the timers are retriggered. This technique, using only one output line, contributes to the simplicity of the hardware.
\begin{tabular}{|c|c|c|c|c|}
\hline \multicolumn{2}{|l|}{\multirow[t]{6}{*}{}} & \multicolumn{2}{|l|}{\multirow[t]{6}{*}{\[
\begin{aligned}
& \text { POT }=\$ 17 \mathrm{E} 3 \\
& \mathrm{POT}+1=\$ 17 \mathrm{E} 4 \\
& \mathrm{POT}+2=\$ 17 \mathrm{E} 5 \\
& \mathrm{POT}+3=\$ 17 \mathrm{E} 6 \\
& \mathrm{PBD} 2=\$ 17 \mathrm{O} 2 \\
& \mathrm{PBDD} 2=\$ 17 \mathrm{O} 3
\end{aligned}
\]}} & \multirow[t]{6}{*}{\begin{tabular}{l}
POT 1, Y AXIS \\
POT 1, X AXIS \\
POT 2, Y AXIS \\
POT 2, X AXIS \\
PORT B DATA REGISTER \\
PORT B DIRECTION REGISTER
\end{tabular}} \\
\hline & & & & \\
\hline & & & & \\
\hline & & & & \\
\hline & & & & \\
\hline & & & & \\
\hline 8510 & A2 01 & JOY & LDX \#1 & ENTRY FOR ONE JOYSTICK. \\
\hline 8512 & D0 01 & & BNE HOP & FORCED JUMP. \\
\hline 8514 & A2 03 & JOY2 & LDX \#3 & ENTRY FOR TWO JOYSTICKS. \\
\hline 8516 & A9 02 & HOP & LDA \#2 & INITIALIZE TIMER POINTER. \\
\hline 8518 & 8D 0317 & & STA PBDD2 & SET LINE Bl FOR OUTPUT, REST INPUT. \\
\hline 851B & 08 & & PHP & SAVE INTERRUPT STATUS. \\
\hline 851 C & 78 & & SEI & DISABLE INTERRUPT. \\
\hline 851 D & OA & LP & ASL & UPDATE TIMER POINTER. \\
\hline 851 E & A0 00 & & LDY \#0 & TRIGGER TIMER VIA \\
\hline 8520 & 8C 0217 & & STY PBD2 & LOW TO \\
\hline 8523 & A0 02 & & LDY \#2 & HIGH TRANSITION \\
\hline 8525 & 8 C 0217 & & STY PBD2 & OF LINE Bl. \\
\hline 8528 & A0 FF & & LDY \#FF & INITIALIZE COUNTER. \\
\hline 852A & CA & LP1 & INY & UPDATE COUNT. \\
\hline 852B & 2C 0217 & & BIT PBD2 & TEST TIMING PULSE. \\
\hline 852E & D0 FA & & BNE LP1 & IF HIGH, CONTINUE COUNT. \\
\hline 8530 & 48 & & PHA & \\
\hline 8531 & 98 & & TYA & \\
\hline 8532 & 9D E3 17 & & STA POT, X & SAVE COUNT. \\
\hline 8535 & 68 & & PLA & \\
\hline 8536 & CA & & DEX & \\
\hline 8537 & 10 E 4 & & BPL LP & MORE TIMERS? \\
\hline 8539 & 28 & & PLP & NO, RESTORE INTERRUPT STATUS. \\
\hline 853A & 60 & & RTS & \\
\hline
\end{tabular}
program. Remember to keep the counting loop as efficient as possible.

\section*{Calibration}

The count we obtain from the interface is equivalent to the duration of the timing pulse divided by the processing time required by the computer to execute one counting loop. My 6502 system, running at a clock frequency of 1 MHz , will execute the counting loop in listing 1 (hexadecimal 852A thru 852E) in \(9 \mu \mathrm{~s}\). It stands to reason that if you want a joystick to read from 0 to 100 on this machine, you would choose a potentiometer and capacitor that would set the maximum duration of the timing pulse to \(909 \mu \mathrm{~s}(101 \times 9 \mu \mathrm{~s})\).

The following formula is used to derive the value of the timing capacitor:
\[
C=\frac{\text { pulse duration }}{1.1 \times R}
\]
where \(C\) is in farads, duration is in seconds, and \(R\) is in ohms. Assuming
a joystick with 100 k -ohm potentiometers, a \(0.0083 \mu \mathrm{~F}\) capacitor is needed to produce a \(909 \mu \mathrm{~s}\) timing pulse. Since the actual value of most capacitors is not precisely known, it may be desirable to trim the maximum timer intervals. This can be done by placing extremely smallvalue capacitors in parallel with the main timing capacitor of the circuit that has the smaller maximum pulse of the two. Silver mica capacitors should work well here.

\section*{Construction}

The circuit is quite simple and compact. With point-to-point wiring, several joystick interfaces can be constructed on a small circuit card. Placement of components is not critical. Each interface should draw less than 40 mA from a +5 V supply. Surplus joysticks can be purchased for about \(\$ 4\), while the 556 timer costs less than \(\$ 1\); so, for about \(\$ 6\) and one night's work, you can add this joystick interface to your system.

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\hline FUNCTION & \begin{tabular}{l}
APPLE'S \\
DOS 3.3 \\
RANDOM \\
ACCESS
\end{tabular} & KRAM & FEATURES \\
\hline Retrieve by Social Sec. \# & NO & YES & \begin{tabular}{l}
Relative record is limited to 7 digit \#'s; \\
KRAM keys up to 48 bytes!
\end{tabular} \\
\hline Retrieve by Last Name & NO & YES & Relative record cannot file alphabetically \\
\hline Erase a record & NO & YES & Relative record cannot erase records \\
\hline Dynamic record allocation & NO & YES & KRAM files grow as needed \\
\hline Dynamic compression & NO & YES & KRAM recaptures space when records are deleted \\
\hline Mutliple files open & NO & YES & KRAM can keep 5 files open simultaneously \\
\hline BEST WAY & NO & YES & It's obvious \\
\hline
\end{tabular}

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- Partial key in .4 sec.
- (. 2 sec. with Corvus Disk)
- Supports multiple disks
- Read next or previous record
- Dynamic space allocation
- Dynamic space reclamation
- Dynamic index compression
- Never needs reorganization

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\title{
Pascal and the Great Race
}

\author{
David A Mundie, 104 Oakhurst Cir, Charlottesville VA 22903
}

I have some comments on the record maintenance techniques described in 'The Great Race and Micro Disk Files," by J J Roehrig (April 1980 BYTE, page 142).
Mr Roehrig's initial method took almost a minute just to write 120 real variables, so it is little wonder that he began looking for a better way. His decision to minimize disk transfers by not sorting the records on the disk seems eminently sensible. However, his other decision, to read and write individual elements of the array instead of using a FOR...NEXT loop is lamentable. Surely there is something wrong with a language so inefficient that loops are prohibitively slow. One wonders what he would have done had there been 1000 elements in the array rather than twelve.

Mr Roehrig might consider changing programming languages as a solution to his problem. The root of his difficulty is that BASIC does not allow for files of arrays (or any other structured data type, for that matter). In Pascal, it would be possible to define SCRATCH as a file

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of arrays of reals, with twelve reals in each array. Writing an array is then accomplished by the simple statement PUT(SCRATCH), while reading is done by GET(SCRATCH)-no loops, and especially no referencing of each element of the array.

\section*{Listing 1}
```

PROGRAM RACETEST;
CONST DUMMYVALUE = 1.23456;
TYPE REALARRAY = ARRAY[1..12] OF REAL;
VAR I,J: INTEGER;
DUMMY: REALARRAY;
SCRATCH: FILE OF REALARRAY;
PROCEDURE CLOCK;
BEGIN
WRITELN ('CLOCK: `);
READLN
END;
BEGIN (*RACETEST-MAIN PROGRAM*)
FOR I:= 1 TO 12 DO
DUMMY[I] : = DUMMYVALUE;
CLOCK;
REWRITE (SCRATCH, 'SCRATCH');
FOR I := 1 TO 10 DO
BEGIN
SCRATCHI := DUMMY;
PUT (SCRATCH)
END;
CLOCK;
FOR J : = 1 TO 5 DO
BEGIN
RESET (SCRATCH);
FOR I := 1 TO 10 DO
BEGIN
DUMMY := SCRATCH!;
GET (SCRATCH)
END;
END;
CLOCK;
CLOSE (SCRATCH)
END.

```

A Pascal program equivalent to his program is given in listing 1. Because ten arrays of twelve reals do not fill up the minimum UCSD Pascal buffer of 512 bytes, for benchmarking purposes I actually used an array size of 120 real variables, then divided the execution times by 10. This yields a time of about 0.4 seconds to write ten records, compared to Mr Roehrig's minimum of 3 seconds, or the estimated 20 seconds using loops. Reading ten records five times took about 1 second, compared to his minimum of 6 seconds. Part of the difference may be attributable to hardware (I used a Pascal Microengine with double-density 8 -inch disks), but I am convinced that the difference is largely due to Pascal's more rational handling of files. In this case, at least, higher-level constructs seem to be not only easier to use, but also more efficient than those at a low level.

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The article "A Power-Line Protection Circuit" by Neil Schneider and Bror Erickson (March 1980 BYTE, page 126) generated a great deal of correspondence. This included the following criticism by Mr Newswanger and the circuit offered by Mr Schafer.

\section*{Protection Circuits}

Donald W Newswanger, Dept of Building and Safety, City Hall, Rm 485, Los Angeles CA 90012

I was disappointed to see the article "A Power-Line Protection Circuit" (March 1980 BYTE, page 126). No direct internal connection should ever be made to a hotchassis transformerless television set. The antenna terminals may be safely used with a suitable RF (radiofrequency) modulator, but no attempt should be made to connect directly into the video circuit. Transformerisolated television sets and monitors are readily available for this purpose.

The circuits in both figure 1 and figure 2 of that article introduce problems into the building wiring system. The use of either circuit will trip a ground-fault circuit breaker. Circuit 2 is particularly bad since it directly interconnects the ground wire and the neutral during normal operation. The neutral conductor of a two-wire cir-

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cuit carries the same current as the hot wire of the circuit. The interconnection of the neutral and ground wire will cause part of the normal neutral current from all applicances connected to the circuit to flow through the ground wire. The ground wire is intended to provide a ground path for appliances and should never be used as a current-carrying conductor. These circuits violate the provisions of the National Electrical Code and the UL/ANSI Standards.

I have a low-cost personal computer and feel that my 120 VAC/12 VDC portable television set was a good investment. BYTE should encourage the use of line-isolated television sets and monitors and discourage the use of makeshift substitutes.

Steven A Schafer, 202 West Dr, Princeton NJ 08540
The purpose of the ground wire in the standard power delivery system is to provide a stable reference and to bleed away any small charges caused by leakage currents or static. It should never be used to supply power to any device. A current of more than a few milliamperes in the ground line is enough to trigger a ground-fault interrupter, if such a device is installed.

For the same reason, the neutral wire should never be connected to the ground wire; even though they are supposedly at the same potential, the neutral wire is not guaranteed to be at earth-ground, and connecting it to the ground wire will often cause a small current to flow. For obvious safety reasons, neither the hot nor the neutral side of the power line should be connected to any exposed conductor.

The circuit shown in figure 1 is a nearly foolproof way to protect against wiring errors. If a polarity error exists between the protected equipment and any other devices connected to it, relay 2 and the neon indicator will turn on, disabling relay 1 and preventing power from being applied to the protected equipment. If there is no error, relay 2 remains off, and depressing the push-button switch


Figure 1: Steven Schafer's power-line protection circuit. The line marked \(H\) is the hot side of the power line; the line marked \(N\) is the neutral side of the power line. The resistor in series with the neon lamp should have a value of 100 k ohms.


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will latch relay 1 on and apply power to the load. The only way to defeat the circuit is to hold the push-button switch closed while inserting the power plug in the wall socket.

\title{
Making 6502 Indirect Subroutine Calls Efficient
}

\author{
Philip K Hooper, 5 Elm St, Northfield VT 05663
}

I enjoyed the article "Indirect Addressing for the 6502," by Kenneth Skier (January 1980 BYTE, page 118), and I would like to suggest some alternative techniques. These are based on the observation that once the subroutine of interest has finished executing, control may return directly to the original calling program rather than to the interim location holding the volatile address of the subroutine. Implementing this permits savings in both time and storage, as will be shown.
Approach A involves initially writing hexadecimal 4C (the JMP op code) into the first of three read/write memory locations, the second and third of which will be set dynamically to the actual address of the desired subroutine, as in Mr Skier's article. The subroutine will then be summoned correctly by a simple JSR to the read/write memory location containing the 4C. Return will be to the main program.

Approach B requires no initialization of read/write memory, although two consecutive bytes of read/write memory must be reserved for use as a pointer. The main program does require three additional bytes containing hexadecimal 6C (op code for JMP indirect) followed by the address, low byte first, of the read/write memory location reserved for the pointer. In use, the pointer will be loaded (as before) with the actual subroutine address, and a JSR to the byte containing the 6 C will result in the correct location, execution, and return from the desired subroutine.

Table 1.
\begin{tabular}{|c|c|c|c|}
\hline Time overhead in \(\mu \mathrm{s}\) & \begin{tabular}{l}
Approach used in article 24 \\
(JSR JSR RTS RTS)
\end{tabular} & \[
\begin{gathered}
\text { Approach A } \\
15 \\
\text { (JSR JMP RTS) }
\end{gathered}
\] & \[
\begin{gathered}
\text { Approach B } \\
17 \\
\text { (JSR JMPI RTS) }
\end{gathered}
\] \\
\hline Bytes needed to do initialization & 8 or 10 & 4 or 5 & 0 \\
\hline Additional bytes of program memory & 0 & 0 & 3 \\
\hline Bytes of read/write memory required & 4 & 3 & 2 \\
\hline Bytes required by stack & 4 & 2 & 2 \\
\hline
\end{tabular}

Table 1 summarizes the storage and time overhead requirements of these three JSR (I) techniques. For sheer speed, approach A performs best, while approach B can save two or three bytes, at a cost of two cycles per invocation.


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Heads Above The Rest in Disk Technology

\section*{Michael Fallgatter, 514 Bethesda Ct, Waukesha WI 53186}

The article "Indirect Addressing for the 6502," by Kenneth Skier (referenced above), was most interesting, but I would like to point out that, in the case of indirect transfers to subroutines, a much faster-running linkage is possible. Rather than using the linkage routine:

JSR variable address RTS
the linkage using the 6502 indirect-jump command

\section*{JMP variable pointer}
produces the same result, takes less memory, and cuts the time required for the transfer of control by over \(50 \%\), from thirty-eight to eighteen machine cycles. Using this technique and assuming a table of subroutine addresses residing in a single page of memory, the listings in Mr Skier's article become those shown here.

Listing 1: Initiate zero-page bytes

LDA \#\$6C
STA zero-page byte \#1
Write IMP indirect
LDA \#Stable page
STA zero-page byte \#3

Listing 2: Transfer from main program
LDX subroutine *-pointer to address in table
STX zero-page byte \#2
JSR zero-page byte \#1

Listing 3: Zero-page linkage routine to create subroutine call
STX zero-page byte \#2
JMP indirect, via subroutine address table

Listing 4: Simulate indirect subroutine jump
LDX subroutine
JSR CALL SUBROUTINE (X)

Finally, since no indexed instructions are involved, the A register could be used instead of X. Also, there is a very minimal memory and execution-time penalty paid for using a nonzero page for the transfer routine.

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\title{
Machine Problem Solving, Part 1: \\ Trial-and-Error Search, A Mechanical Plan to Save the Missionaries
}

\author{
Professor Peter W Frey \\ Northwestern University \\ Cresap Neuroscience Laboratory \\ 2021 Sheridan Rd \\ Evanston IL 60201
}

Modern computers are famous for their numbercrunching ability. Their facility at inverting a 60 by 60 matrix or at solving a set of linear differential equations is truly impressive. In fact, machines are so good at solving numerical problems that most of us take these skills for granted.

Computers are also useful as general-purpose control devices. Many personal-computing enthusiasts enjoy impressing their neighbors with their machine's ability to control lights, water sprinklers, and burglar alarms, and to take telephone calls and regulate the furnace. Homes of the future will be completely computerized.

The computer also makes an excellent bookkeeper: faithfully recording financial transactions, maintaining mailing lists, and generating timely reminders for important meetings. Personal computers also provide many hours of entertainment for their owners with games of manual dexterity, games of chance, and simulated battles among the stars or in dark dungeons. These many uses provide a clear rationale for the rapidly developing popularity of the personal computer.

The most exciting application of the computer lies in still another direction. It is as a thinking machine that the modern computer truly sparks our imagination. When faced with a problem that has no easy numerical solution, men have typically discarded their mechanical calculators and put on their proverbial thinking caps. For this type of problem, the human brain has always been superior to mechanical devices. An immense amount of respect for the human brain can be gained by trying to program a computer to select the best move in a game like chess. Even a multimillion-dollar mainframe computer turns out to be a woodpusher when asked to compete against a skilled human player.

\section*{Solutions by Searching}

When machines confront nonnumerical problems, their primary weapon in finding a solution is to examine
a vast labyrinth of potential outcomes in search of one which satisfies the desired conditions. Although this approach is not very elegant, it is, in fact, highly similar to that used by humans. The noted psychologist Donald Campbell (see reference 1) observed that trial-and-error search plays a key role in human problem solving: "a blind-variation-and-selective-survival process is fundamental to all inductive achievements, to all genuine increases in knowledge, to all increases in fit of system to environment."

\section*{It is as a thinking machine that the modern computer truly sparks our imagination.}

Campbell also concluded that specialized problemsolving skills such as those observed in an experienced surgeon or airline pilot are "inductive achievements achieved originally by a blind-variation-and-selectivesurvival process." Thus, trial-and-error search provides the cornerstone for human efforts in acquiring new knowledge.

Search is even more important in solving problems by computer. With most problems, humans have background information which can be successfully employed to direct the solution process. Machines generally lack this. Problem solving by computer usually requires that all relevant facts be discovered during the solution process. This important difference between human and machine problem solvers has been addressed by recent efforts in artificial intelligence. By developing specialized information libraries, the computer scientist has created search programs which are reasonably competent at tasks such as diagnosing medical problems or developing threedimensional models for complex chemical structures. For


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most problem-solving efforts, however, it is much easier to emphasize search rather than sophisticated pattern matching.

\section*{Games as Problems}

Games and puzzles provide excellent sample problems. Marvin Minsky states that "it is not that the games and mathematical problems are chosen because they are clear and simple; rather it is that they give us, for the simplest initial structures, the greatest complexity, so that one can engage some really formidable situations after a relatively minimal diversion into programming." (See reference 2.) Man's fascination with intellectual games is not a new phenomenon. The Dutch scholar Huizinga suggested many years ago that the human race should have been named homo ludens (the game player) rather than homo sapiens.

There are two important aspects of playing a game or solving a puzzle. The first consists of representing the problem in a way that permits efficient analysis. The second involves devising a search technique which is capable of finding a solution. The first task, finding a good way to represent the problem, is usually the key to an elegant solution. Unfortunately, few guidelines exist that provide a mechanical rule for developing a good representation. For this reason, problem representation generally must be devised individually for each game or puzzle by the human programmer.

The situation is quite different in respect to the search process. In this case, there are well-developed principles that have proven useful in many different problem areas. My purpose in this article will be to focus on the search

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\section*{The most basic type of search is called the trial-and-error search.}
process and to consider general techniques that have broad applicability.

\section*{Trial-and-Error Search}

The most basic type of search process is called trial-and-error search. In this case, the problem solver examines various operations until a sequence is found that leads to a solution. In primitive implementations, the different options are considered haphazardly rather than being ordered according to a specific plan. To demonstrate this approach, we will develop a solution for the missionaries-and-cannibals problem.
In its traditional form, this problem involves three missionaries and three cannibals who are located on one bank of a river and wish to cross. A boat is available which will hold two people and which can be navigated by one or two people. The special restriction that makes the problem interesting is that the sequence of river crossings must never result in an arrangement where the cannibals outnumber the missionaries on either bank. If the missionaries are outnumbered, their life expectancy will be immediately and permanently shortened.
In determining the number of individuals on each bank, the persons in the boat when it reaches shore are considered to be residents of that bank. The object for the problem solver is to develop a schedule of river crossings which transports the entire party across without losing any missionaries.

\section*{Representing the Problem}

The first step in addressing this problem is to find a representation that is compatible with a machine problem-solving approach. For our effort, we would like to write a program in Level II BASIC for the Radio Shack TRS-80 computer. This machine is widely available and has more than enough power to solve this puzzle. We will consider the problem in terms of discrete states and discrete operations. We will not concern ourselves with the details of paddling a boat across a river, but rather with the executive decisions, ie: who is to be in the boat on each journey across.
The state space will consist of a description of the number and types of occupants on each bank before the boat makes a crossing or after a crossing is completed. We will employ a shorthand notation which represents a missionary by the letter M , a cannibal by the letter C , the boat by the symbol \(<=>\), and the river by two vertical lines. Therefore, the character sequence CCMM \(|<=>| C M\) indicates that there are two cannibals and two missionaries on the left bank of the river and one cannibal, one missionary, and the boat on the right bank. This notation is adequate to describe all possible states of the problem.
The operations (ie: legal moves) we can perform to transpose one state into another are quite limited in number. In fact, there are a maximum of five operations that can be used, and often only a subset of these will be feasible. The five operations consist of transporting (1) one

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cannibal, (2) two cannibals, (3) one missionary, (4) two missionaries, or (5) one cannibal and one missionary.

To execute one of these operations in a particular direction, the boat must be located on the departure bank. In addition, an operator cannot be applied if the appropriate individuals are not present on the departure bank. For example, we cannot move two missionaries from the left bank to the right bank if there are fewer than two missionaries on the left bank at that point in time.

\section*{Programming the Problem}

Our program will start with a few "housekeeping" functions that are necessary even though they have little to do with the logic of our solution. It is necessary to set aside 300 bytes of memory for string variables, to inform the machine that all variables that are not specifically defined as string variables are to be treated as integer variables (this saves memory and speeds execution), to define two special variables ( \(\mathrm{X} \$\) and \(\mathrm{Y} \$\) ) for clearing sections of the video display, and to blank out the entire screen.

In addition, for our graphic presentations we need a representation for the boat on the left side of the river (BLS) and one for the boat on the right side of the river (BRS). All of this is accomplished in our first two lines (given here and as part of listing 1; the function STRING\$ ( \(n\), " \(X\) ") returns a string consisting of \(n\) symbols using the first character of " \(X\) " ):
```

100 CLEAR 300: DEFINT A-Z:
Y$=STRING$(40," "): CLS
110 X$=STRING$(9," "):
BL$="<=>"+X$:BRS=X\$+"<=>"

```

It is also helpful to set up a few arrays to store essential information. We need to know the position of the boat, the number of cannibals on the left bank, and the number of missionaries on the left bank after each river crossing. This information will be retained in arrays B, C, and M. We also need to remember which of the crossing options ( 1 cannibal, 2 cannibals, 1 missionary, etc) we have considered at each choice point in our crossing sequence. This information is stored numerically by array D and for graphic purposes in string array MV\$. Finally, we need to specify the crossing options with respect to the cannibals, array CT, and the missionaries, array MT. The TRS-80 is instructed to establish these arrays in line 120:

120 DIM B(30), C(30), CT(5), D(30),
M(30), MT(5), MV\$(30)
To make the program more interesting, we will generalize the problem so that the number of travelers can vary from four to sixteen. The number of travelers will be represented by the variable N which can be specified by the user:

\section*{130 PRINT@526, "NUMBER OF TRAVELERS (4 TO 16)";: INPUT N \\ 140 CLS: IF N < 4 OR N \(>16\) THEN 130}

Line 140 makes sure that the value entered for N is in the proper range. This is important with the TRS-80 because
keyboard bounce is apt to provide a value like 122 when we intended 12. The program would experience difficulties if it attempted execution with N set at a value of 122 .

Next, we set the stage properly. First we need a title (line 150) and then we need a river for our travelers to cross (line 160):
```

150 PRINT@24, "MISSIONARIES AND
CANNIBALS";
160 FOR K=4 TO 43: SET (58,K): SET (85,K):
NEXT K

```

\section*{Program Operation}

Now it is time to get on with the main act. The initial number of cannibals on the left bank (CI) is computed as

Listing 1: Trial-and-error solution to the cannibals-andmissionaries problem, written for the TRS-80 in Level II BASIC.

\footnotetext{
100 CLEAR 300: DEFINT A-Z: Y \(\$=\) STRING \(\$(40, "\) " \()\) : CLS
\(110 \mathrm{X} \$=\) STRING \(\$(9, \cdots "):\) BL \(\$="<=>"+\mathrm{X} \$: B R \$=X \$+"<=>"\)
120 DIM B(30), C(30), CT(5), D(30), M(30), MT(5), MV\$(30)
130 PRINT@526, "NUMBER OF TRAVELERS (4 TO 16)";: INPUT N
140 CLS: IF \(\mathrm{N}<4\) OR \(\mathrm{N}>16\) THEN 130
150 PRINT@24, "MISSIONARIES AND CANNIBALS";
160 FOR K=4 TO 43: SET(58,K): \(\operatorname{SET}(85, K)\) : NEXT K
\(200 \mathrm{CI}=\mathrm{INT}(\mathrm{N} / 2): \mathrm{MI}=\mathrm{N}-\mathrm{CI}: \mathrm{BP}=\mathrm{I}: \mathrm{I}=0\)
\(210 \mathrm{CL}=\mathrm{CI}: C R=0: M L=M I: M R=0\)
\(220 \mathrm{CT}(1)=2: \mathrm{CT}(2)=1: \mathrm{CT}(3)=0: \mathrm{CT}(4)=0: \mathrm{CT}(5)=1\)
\(230 \mathrm{MT}(1)=0: \mathrm{MT}(2)=0: \mathrm{MT}(3)=2: \mathrm{MT}(4)=1: \mathrm{MT}(5)=1\)
300 GOSUB 2000: GOSUB 1000
\(310 \mathrm{C}(\mathrm{I})=\mathrm{CL}: \mathrm{M}(\mathrm{I})=\mathrm{ML}: \mathrm{B}(\mathrm{I})=\mathrm{BP}\)
320 IF ML \(=0\) AND CL \(=0\) THEN 700
330 FOR K \(=1\) TO 800: NEXT K
\(340 \mathrm{I}=\mathrm{I}+\mathrm{I}: \mathrm{D}(\mathrm{I})=0\)
\(350 \mathrm{D}(\mathrm{I})=\mathrm{D}(\mathrm{I})+1: \mathrm{IF} \mathrm{D}(\mathrm{I})>5\) THEN 600
\(360 \mathrm{IF} \mathrm{BP}=-1\) THEN 380
\(370 \mathrm{IF} \mathrm{CL}<\mathrm{CT}(\mathrm{D}(\mathrm{I})\) ) OR ML<MT(D(I)) THEN 350 ELSE 390
\(380 \mathrm{IF} \mathrm{CR}<\mathrm{CT}(\mathrm{D}(\mathrm{I})\) ) OR \(\mathrm{MR}<\mathrm{MT}(\mathrm{D}(\mathrm{I})\) ) THEN 350
\(390 \mathrm{CL}=\mathrm{CL}-\mathrm{BP}{ }^{*} \mathrm{CT}(\mathrm{D}(\mathrm{I})\) ): \(\mathrm{CR}=\mathrm{Cl}-\mathrm{CL}\)
\(400 \mathrm{ML}=\mathrm{ML}-\mathrm{BP}{ }^{*} \mathrm{MT}(\mathrm{D}(\mathrm{I})): \mathrm{MR}=\mathrm{MI}-\mathrm{ML}: \mathrm{BP}=-\mathrm{BP}\)
410 IF ML>0 AND CL \(>\) ML THEN 500
420 IF MR > 0 AND CR > MR THEN 500 ELSE \(\mathrm{K}=0\)
\(430 \mathrm{IF} \mathrm{CL}=\mathrm{C}(\mathrm{K})\) AND \(\mathrm{ML}=\mathrm{M}(\mathrm{K})\) AND \(\mathrm{BP}=\mathrm{B}(\mathrm{K})\) THEN 500
\(440 \mathrm{~K}=\mathrm{K}+1\) : IF \(\mathrm{K}<\) I THEN 430
\(450 \mathrm{~A} \$=\) STRING \(\$\left(C T\left(\mathrm{D}(\mathrm{I})\right.\right.\) ). \(\left.{ }^{\prime} \mathrm{C}=1\right): \mathrm{B} \$=\) STRING \(\$(\mathrm{MT}(\mathrm{D}(\mathrm{I})\) ), "M")
\(460 \mathrm{IF} \mathrm{BP}=-1\) THEN MV \(\$(\mathrm{I})=\mathrm{A} \$+\mathrm{B} \$+"->"\)
ELSE MV\$(I) \(=\) " \(<-"+A \$+B \$\)
470 GOTO 300
\(500 \mathrm{BP}=-\mathrm{BP}: \mathrm{CL}=\mathrm{CL}+\mathrm{BP} * \mathrm{CT}(\mathrm{D}(\mathrm{I})): \mathrm{CR}=\mathrm{CI}-\mathrm{CL}\)
\(510 \mathrm{ML}=\mathrm{ML}+\mathrm{BP}^{*} \mathrm{MT}(\mathrm{D}(\mathrm{I})): \mathrm{MR}=\mathrm{MI}-\mathrm{ML}\) : GOTO 350
600 PRINT@960, "BACK UP AND TRY SOMETHING ELSE";
\(610 \mathrm{I}=\mathrm{I}-\mathrm{l}\) : IF \(\mathrm{I}<1\) THEN PRINT@ 960 , \(\mathrm{Y} \$\) : : GOTO 800
\(620 \mathrm{CL}=\mathrm{C}(\mathrm{I}-1): \mathrm{CR}=\mathrm{CI}-\mathrm{CL}: \mathrm{ML}=\mathrm{M}(\mathrm{I}-1): \mathrm{MR}=\mathrm{MI}-\mathrm{ML}\)
\(630 \mathrm{BP}=\mathrm{B}(\mathrm{I}-1)\) : GOSUB 2000: GOSUB 1000
640 FOR K \(=1\) TO 800: NEXT K
650 PRINT © 960, Y\$;: GOTO 350
700 PRINT@ 960, "SUCCESS";: GOTO 700
800 PRINT © 64, X\$;: PRINT@ 960, "FAILURE";: GOTO 800
1000 IF I = 0 THEN RETURN
1010 FOR K = 1 TO 14: PRINT © K \({ }^{(64, ~ X \$ ;: ~ N E X T ~ K ~}\)
\(1020 S=I-13\) : IF \(S<1\) THEN \(S=1\)
1030 FOR K = S TO I: J = K \(-\mathrm{S}+1\)
\(2000 \mathrm{Z} \$=\) STRING \(\$(8-C R, " "): C R \$=S T R I N G \$\left(C R,{ }^{\prime \prime} C^{\prime \prime}\right)+Z \$\)
2010 Z \(\$=\) STRING \(\$(8-C L, " "): C L \$=\) Z \(\$\) + STRING \(\$(C L, " C ")\)
\(2020 \mathrm{Z} \$=\) STRING \(\$(8\) - MR," " \():\) MR \(\$=\) STRING \(\$(M R, " M ")+Z \$\)
\(2030 \mathrm{Z} \$=\) STRING \(\$(8-M L, " "): M L \$=Z \$+S T R I N G \$(M L, " M ")\)
2040 IF BP \(=1\) THEN B \(\$=\) BL \(\$\) ELSE \(B \$=\) BR \(\$\)
2050 PRINT@ 468, CL\$;: PRINT@ 492, CR\$;: PRINT@ 478, B\$; 2060 PRINT@ 532, ML\$;: PRINT@ 556, MR\$;: RETURN
}

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is the initial number of missionaries on the left bank (MI). We will assume an equal number of missionaries and cannibals when N is even and an extra missionary when N is odd. (If there were an extra cannibal at the beginning, our problem would end before we had a chance to try our first crossing.)

The position of the boat will be indicated by the variable BP. When the boat is on the left bank, BP will have a value of 1 . A value of -1 will indicate that the boat is on the right bank. The index reflecting the number of crossings (I) is set to zero and the values for the variables indicating the number of cannibals on the left bank (CL), the number of cannibals on the right bank (CR), the number of missionaries on the left bank (ML), and the number of missionaries on the right bank (MR) are also initialized:
\[
\begin{aligned}
& 200 \mathrm{Cl}=\mathrm{INT}(\mathrm{~N} / 2): \mathrm{MI}=\mathrm{N}-\mathrm{CI}: \mathrm{BP}=1: \mathrm{I}=0 \\
& 210 \mathrm{CL}=\mathrm{CI}: \mathrm{CR}=0: \mathrm{ML}=\mathrm{MI}: \mathrm{MR}=0
\end{aligned}
\]

We also wish to specify each crossing option by specifying the number of cannibals (CT) and the number of missionaries (MT) who are transported:
\[
\begin{aligned}
220 \mathrm{CT}(1) & =2: \mathrm{CT}(2)=1: \mathrm{CT}(3)=0: \mathrm{CT}(4)=0: \\
\mathrm{CT}(5) & =1 \\
230 \mathrm{MT}(1) & =0: \mathrm{MT}(2)=0: \mathrm{MT}(3)=2: \mathrm{MT}(4)=1: \\
\mathrm{MT}(5) & =1
\end{aligned}
\]

The main loop of our program begins with calls to two subroutines which handle the graphic display. One subroutine (which appears later in this article at line


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1000) displays an up-to-date list of the crossings attempted so far. The other subroutine (line 2000) provides a pictorial representation of the current position of the missionaries, cannibals, and boat. These routines are not essential for solving the problem, but they add a nice touch to the program and allow the user to watch the machine's "thought processes." These subroutines are invoked at line 300 :

\section*{300 GOSUB 2000: GOSUB 1000}

Each time through the loop, it is necessary to make a permanent record of the current status of our principal characters:
\[
310 \mathrm{C}(\mathrm{I})=\mathrm{CL}: \mathrm{M}(\mathrm{I})=\mathrm{ML}: \mathrm{B}(\mathrm{I})=\mathrm{BP}
\]
and then to check to see if the problem has been solved:
\[
320 \text { IF } \mathrm{ML}=0 \text { AND } \mathrm{CL}=0 \text { THEN } 700
\]

If not, we create a brief delay so that the human observer will not miss any of the action:

\section*{330 FOR \(K=1\) TO 800: NEXT K}
and then get about our main business, examining the feasibility of making a particular crossing by incrementing I by one and initializing \(\mathrm{D}(\mathrm{I})\), which keeps track of the particular crossing option we are trying at each step I in the crossing sequence. The variable \(D(\mathrm{I})\) is then incremented and a test is made to see if we have exhausted the available options:
\[
\begin{aligned}
& 340 \mathrm{I}=\mathrm{I}+1: \mathrm{D}(\mathrm{I})=0 \\
& 350 \mathrm{D}(\mathrm{I})=\mathrm{D}(\mathrm{I})+1: \mathrm{IF} \mathrm{D}(\mathrm{I})>5 \text { THEN } 600
\end{aligned}
\]

\section*{Testing Options}

If all options have been tried without success, the machine is directed to line 600 and asked to execute a back-up procedure that tries another option at an earlier position in the sequence. If we still have a viable option at this previous value of I , we continue by examining the particular crossing option which is indicated. First, we determine the location of the boat (line 360), then make sure we have a sufficient number of missionaries and cannibals on the departure bank to carry out the indicated crossing (lines 370 and 380), and finally we make the crossing (lines 390 and 400):
\[
\begin{aligned}
& 360 \text { IF } \mathrm{BP}=-1 \text { THEN } 380 \\
& 370 \mathrm{IF} \mathrm{CL}<\mathrm{CT}(\mathrm{D}(\mathrm{I})) \text { OR } \mathrm{ML}<\mathrm{MT}(\mathrm{D}(\mathrm{I})) \\
& \text { THEN } 350 \mathrm{ELSE} 390 \\
& 380 \mathrm{IF} \mathrm{CR}<\mathrm{CT}(\mathrm{D}(\mathrm{I})) \mathrm{OR} \mathrm{MR}<\mathrm{MT}(\mathrm{D}(\mathrm{I})) \text { THEN } 350 \\
& 390 \mathrm{CL}=\mathrm{CL}-\mathrm{BP} \mathrm{CT}(\mathrm{D}(\mathrm{I})): \mathrm{CR}=\mathrm{CI}-\mathrm{CL} \\
& 400 \mathrm{ML}=\mathrm{ML}-\mathrm{BP} * \mathrm{MT}(\mathrm{D}(\mathrm{I})): \mathrm{MR}=\mathrm{MI}-\mathrm{ML}: \\
& \mathrm{BP}=-\mathrm{BP}
\end{aligned}
\]

Next, we check to make sure that the cannibals do not outnumber the missionaries on either bank. If they do, we go to line 500 to reverse the crossing, and then to line 350 to select another crossing option:

410 IF ML>0 AND CL>ML THEN 500
420 IF \(\mathrm{MR}>0\) AND \(\mathrm{CR}>\) MR THEN 500 ELSE \(\mathrm{K}=0\)


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In addition to an insufficient number of the appropriate persons or the threat of cannibalism, there is another reason for discarding the current crossing plan and going to line 350 to try another. This third reason has to do with repetition of a previous state of the system. We have no desire to create loops which transport the same individuals back and forth forever. In lines 430 and 440 , we check to make sure that the current state has not occurred previously:
```

4 3 0 ~ I F ~ C L = C ( K ) ~ A N D ~ M L ~ = ~ M ( K ) ~ A N D ~ B P = B ( K )
THEN 500
440 K=K +1: IF K<I THEN 430

```

If our current crossing option passes these three tests, then we are ready to proceed. The crossing is recorded for posterity's sake; then we jump to line 300 to start the process once again:
```

450 A$=STRING$(CT(D(I)),"C"):
B$=STRING$(MT(D(I)),'M")
460 IF BP = -1 THEN MV$(I)=A$+B$+"->"
    ELSE MV$(I)="<-"+AS+B\$
470 GOTO 300

```

\section*{Backing Up}

This completes the main loop of the program. We have a few loose ends which need to be taken care of before the job can be considered finished. When we found that a crossing option was not feasible either because of cannibalism (lines 410 and 420), or because of repetition of a previous position (lines 430 and 440), the machine was instructed to go to line 500 and reverse its previous move. Line 500 must therefore exist as follows:
```

$500 \mathrm{BP}=-\mathrm{BP}: \mathrm{CL}=\mathrm{CL}+\mathrm{BP}^{*} \mathrm{CT}(\mathrm{D}(\mathrm{I})$ ):
$\mathrm{CR}=\mathrm{CI}-\mathrm{CL}$
$510 \mathrm{ML}=\mathrm{ML}+\mathrm{BP} * \mathrm{MT}(\mathrm{D}(\mathrm{I})):$
MR=MI-ML: GOTO 350

```

After returning to line 350 to try another crossing, we may find that all five options have been exhausted. If so, it is time to back up our search and try something different at an earlier point in the crossing sequence. The back-up instructions start at line 600:
```

600 PRINT@960, "BACK UP AND TRY
SOMETHING ELSE";
610 I=I-1: IF I <1 THEN PRINT@ 960,
Y$;: GOTO 800
620 CL=C(I-1): CR=CI-CL:ML=M(I-1):
    MR=MI-ML
6 3 0 ~ B P = B ( I - 1 ) : ~ G O S U B ~ 2 0 0 0 : ~ G O S U B ~ 1 0 0 0 ~
640 FOR K=1 TO 800: NEXT K
650 PRINT@ 960, Y$;: GOTO 350

```

The back-up procedure is a little tricky. First, we decrement I by 1, then we set the current status of our main characters to the way it was before we made the last crossing. Our objective is to examine another crossing option at the new value of I. To do this, the position we transform must be the situation as it existed before the

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\title{
A search program such as this one can be quite effective if the number of possible move combinations is not too large.
}
last move. The back-up procedure also calls our graphic routines (line 630), delays a bit for dramatic effect (line 640 ), and then erases the back-up message (line 650) before exiting for line 350 .

There are two terminal conditions for the search process. If we move all the cannibals and missionaries across the river, our mission is successfully completed. This condition is detected by line 320 which directs the machine to line 700 :

\section*{700 PRINT@ 960, "SUCCESS";: GOTO 700}

If we back up to the point where \(\mathrm{I}=0\), then we have exhausted all possibilities and our search has failed. This state of affairs is tested in line 610 and if it holds, the machine is sent to line 800:

\section*{800 PRINT@ 64, X\$;: PRINT@ 960, 'FAILURE";: GOTO 800}

This finishes our program except for specifying the two subroutines which maintain our video display. The first of these occurs at line 1000 and keeps an up-to-date listing of the crossing sequence:
```

1000 IF I =0 THEN RETURN
1010 FOR K=1 TO 14: PRINT@ K*64, X$;:
    NEXT K
1020 S=I-13: IF S<1 THEN S=1
1030 FOR K=S TO I: J=K-S+1
1040 PRINT@ J*64, K; " "'; MV$(K);:
NEXT K: RETURN

```

The second subroutine provides a graphic display of the current position of the boat and of all missionaries and cannibals:
```

2000 Z$=STRING$(8-CR," ''):
CR$=STRING$(CR,"C") + Z\$
2010 Z\$ =STRING$(8-CL,"'"):
    CL$=Z$+STRING$(CL,"C")
2020 Z$=STRING$(8-MR," "):
MR$=STRING$(MR,'M")+Z\$
2030 Z$=STRING$(8-ML," '):
ML$=Z$+STRING$(ML,"M")
2040 IF BP=1 THEN B$=BL\$ ELSE B$=BR$
2050 PRINT@ 468, CL$;: PRINT@ 492, CR$;:
PRINT@ 478, B$;
2060 PRINT@ 532,ML$;: PRINT@ 556, MR\$;:
RETURN

```

\section*{Limitations and Features}

A search program such as this one can be quite effective if the number of possible move combinations is not too large. The missionaries-and-cannibals problem is an
ideal example for this type of search because there is a limited number of options at each choice point. If there were many options at each choice point, a simple trial-and-error search might take a very long time to find a solution sequence. If there were a solution, however, it would find it.

The key features of this program are the I index and the \(D(I)\) array. If we use game terminology, the I variable indexes the move number (ie: first move, second move, third move, etc) and the \(\mathrm{D}(\mathrm{I})\) array keeps track of which move option is currently being considered at each level I of the search. In the missionaries-and-cannibals problem, our program exhaustively considers the various move options. It accepts the first legal move option it can find at each level I of the search.
A move is legal unless it fails one of the three tests (insufficient passengers, lines 370 and 380; cannibalism, lines 410 and 420; or repetition, lines 430 and 440). The search continues forward until it reaches a level where none of the five possible move options are feasible. It then backs up until it can find a new move option at a lower level and then starts forward again. This is a simple yet powerful strategy.

\section*{Improving the Process}

Our implementation of this strategy could be made considerably more "intelligent" if we gave some thought to the order in which crossings are considered. In lines 220 and 230 , we define the five crossing options. We could reduce the number of back-ups by establishing one order of move consideration for trips across to the right bank and another order for trips back to the left bank.
The interested reader might enjoy looking at academic studies which have examined this issue in detail (see, for example, reference 3 ). Some minor modifications can increase the efficiency of the present program by a large factor. One strategy for implementing this idea consists of defining one set of crossing options for left-to-right movement (say lines 220 and 225) and another set of crossing options for right-to-left movement (say lines 230 and 235) and then selecting between the two depending on the value of BP .
Many problems require more direction to the search process if a solution is to be found in a reasonable amount of time. Next month, in the second part of this three-part article, we will consider a much more challenging endeavor, cryptarithmetic. Allen Newell, one of the pioneers in analyzing human thinking in terms of information-processing models, made extensive use of cryptarithmetic as a valuable research paradigm. We will develop a search program in TRS-80 Level II BASIC that is capable of solving all cryptarithmetic problems.

\footnotetext{
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2. Minsky, M (editor), Semantic Information Processing, MIT Press, Cambridge MA, 1968, page 12.
3. Jeffries R, P G Polson, L Razran, and M E Atwood, "A Process Model for Missionaries-Cannibals and Other Rivercrossing Problems," Cognitive Psychology, 1977, volume 9, pages 412 thru 440.
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\section*{A Better Way to Indirectly Address the 6502}

In the article "Indirect Addressing for the 6502," by Ken Skier in the January 1980 BYTE (page 118), there was an error in listings 2 and 3. Because absolute addresses occupy 2 bytes, the address of the \(\chi\) th subroutine will be in position \(2 X\) in the address table, not the \(X\) th. This problem can be corrected by storing the high address bytes in one table and the low-order bytes in another. With this structure the \(X\) th entry will correspond to the \(X\) th
subroutine.
I would like to suggest two other methods of implementing indexed indirect jumps which are more efficient in terms of code length and execution time. The first method is that of vectoring: 3 bytes are reserved as the "vector." The first byte always contains a hexadecimal 4C (JMP). The target address is placed in the next 2 bytes and a JMP or JSR is then done to the vector, so that control passes to the selected module.

The second method, however, is the more effective and concinnate. Sup-

Listing 1
CALL.X
LDA. TBL. HI, X
PHA
LDA TBL.LO,X
PHA
RTS
pose that we wish to call routine \(X\), and that the address table is structured as 2 rows: TBL.LO and TBL.HI.
Consider the routine
CALL. \(X\), shown here as listing 1.

By doing a JMP or JSR to CALL. \(X\) an indexed indirect JMP or JSR will be effected to the \(X\) th routine. One point to be observed here is that the execution of a RTS instruction pops the stack into the program counter, and then increments it. Thus the addresses in the table must be one less than their actual value.
Thomas Gettys, Co-editor SYM-Physis
SYM-1 Users' Group
POB 315
Chico CA 95927

\section*{Notes on Attending a USUS Meeting}

The first meeting of the USUS (UCSD System Users Society) was held in San Diego, California, on June 20 and 21, 1980. The meeting was called by SofTech Microsystems, then
turned over to the approximately one hundred participants at the meeting. Speakers at the meeting included Carl Helmers and Ken Bowles. Organization, choosing a name, and the election of officers were the main formal goals. Jim Bandy was elected president, A Winsor Brown was elected vice-president, Chip Chapin was elected secretary, and Jon Bondy was elected treasurer. Informal accomplishments included the usual exchange of information which occurs between users of similar software. The next meeting of the USUS group will coincide with the Minicomputer and Microcomputer Conference and Exposition to be held on October 14, 15, and 16, 1980, in San Francisco, California. For further information, contact the secretary, Chip Chapin, at the following temporary address: UCSD System Users Society, attn: Chip Chapin, Secretary, 9494 Black Mountain Rd, San Diego CA 92126....CH

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Photo 1: Video screen display of the character set produced by the APL character-generator circuit described in this article.

Many computer enthusiasts are beginning to use APL and are discovering the benefits of this powerful high-level language. Unfortunately, most personal computers are not equipped to generate the special APL characters.

Various solutions to this problem have been proposed, ranging from using inverse-video characters to using a

\footnotetext{
Author's Note:
Readers who wish to build this circuit but do not have access to an erasable programmable read-only memory (EPROM) programmer can obtain preprogrammed 2708s from the author for \(\$ 20\).
}


Photo 2: The circuit of figure 1 as constructed on a small perforated circuit board.
programmable display that allows you to define any characters you want under program control.

Here is another solution. With the addition of only a few integrated circuits, and with only a single change in your present video interface, you can have the essential APL characters, including overstrikes. The circuit presented here should work with any video display using the popular MCM6571 character generator and can easily be adapted for others.

The first thirty-two positions in the MCM6571 are occupied by Greek letters and other seldom-used characters. The idea is to replace these with APL characters. After I listed the useful APL characters and

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Figure 1: Schematic diagram of the character-generator circuit which is to be constructed on a small circuit board for connection to the main video display board by a multiconductor cable. All connections are made through a 24 -pin dual-in-line plug that plugs into the socket vacated by the removal of the MCM6571 from the video display board. The MCM6571 socket must have -5 V potential applied to its pin 14; this is the only modification needed on the video display board itself. Adding this -5 V connection does not affect normal operation since pin 14 on the MCM6571 package is not connected inside. To get the \(\{\) and \(\}\) characters instead of the \(\mathcal{A}\) and characters, disconnect pin 16 of the 74150 device.
eliminated those already found in the ASCII (American Standard Code for Information Interchange) character set, thirty-five remained to be implemented.
Most people can probably do without the braces and accent grave ( | )') from the ASCII character set, so I replaced them. If you need to have the braces, you can substitute them for the \(\nrightarrow\) (NAND) and \(\nsim\) (NOR) symbols.
The circuit to produce the APL characters is presented in figure 1. It contains the original MCM6571 character generator from the video interface and a 2708 erasable programmable read-only memory (EPROM) programmed as an APL character generator. The 74145

BCD-to-decimal decoder and 74150 1-of-16 data selector decide which character generator to select, and the 74157 noninverting 2 -to- 1 -line data selectors act accordingly.
The circuit can be built on a small board and plugged into your video display with a short ribbon cable and a 24 -pin dual-in-line plug. The only modification to your video interface is to connect -5 V to pin 14 of the character-generator socket. This will not affect normal operation because pin 14 is not connected inside the MCM6571.

The data that must be programmed into the 2708 is listed in table 1. The character codes that invoke the APL characters are shown in table 2 .

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\(\square\) muSIMP/muMATH-muSIMP is a high level
(1) programming language suitable for symbolic
\(W_{\text {using a fast and efficient interpreter requiring }}^{\text {and }}\) only 7 K bytes of machine code. muMATH is a package of programs written in muSIMP. The package performs sophisticated mathernatical functions Keeps track of up to 611 digits. Performs matrix operations on arrays: transpose, multiply, divide, inverse and other integer powers. Logarithmic, exponential, trigonometric simplification and transformation. symbolic diferegration of detinite and indefinite integrals. tegration of detinite and indetinite integrals.
Requires \(\mathbf{4 0 K ~ C P / M . . . . . . . . . . . . ~} \mathbf{\$ 2 5 0 / \$ 2 0}\)
\(\square\) muLISP-79-Microcomputer implementation of LISP. The interpreter resides in only 7K bytes of memory yet includes 83 LISP functions. Has infinite precision integer anthmetic expressed
in any radix from 2 to 36 . muLISP- 79 includes in any radix from 2 to 36 . muLISP- 79 includes functions and entertaining sample programs. ........................ \(\$ 200 / \$ 15\)
\(\square\) XMACRO-86 - 8086 cross assembler. All age. Mnemonics slightly modified from Inte
 available . . . . . . . . . . . . . . . . . . . . . . \(5275 / \mathbf{2 5}\)
\(\square\) EDIT-80 - Very fast random access text editor for taxt with or without line numbers. Global and
intra-line commands supported. File compare
utility included. ..................... \(\$ 89 / \$ 15\) utility included.
\(\square\) PASCAL/M* - Compiles enhanced Standard (3) Pascal to compressed efficient Pcode. Totally CP/M compatible. Random access tiles. Both 6 and 32-bit Integers. Runtime error recovery Convenient STRINGs. OTHERWISE clause on CASE. Comprehensive manual ( 90 pp . indexed). SEGMENT provides ovenay structure.
INPORT, OUTPORT and untyped files for arbirary //O. Requires 56 K CP/M. Specify 1) 8080 CP/M, 2) Z80 CP/M. or 3) Cromemco \(\square\) PASCAL/Z- 280 native code PASCAL com-
(1) piler. Produces optimized, ROMable re-entran support library. The package includes compiler elocating assembler and linker, and source for all library modules. Variant records, strings and direct I/O are supported. Requires 56 K
PASCAL/MT-Subset of standard PASCAL
- Generates ROMable 8080 machine code.
(1) Symbolic debugger included. Supports interrupt procedures, CP/M file I/O and assembly language interface. Real variables can be BCD, software floating point, or AMD 9511 hardware floating point. Includes strings enumerations and record data types. Manual explains BASIC \(\$ 30\)
\(\square\) APL/V80-Concise and powerfullanguage for
(2) application software development. Complex
(2) programming problems are reduced to simple
expressions in APL. Features include up to 27 K

N active workspace, shared variables, arrays of up to 8 dimensions, disk workspace and copy object library. The system also supports auxil iary processors for interfacing I/O ports. Re-
 ALGOL-60-Powerful block-structured lan
(2) guage compiler featuring economical run-time dynamic allocation of memory. Very compac ( 24 K total RAM) system implementing almos all Algol 60 report features plus many powerfu extensions including stiing handling direct disk CBASIC-2 Disk Extended BASIC-Non - interactive BASIC with pseudo-code compile and run-time interpreter. Supports full file conrol, chaining, integer and extended precision

\section*{MICRO FOCUS}

STANDARD CIS COBOL - ANSI ' 74 COBOL (1) standard compiler fully validated by U.S. Navy tests to ANSI level 1. Supports many features to level 2 including dynamic loading of COBOL
modules and a full ISAM file facility. Also, pro modules and a full ISAM file facility. Also, pro gram segmentation. interactive debug and powerful interactive extensions to support pro from COBOL programs used with any dumb
\(\square\) FORMS 2-CRT screen editor. Output COBOL data descriptions for copying into CIS COBOL programs. Automatically creates a query and update program of indexed files using CRT protected and unprotected sčreen
formats. No programming experience needed. Output program directly compiled by STAN

EIDOS SYSTEMS
KISS-Keyed Index Sequential Search. Offers complete Multi-Keyed Index Sequential and Di rect Access fite management. includes built-in
utility functions for 16 or 32 bit arithmetic utility functions 10 or 16 or 32 bit arithmetic
string/integer conversion and string compare. Delivered as a relocatable linkable module in Microsoft format for use with FORTRAN-80 o COBOL-80, etc.
KBASIC-Microsoft Disk Extended BASIC version 4.51 integrated by implementation of nine additional commands in language. Pack age includes KISS. REL as descnbed above and a sample mail list program. ... \$585/\$45
To licensed users of Microsoft BASIC-80 To licensed users of Microsoft BASIC-80
(MBASIC) .................. \(\$ 435 / \$ 45\)

XYBASIC Interactive Process Control BASIC - Full disk BASIC features plus unique commands to handle byte rotate and shift and to test and set bits. Available in several ver intege Integer ROM squared .
. \(3350 / 525\) Integer CP/M
\$350/\$25 Extended ROM squared Extended Disk CP/M Integer CP/M Run Time Compiler \(\$ 550 / \$ 25\)
\(. \$ 350 / \$ 25\) Extended CP/M Run Time Compiler \(\$ 450 / \$ 25\)
\(\square\) RECLAIM-A utility to validate media unde CP/M. Program tests a diskette or hard disk surface for errors, reserving the imperfection in invisible files, and permitting continued usage of the remainder. Essential for any hard
disk. Requires CP/M version 2. . . . \(500 / \$ 5\) BASIC UTILITY DISK-Consists of: (1) CRUNCH-14 - Compacting utility to reduce the size and increase the speed of programs in
Microsoft BASIC 4.54, BASIC-80 and TRS-80 BASIC. (2) DPFUN - Double precision subrou functions including square root ratural iog ta base 10, sine, arc sine, hyperbolic sine, hyper bolic arc sine, etc. Furnished in source on dis
STRING/80-Character string handling plus routines for direct CP/M BDOS calls from
FORTRAN and other compatible Microsoft lan guages. The utility library contains routines tha enable progre: ns to chain to a COM file, retrieve command line parameters and search file direc tories with full wild card facilities. Supplied as linkable modules in Microsoft format. \(\$ 95 / \$ 20\) STRING/80 source code available THE STRING BIT-FORTRAN
THE STRING BIT-FORTRAN characte string handling. Routines to find, fill, pack,
move, separate, concatenate and compare character strings. This package completely eliminates the problems associated with character string handling in FORTRAN
Supplied with source ................ \(\$ 65 / \$ 15\)
VSORT - Versatile sort/merge system for fixe length records with fixed or variable length fields. VSORT can be used as a stand-alone package or loadeo and called as a subroutine rom CBASIC-2. When used as a subroutine VSORT maximizes the use of buffer space by stetion of sorting Records may be ip to 255 pytes long with a maximum of 5 fields. Upper lower case transiation and numeric fields supported. .......................... \(\$ 175 / \$ 20\)
- CPM/374X - Has full range of functions to cre ate or re-name an IBM 3741 volume, display directory information and edit the data set con tween 3741 volume data sets and CP/M

\section*{Coming soox}

CPAIds
MASTER TAX-Professiona! tax preparation \(\dagger\) program. Prepares schedules A, B, C, D. E, F G, R/RP, SE, TC, ES and forms 2106, 2119 \(2210,3468,3903,2441,4625,4726,4797\)
4972,5695 and 6521 . Printing can be on readily available. pre-printed continuous forms, on overlays, or on computer generated, IRS ap proved forms. Maintains client history files and is interactive
II
(see below) \(\square\) STANDARD TAX - As above for schedules A
+ B, C, D, E, G, R/RP, SE, TC and forms 2106 and 2441. Also. does not maintain client history GENERAL LEDGER II-Designed for CPA' Stores complete 12 month detailed history of transactions. Generates financial statements. depreciation, loan amortizations, journals, tria balances, statements of changes in financial position, and compilation leiters. Includes payroll system with automatic posting to gen-
eral ledger. Prints payroll register, W2's and eral redger. Prints payroll register, W2 S and
\(=5\)
T/MAKER - Powerful new tool for prepaning management reports with tabular data. Makes financial modeling projects easy. Do you want a
weekly profitability report? Ser wo the table and 4 weekly profitability report? Set up the table and
compute. Just change the sales figures for next compute. ust change the sales ngures for next
week and compute. You have a new report! T/MAKER includes a fuli screen editor for setting up tables which pages left, right, up and down. Compute includes standard anithmetic, percents, exponents, common transcendental functions, averages, maxima. minima, projections, etc. Requires \(48 \mathrm{~K} \mathrm{CP} / \mathrm{M}\) and CBASIC-2
BSTAM - Utility to link one comp also equipped with BSTAM. Allows file tronsther at full data speed BSTAM. Allows file transfers CRC block control (heck for very to hex), with detection and automatic retry. We use it! it is great! Full wildcard expansion to send \(w\). COM etc. 9600 baud with wire. 300 baud with phone version. Both ends need one. Standard and WHATsit can talk to one another. \(\$ 150 / \$ 10\) using associative tags to retrieve information by subject. Hashing and random access used for fast response. Requires CBASIC-2 \(\mathbf{\$ 1 7 5 / \$ 2 5}\) SELECTOR III-C2-Data Base Processor to ( Create and maintain multi-key data bases. summaries or mailing labels. Comes with sample applications, including Sales Activity, Inventory, Payables. Receivables. Check Register, and Client/Patient Appointments, etc. Requires CBASIC-2. Supplied in source .... \(\$ 295 / \$ 20\) GLECTOR-General Ledger option to for customized COA. Unique chart of transaction types insure proper double entry bookkeeping. Generates balance sheets, P\&L
statements and journals. Two year record alstatements and journals. Two year record al-
lows for statement of changes in financial position report. Supplied in source. Requires SELECTOR III-C2.CBASIC-2 and 56 K \(\square\) CBS - Configurable Business System is a comprehensive set of programs for defining out using a programming language such as BASIC. FORTRAN, etc. Multiple key fields for each data file are supported. Sel-up program
customizes system to user's CAT and printer customizes system to user's CAT and printer.
Provides fast and easy interactive data entry Provides fast and easy interactive data entry
and retrieval with transaction processing. and retrieval with transaction processing.
Report generator program does complex calcuReport genefator program does complex calcu-
lations with stored and derived data, record selection with multiple criteria, and custom formats. Sample inventory and mailing list systems included No support Ianguage required

Ianguage
\(. \$ 295 / \$ 40\)

\section*{MICRO DATA BASE SYSTEMS}
\(\square\) HDBS - Hierarchical Daia Base System CODASYL oriented with FILEs, SETs. REC ADD DELETE, UPDATE, SEARCH, and TRAVERSE commands supported. SET ordering is sorted, FIFO, LIFO, next or prior. One to many set relationship supported. Read/write protection at the FILE level. Supports FILEs which extend over multiple ftoppy or hard disk devices.
MDBS-Micro Data Base System. Full network data base with all features of HDBS plus multilevel read/write protection for FILE, SET, RECORD and STEM. Explicit representation of one to one, one to many, many to many, and many to owner and multiple record types within SETs owner and multiple record type
HDBS-z80 version
\$250/\$40**
\(\square\) MDBS-Z80 version
. \(7750 / 540^{* *}\) 8080 version available at \(\$ 75\) extra.
When ordening, specify one of the language interfaces listed below Additional language interfaces available at time of purchase for \(\$ 100\) or \(\$ 125\) if purchased later.
*The single manual covering HDBS and MDBS when purchased alone comes without
specific language interface manual. Manuals specific language interface manual. Manuals
are available for the following Microsoft languages:
1) MBASIC 4.51, 2) BASIC-80 5.0, 3) Compiled BASIC of FORTRAN-80, 4) COBOL-80, 5) MACRO-80

SNA/S10

\section*{MICROPRO}

SUPER-SORT I-Sort, merge, extract utility as absolute executable program or linkable module in Microsoft format. Sorts fixed or variable records with data in binary BCD. Packed Decimal. EBCDIC, ASCII, floating \& fixed point, exponential, tield justified, elc. Even variable SUPER-SORT II-Above available as absolute program only .................. \(\$ 175 / \$ 25\) SUPER-SORT III-As II without SELECT//
EXCLUDE DATASTAR - Protessional forms control entry and display system for key-to-disk data capture. Menu driven with built-in learning aids.
Input field verification by liength, mask, attribute (i.e. upper case, lower case, numeric, auto-dup, etc.). Built-in arithmetic capabilities using keyed data. constant and derived values. Visual feedback for ease of forms design. Files compatible with CP/M-MP/M supported languages. Re-

WORD-STAR-Menu driven visual word pro(t) cessing system for use with standard terminals Text formatting performed on screen. Facilities for text paginate, page number, justify, center while simultaneously editing a second. Edit facilities include global search and replace Read/Write to other text files, block move, etc Requires CRT terminal with addressable cursor positioning . ................... \$445/\$40
WORD-STAR-MAIL-MERGE-As above with option or produclion maliing of personalized NAD . . ............................ \(\$ 575 / 840\)
\(\square\) WORD-STAR Customization Notes - For many standard terminal nor have one of the tions in the distribution version of WORDSTAR ……….................... SNA/\$85 WORD-MASTER Text Editor-In one mode ing superset of CP/M's ED commands includ ing glooal searching and replacing, forwards
and backwards in file in video mode, provides full screen editor for users with serial address-
able-cursor terminat
] TEXTWRITERIII-Text formatter to justity and paginate letters and other documents. Specia 1eatures include insertion of text during execu-
tion from other disk files or console recipe documents to be created from linked fragments on other files. Has facilities for sorted index, table of contents and footnote insertions Ideal for contracts, manuals, etc. Now compatible with Electric Pencil* and Word-Star pre-
pared files ......................... \(\$ 25 / \$ 20\) pared files

\section*{New lawer prices \\ for appalicito Soptenere}

\section*{PEACHTREE SOFTWARE}
\(\square\) General accounting sotware for small busi(1) nesses. Each product can be used alone or with fautomatic posting to the general ledge
Supplied in source for Microsol BASIC 4.51 . GENERAL LEDGER.
\(\$ 530 / \$ 40\)
\(\$ 530 / 540\) ACCOUNTS PAYABLE
ACCOUNTS RECEIVABLE....... \(\begin{array}{r}\$ 530 / \$ 40 \\ \text { PAYROLL }\end{array}\) \$530/\$40 PAYROLL
ALSO:
ALSILING ADDRESS
PROPERTY MANAG
\(\$ 650 / \$ 40\)
\(\$ 530 / \$ 40\)
\(.5925 / \$ 40\)

\section*{GRAMAM-DORIAN SOFTWARE}

Comprehensive accounting software written in CBASIC-2 and supplied in source code. Each software package can be used as a stand-alone \(\dagger\) system or integrated with the General Ledger quires CBASIC-2.
GENERAL LEDGER
\(\$ 805 / 540\)
\(\$ 805 / 540\) ACCOUNTS RECEIVABLE ......... \(\$ 805 / \$ 40\)
INVENTORY SYSTEM JOB COSTING APARTMENT MANAGEMENT .... \(\$ 805 / \$ 40\)
CASH REGISTER ............. \(\$ 805 / \$ 40\)

POSTMASTER-A comprehensive package 8 for mail list maintenance that is completely menu driven. Features include keyed record program is included which provides neat letters on single sheet or continuous forms trcludes NAD tile translator. Requires CBASIC-2
\(\$ 150 / \$ 20\)

\section*{STRUCTURED SYSTEMS GROUP}
\(\square\) Complete interactive accounting software for \(\dagger\) business. Each product can be used stand ledger. Each product is thoroughly tested and very well documented. Each product requires CBASIC-2.
GENERAL LEDGER … ........ \(\$ 820 / \$ 40\) ACCOUNTS RECEIVABLE ….... \(\$ 820 / \$ 40\)
 INVENTORY CONTROL .............. \(\$ 820 / 540\)

\section*{NEW! NEWSLETTER FROM LIFEBOAT}

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=
\(1]\) ANALYST - Customized data entry and report\(\dagger\) ing system. User specifies up to 75 data items per record. Interactive data entry, retrieval. and update racility makes information generator provides customized reports using generator provides cusiomized reporis using
sefected records with multiple level breakpoints for summarization. Requires a disk sort utility such as QSORT, SUPER-SORT or VSORT and CBASIC-2 ............ \$250/\$15 LETTERIGHT-Program to create, edit and type letters or other documents. Has fachities io video screen presentation. Designed to integrate with NAD for form letter mailings. Re-
quires CBASIC-2 . . . . . . . . . . . \(200 / 525\)
1 NAD Name and Address selection systemInteractive mail list creation and maintenance program with output as full reports with reference data or restricted information for mail labels. Transfer system for extraction and transfer of selected records to create new files. Re-
quires CBASIC-2 . . . . . . . . . . . . . \(\$ 100 / 520\)
\(\square\) OSORT - Fast sart/merge program for files with fixed record length, variable field length ing keys Full tock up ing keys. Full back-up of input files created

\section*{}
\(\square\) HEAD CLEANIMG DISKETTE-Cleans the drive Read/Write head in 30 seconds. Diskette absorbs loose oxide particles, fingerprints, and other foreign particles that might hinder the performance of the drive head. Lasts at least 3
months with daily use. Specity \(5^{\prime \prime}\) or \(8^{\prime \prime}\) Single sided \(\$ 20\) enth 55 Single sided ........... \(\$ 20\) ench/ish 565 for 3
\(\square\) FLIPPY DISK KIT - Template and insifuctions to modity single sided \(51 / 4^{\prime \prime}\) diskettes for use of
\(\square\) FLOPPY SAVER - Protection for center holes for \(5^{\prime \prime}\) and \(8^{\circ}\) Hoppy disks. Only 1 needed per tool and tough 7 mil mylar reinforcing rings for 25 diskettes. 5 5', Kit.
 8, Rings only . . . . . . . . . . . . . . . . . . . . . . . . . \(\$ 8.85\)
\(\square\) PASCAL USER MANUAL AND REPORTthe language. Recommended for use by Pascal/ \(\mathcal{L}\), Pascal/M and Pascal/MT users \(\mathbf{\$ 1 2}\)
\(\square\) TME C PROGRAMMING LANGUAGE-By on the tanguage. Recommended for use by
BDS C tiny C and Whitesmiths C users 512 \(\square\) STRUCTURED MICROPROCESSOR PRO-GRAMMING-By the authors of SMAL/80 Covers structured programming, the 8080 guage
ACCOUNTS PAYABLE \& ACCOUNTS RECEIVABLE-CBASIC - By Osborne/
McGraw.Hill GENERAL LEDGER-CBASIC-By
Osborne/McGraw-Hill ................ \(\$ 20\) 1 PAYROLL WITH COST ACCOUNTING-
CBASIC-by Osborne/McGraw-Hill LIFEBOAT DISK COPYING SERVICETranster data or programs from one media for-
mat to another at a moderate cost from \(\$ 25\)

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TRS-80 is a trademark of Tandy Corp Pascal/M is a trademark of Sorcim.
Soficard is a trademark of Microsof Apple is a trademark of Apple Computer PASM, PLINK, BUG and \(\mu\) BUG are trademarks of Phoenix Sottware Associates Lid.
CPAids is a trademark of Computer Tax Service, Inc.
\(\dagger\) Recommended system conliguration consists and 132 column printer
- Modified version available for use with CP/M as implemented on Heath and TRS-80 Model computers.
(1) User license agreement for this product must esigned and returned to Lifeboal Associates before shipment may be made.

This product Includes/eXcludes the language
(3) Serial number of CP/M system must be supplied with orders.
(8) Requires Z80 CPU

\section*{Ordering Information}

\section*{MEDIA FORMAT ORDERING CODES}

When ordering, please specity formal code.
LIFEBOAT ASSOCIATES MEDIA FORMATS LIST
Disetle, carridge disk and cartridge tape format codes to be specilied when ordering software for listed computer or disk systems. Als sotware products have specilic requirements in memory size, support operating system or language.


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Executes Pascal 13x laster than an LSI-11!
The SUPEA-MICAO'- series of X-pert Systems,"' designed by Computex. combine high pertormance with low cost. The X 9000 system line features the with low cost. The X9000 system line features
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\begin{tabular}{lllllllllllllllll}
200 & 00 & 00 & 00 & 00 & 00 & 00 & 08 & \(1 C\) & \(2 A\) & \(2 A\) & \(1 C\) & 08 & \(7 F\) & 00 & 00 & 00 \\
210 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 \\
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240 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 \\
250 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 \\
260 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 \\
270 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 \\
280 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 \\
290 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 \\
\(2 A 0\) & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 \\
\(2 B 0\) & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 \\
2 CO & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 \\
2D0 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 \\
\(2 E 0\) & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 \\
2F0 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00
\end{tabular}
\(\begin{array}{lllllllllllllllll}300 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00\end{array}\) \(\begin{array}{lllllllllllllllll}310 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00\end{array}\) \(320 \quad 00 \quad 00 \quad 00\) \(330 \quad 00 \quad 00 \quad 00 \quad 00 \quad 00 \quad 00 \quad 00 \quad 00\) \(\begin{array}{lllllllllllllllll}340 & 00 & 00 & 00 & 00 & 00 & 00 & 4 \mathrm{~A} & 2 \mathrm{E} & 20 & 57 & 2 \mathrm{E} & 20 & 00 & 00 & 00 & 00\end{array}\) \(\begin{array}{lllllllllllllllll}350 & 00 & 00 & 00 & 00 & 00 & 00 & 4 \mathrm{C} & 61 & 6 \mathrm{E} & 67 & 6 \mathrm{E} & 65 & 72 & 20 & 00 & 00\end{array}\) \(\begin{array}{lllllllllllllllll}360 & 00 & 00 & 00 & 00 & 00 & 00 & 57 & 42 & 32 & 4 F & 53 & 5 A & 2 F & 36 & 20 & 00\end{array}\) \(\begin{array}{lllllllllllllllll}370 & 00 & 00 & 00 & 00 & 00 & 00 & 33 & 2 F & 34 & 2 F & 37 & 38 & 00 & 00 & 00 & 00\end{array}\) \(\begin{array}{llllllllllllllllll}380 & 00 & 00 & 00 & 00 & 00 & 00 & 42 & 24 & 18 & 18 & 24 & 42 & 00 & 00 & 00 & 00 & \times\end{array}\) \(\begin{array}{llllllllllllllllll}390 & 00 & 00 & 00 & 3 C & 02 & 02 & 3 E & 42 & 42 & 42 & 42 & 42 & 00 & 00 & 00 & 00 & y\end{array}\) \(\begin{array}{llllllllllllllllll}3 A 0 & 00 & 00 & 00 & 00 & 00 & 00 & 7 E & 20 & 10 & 08 & 04 & 7 E & 00 & 00 & 00 & 00 & z\end{array}\)
 \(\begin{array}{llllllllllllllllll}3 C 0 & 00 & 00 & 00 & 00 & 00 & 00 & 08 & 08 & 08 & 08 & 00 & 08 & 08 & 08 & 08 & 00 & \text {; }\end{array}\) \(\begin{array}{llllllllllllllllll}3 D 0 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 00 & 08 & 54 & 2 B & 41 & 00 & 00 & 00 & 00 & \nsim\end{array}\)
 \(\begin{array}{llllllllllllllllll}3 F 0 & 00 & 00 & 00 & 00 & 00 & 00 & 7 F & 7 F & 7 F & 7 F & 7 F & 7 F & 7 F & 7 F & 7 F & 00 & -1\end{array}\)
is the data in hexadecimal form, and to the right is the character formed by the data in that row.

\section*{SYSTEM X9020}
(CPU Manual 519.95 )

\$4195*

\section*{READY TO RUN}

SYSTEM FEATURES (partion list)
Pascal MICROENGINE \({ }^{\text {™ }} \times 9000\)
- 16 brt P-code CPU
- \(64 k\) bytes RAM/Full DMA
- Fioppy disk controller (SS or DS)
- Fioating point hardware (IEEE standard)
- System software with enhancements
- 2 serral 2 parallel ports
- Pascal compiler, text editors, file manager.

CPU \& memory diagnostics, symbolic Pascal debugger, linker, utilities and more
Floppy Diak Drwes (2)
- 1M combined memory
- Double densily, single sided
- Standard 8" diskettes
- 6 ms track to track

\section*{SYSTEM X9023 s5095*}

PLUS...
x -920 FEATURES (partial liat) (Manual \$10)
X-922 F FEATUAEs (pantiel lil)
- Microprocessor controlled
- Serial RS232C and 20 ma current loop
- 10 baud rates -75 to 19.200
- 24 lines \(\times 80\) characters
- \(12 \times 10\) character resolution
- Oual intensity display
- Programmable reverse video \& underline
- 14 key numeric pad with decimal
- 8 specit function keys
- 2 block tranamission keys
- Block. protect a seif-test modes
- 80 storable tabbing
- Insert/delete character and line
- Addressable cursor
- A host of other features, including cursor controls and remote commands such as cleap to nulis, spaces, end of line, end of screen; set hi, low, zero intensity; set blink, atc
*LIMITED TIME cath price. 10\% DOWN guarantees priority. Master Charge \& VISA cards accepted.

Prices: X9000 CPU \$2995. Manual \$19.95. X-920 CAT \(\$ 995\). Manual \$10. Perkin-Elmer "Bantam" CRT \$799. x-800 disk drive \(\$ 495\). Hardware F.O.B. Chicago. Manuale postpaid.
Custom systems are also available We eervice what we Custom systems are also available. We eervice what w contracts. Custom software. We provide expert fechnice support.


Photo 3：The APL character－generator circuit is connected by a ribbon cable to the socket formerly occupied by the MCM6571 part on the video display board．Use of sockets enables you to unplug the APL character－generator circuit and plug the MCM6571 back into the video display board if you need to reactivate the Greek characters．
\begin{tabular}{|c|c|c|c|c|c|}
\hline Hexadecimal Code & Old Character & New Character & Hexadecimal Code & Old Character & New Character \\
\hline 00 & a & \(\rightarrow\) & 10 & \(\rho\) & \(\leftarrow\) \\
\hline 01 & \(\beta\) & ¢ & 11 & \(\sigma\) & ＊ \\
\hline 02 & \(r\) & \(\pm\) & 12 & \(\tau\) & \(p\) \\
\hline 03 & 8 & A & 13 & \(v\) & \(\Gamma\) \\
\hline 04 & － & L & 14 & ¢ & Q \\
\hline 05 & 5 & － & 15 & \(\chi\) & \(\downarrow\) \\
\hline 06 & 7 & ＋ & 16 & ＊ & ＊ \\
\hline 07 & \(\theta\) & \(\nabla\) & 17 & \(\omega\) & \(\div\) \\
\hline 08 & \(\checkmark\) & 4 & 18 & \(\Omega\) & \(v\) \\
\hline 09 & \(\star\) & 2 & 19 & 5 & \(\uparrow\) \\
\hline OA & \(\lambda\) & － & 1A & \(\rightarrow\) & \(\wedge\) \\
\hline OB & \(\mu\) & \(\square\) & 1B & 4 & ⿴囗十 \\
\hline 0 C & \(\nu\) & \(\square\) & 1 C & 1 & \(\leq\) \\
\hline OD & \(\boldsymbol{\varepsilon}\) & エ & 1D & \(\div\) & － \\
\hline OE & － & T & 1E & \(\Sigma\) & \(\geq\) \\
\hline \multirow[t]{4}{*}{OF} & ＊ & － & 1F & \(\approx\) & ＊ \\
\hline & & & 60 & － & ¢ \\
\hline & & & 7B & ＜ & ＊ \\
\hline & & & 70 & ） & \(*\) \\
\hline
\end{tabular}

\title{
:COMPITRAA:ES: EVERYTHING
}

\section*{FROM COMPUMAX BUSINESS SYSTEMS}

The COMPUMAX business applications programs are written with the novice computer user in mind. They are easy to use, yet powerful in their capabilities. Further, COMPUMAX supplies the BASIC source code. Thus the programs are easy to modify.

\section*{MICROLEDGER}

This General Ledger system performs the essential functions of dual entry bookkeeping and matches revenues and expenses:
MICROLEDGER includes the following programs
LEDGER 1 - builds and maintains the CHART OF ACCOUNTS file. This file contains both current and accumulated totals for each account

LEDGER 2 - builds and updates the JOURNAL TRANSACTION file
LEDGER 3 - lists both the the JOURNAL file and the CHART OF ACCOUNTS
LEDGER 4 - computes the TRIAL BALANCE and executes POSTING of journal transactions into the CHART OF ACCOUNTS. An AUDIT TRIAL of all transaction is output. LEDGER 5 - produces the PROFIT AND LOSS STATEMENT
LEDGER 6 -produces the BALANCE SHEET. Assets, liabilities and owners' equities are shown by account and by totals.

\section*{MICROPAY}

An Accounts Payable system, MICROPAY includes the following program \& functions: PAY 1 - initializes both Transaction and Master files, then begins the Accounts Payable process by inputting and adding records in the Transaction file.

PAY 2 - allows for changes and deletions of Transaction and Master records.
PAY 3 - reports outstanding Accounts Payables in four categories; under 30 days, 31-60
days, \(61-90\) days, and over 90 days.
PAY \(4-\) reports all outstanding Accounts Payables for a single customer or for all customers, and computes Cash Requirements.
PAY 5 - reports all outstanding Accounts Payables for a single date or for a range of dates and computes the Cash Requirements.

PAY 6 - lists both the Transactions and Master files.
PAY 7 - prints checks and accumulates and journalizes Accounts Payables. This program simultaneously creates entries for the MICROLEDGER file.
.\(\$ 140.00\)

\section*{MICROREC}

An Accounts Receivable system. MICROREC includes the following programs and functions:

REC 1-Initializes Accounts Receivable files, adds A/R record and prints invoices.
REC 2 - accepts receipt of customer payments and changes or deletions of A/R Transaction or Master file records.

REC 3 - reports outstanding Accounts Receivables in four categories; under 30 days, 31-60 days, \(61-90\) days, and over 90 days.

REC 4 - reports all outstanding Accounts Receivables for a single customer, or for all customers and computes Cash Projections
REC 5 - produces reports for all outstanding Accounts Receivables for a single date or for a range of dates and computes Cash projections.
REC 6 - lists Transaction and Master files and accumulates and journalizes Accounts Receivables, creating JOURNAL entries which communicate with the MICROLEDGER JOURNAL file.

\section*{MICROINV}

This Inventory Control system presents a general method of Inventory Control and produces several important reports. Its program includes:
INV 1 - initializes Transaction and Master files and adds and updates Transaction and Master records.
INV 2 - handles inventory issued or received, creating inventory records. This program also accumulates and journalizes transactions, producing JOURNAL entries which communicate with the MICROLEDGER file
INV 3 - lists both Transaction and Master files.
INV 4 - produces the STOCK STATUSREPORT, showing the standard inventory stock data and stock valuation, and the ABC ANALYSIS breaking down the inventory into
groups by frequency of usage.
INV 5 -gives a JOB COST REPORT/MATERIALS. showing allocation of materials used year-to-date by each job or work code. (This is complemented by the Job Cost Report/ Personnel in the MICROPERS program.)
INV 6 -computes and provides the E.O.Q. (Economic Order Quantities) . . . \(\$ 140.00\)

\section*{MICROPERS}

This is a Payroll/Personnel program whose functions inciude
PERS 1 - initializes the Master file and allows for entry and updates of Master records. PERS 2 - initializes the Payroll file and allows for entry and updates of payroll records. PERS 3 - lists an Employee Master Record or the entire Employee Master file; lists a single Payroll Record or the entire Payroll file.
PERS 4-computes Payroll and prints the PAYROLL REGISTER. Prints PAYCHECKS and creates JOURNAL entries to be fed into the MICROLEDGER JOURNAL file.
PERS 5 - produces the JOB COSTREPORT/PERSONNEL, computes the quarterly 941 bank deposit, and the Annual \(W\)-2 run.
\(\$ 140.00\)
AtI COMPUMAX programs available in machine readable format (diskette form) for the ollowing machines.

> S. TRS-80' Model I APPI F II APPLE II
PET
Micropolis 1053/11
Microsoft under CP/M
CBASIC under CP/M
Cromemco

\section*{FROM ADVENTURE INTERNATIONAL (By Scott Adams)}
\(\dagger\) 1. ADVENTURELAND - You wander through an enchanted world trying to recover the 13 lost treasures. You'll encounter wild animals, magical beings, and many other perils and puzzles. Can you rescue the Blue Ox from thequicksand? Or find your way out of the maze of pits? Happy Adventuring
+ 2. PIRATE'S ADVENTURE - "Yo ho ho and a bottle of rum" You'll meet up with the pirate and his daffy bird along with many strange sights as you attempt to go from
your London flat to Treasure Island. Can you recover Long John Siver's lost your
treasures? Happy Sailing, matey.
3. MISSION IMPOSSIBLE ADVENTURE - Good morning, your mission is to... and so it starts. Will you be able to complete your mission in time? Or is the world's first automated nuclear reactor doomed? This one's well named. It's hard, there is no magic, but plenty of suspense. Good luck
4. VOODOO CASTLE - Count Cristo has had a flendish curse put on him by his enemies. There he lies, with you his only hope. Will you be able to rescue him or is he forever doomed? Beware the Voodoo Man.
\(\star\) All orders processed within 24-Hours
\(\star\) 30-Day money back guarantee on all Software (less a \(\$ 3\) penalty for handling)
5. THE COUNT - You wake up in a large brass bed in a castle somewhere in Transylvani. Who are you, what are you doing here, and WHY did the postman deliver a bottle of blood? You'll love this Adventure, in fact, you might say it's Love at First Byte
6. STRANGE ODYSSEY - Marooned at the edge of the galaxy, you've stumbled on the ruins of an ancient alien civilization complete with labulous treasures and unearthly technologies. Can you collect the treasures and return or will you end up marooned forever?
7. MYSTERY FUN HOUSE - Can you find your way completely through the strangest Fun House in existence, or will you always be kicked out when the park closes?
8. PYRAMID OF DOOM - An Egyptian Treasure Hunt leads you into the dark recesses of a recently uncovered Pyramid. Will you recover all the treasures or more likely will you join its denizens for that long eternal sleep?
9. GHOST TOWN - Explore a deserted western mining town in search of 13 treasures From rattlesnakes to runaway horses, this Adventure's got them all! Just rememFrom rattlesnakes to runaway horses, this Adventure's got them all! Just remem-
ber, Pardner, they don't call them Ghost Towns for nothin'. (Also includes new bonus scoring system!)
* Nole: Apple requires 24 K and has no lower case.
\(\dagger\) Recommended for the novice adventurer, with many built-in HELPS!

\section*{FROM PERSONAL SOFTWARE INC.}

VISICALC
\(\$ 150.00\)
Take virtually any problem you would explore using calculator, pen, and paper, working in rows and columns. Apply VisiCalc and you'll see why every reviewer of this product has said the same thing: VisiCalc is the most useful, most important programyet developed for personal computing.

With VisiCalc, you work with an electronic worksheet of up to 63 columns and 254 rows. At the juncture of any column and row you can type in words and numbers. Visicalc automatically performs all arithmetic functions, net present value, and transcendental functions - instantly!

\section*{CCA DATA MANAGEMENT SYSTEM}
\(\$ 74.95\)

\section*{DMS Features:}

File Creaton and Maıntenance:
- Fields may be alphanumeric, numeric, integer, floating point, or fixed decimal with

Commas. using constants or other fields in the same record. Functions include add, subtract, using constants or other fieds in the same reco
- Records are easily located, using the SCAN feature. SCAN for records with a field over, below, or between a range of values.
- Records are easily added and updated. DMS "prompts" you with questions
- Records are easily added and updated. DMS "prompts" you with questions
- Multi-diskette capabilities for larger files - up to 85,000 characters per file!
- Multi-diskette capabilities for larger files - up to 85,000 characters per file!
- Sort the records into almost any order, using up to 10 fields as "keys". So you can
- Sort the records into almost any order, using up to 10 fi
sort for customer numbers; within zip code, for instance.
sort for customer numbers; within zip code, for instance.
- Delete records, "compact" files, and backup files on data diskettes easily.
Delete records,
Report Features:
- Print reports with records in any order.
- Select fields to be printed.
- Print mailing labels.
- Numeric totals and subtotals can be specified when a value in an unrelated field in
the same record changes. For example, sort, subtotal, and print according to department, or month, or customer number, or model number.
gUARANTEED PROFIT
THE HORSE SELECTOR II (FLATS) (By Dr. Hal Davis).
50.00

New simplified version of the original Horse Selector. The first Horse Selection System
to actually calculate the estimated odds of each horse.
HIGHER PROFITS (OVER \(100 \%\) ) POSSIBLE THROUGH SELECTIVE BETTING ON:
- Rates each horse in 10 seconds
- Easy to follow rules.
- Can be used with any Apple II Computer.
- 100\% money back guarantee (returned for any reason).
- Uses 4 factors (speed rating, track variant, distance of the present race, distance of the last race)
- Using the above factors, the Horse Selector calculates the estimated odds. BET on horses whose actual payoff (from the Tote Board or Morning Lines) is higher than payoff based on estimated odds.
- Using the above factors, the Horse Selector calculates the estimated odds. BET on
any selected horse with an estimated payoff (based on Tote Board or Morning Lines) higher than calculated payoff (based on Horse Selector II).
higher than calculated payoff (based on Horse Selector II).
\(\bullet\) Source listing for the TRS-80
- Source listing for the TRS-80'•TI-59. HP-67. HP-41, Apple and BASIC Computers.
- No computer or calculator necessary (although a ca

FREE Dutching Tables allows betting on 2 or more horses with a guaranteed profit.


\title{
Construction of a FourthGeneration Video Terminal: Part 2
}

\author{
Theron Wierenga \\ POB 2007 \\ Holland MI 49423
}

Last month in Part 1, I presented the first part of a complete plan for building a versatile, microprocessorcontrolled video terminal. Now we'll look at the rest of the construction details.

We stopped at the point of troubleshooting the 8085 microprocessor and related circuitry. If your tests with oscilloscope and frequency counter show that everything built so far checks out properly, you can proceed with the remainder of the construction.

\section*{Getting the Debug Monitor Operating}

The next step is to install the four 2114 memory circuits, IC19 (the 74LS138 that decodes the 2114s), IC13 (the 7401 that is used with the \(74 L S 138\) decoder), and IC4 and IC5 (the two 8212s that are connected back-to-back to buffer the 2114s). The 2716 must be programmed again, this time with the entire software

\footnotetext{
The numbering sequence of figures, listings, and photos is continued from Part 1 of this article.
}
package that is given in listing 2.
Before continuing, let me define some terms that are frequently used in the next section. Figure 4 on page 128 is a block diagram relating a number of these terms.
- Host computer: the computer to which your completed video terminal will be connected. It will operate completely independently of the terminal circuitry. Communications between the host computer and the video terminal will be via a serial interface driven by UARTs.
- 8085 microprocessor: the computer that will control the internal operation of the video terminal.
- Checkout terminal: any standard computer terminal with a currentloop interface that will be used to debug your video terminal's hardware and software.
- Temporary interface: a simple circuit that must be built to temporarily connect your video terminal to the checkout terminal.
- Terminal control software: the software that directs the 8085 in the procedure of controlling the terminal. It operates the display
and takes care of incoming characters and scrolling. This software resides in the 2716 programmable read-only memory.
- System monitor: a separate operating system that resides within the terminal control software. When this monitor is used, the 8085 microprocessor "abandons" the video terminal circuitry, and then behaves as a separate computer for the checkout terminal. The monitor allows the user to load and display memory locations, run simple programs, and fill and move blocks of data in the memory. The data transfer lines to the host computer are not connected when using the monitor.

\section*{Activating the Monitor}

In normal operation the 8085 operates as a dedicated microprocessor. This means that the microprocessor's total job is to operate the display and process incoming characters. The 2716 programmable read-only memory can hold 2048 bytes of program code. Only about 1500 bytes are needed for the terminal control software, so a portion of the

\title{
What's the difference between BAsic and Pascal?
}

\section*{COMPARE THESE APPROACHES TO DRAWING A CIRCLE}

\section*{in BASIC}
"This is easy..."


\section*{in Pascal}
"The simplest circle drawn with line segments is a regular polygon ..."
procedure Circle (X, Y, Radius: real); const Sides \(=16 ; \mathrm{Pi}=3.14159265\); var N : integer; Theta : real; begin Move (X+Radius, Y); for \(\mathrm{N}:=1\) to Sides do begin Theta: \(=2^{*} \mathrm{Pi}\) ( \(\mathrm{N} /\) Sides); Draw (Radius* cos (Theta) +X . Radius * \(\sin\) (Theta) +Y ); end; end;


Programming by design

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\section*{CHOOSE...}

\section*{Choose an Apple Desk}


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\section*{Choose a Mini Rack}


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\section*{Choose a Printer Stand}


The Universal printer stand fits the:

Centronics 700's
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NEC Spinwriter
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Figure 4: Block diagram of the connection of the video terminal to the host computer. Also shown are the temporary connections to the checkout terminal, used for debugging the project.
additional memory has been filled with a completely separate operating system which is termed the system monitor. By causing the 8085 microprocessor to execute a TRAP interrupt, a jump is made to the section of memory wherein the system monitor resides. In this mode the 8085 microprocessor and its associated circuitry cease to control the video terminal circuitry. The 8085 now behaves as a simple computer with a system monitor. Another terminal, the checkout terminal, is necessary to communicate with the system monitor; the temporary interface is also necessary to connect to the checkout terminal.

Construct this interface for temporary use by breadboarding. A schematic diagram was shown in figure 3, part 1. Any general-purpose computer terminal with a 20 mA current-loop interface can now be connected to your video-terminal board. The 8085 microprocessor will be acting as a computer for the checkout terminal. Be sure that the
data rate is the same for both devices. If your checkout terminal runs at 110 bps, you will have to temporarily connect a 7040 Hz square wave into pins 9 and 25 of the 8251 (IC7), since this frequency is not available on the video-terminal board.

When all connections to the temporary interface are made, open the TRAP switch for a moment. The 8085 microprocessor should send a carriage return, line feed, and question mark to your checkout terminal. Next, type a letter D, and the terminal should perform a carriage return and line feed. Now type in four Os, and it should again perform the carriage return and line feed. Lastly, type in " 003 F " and the checkout terminal should print out four lines of memory contents. If you get to this point, congratulate yourself, take a break, have a glass of wine, and show the family you're not as crazy as they thought you were to start this project.

If you were able to get the first test program to send out "U" characters,

Text continued on page 152:


\title{
A Small-Systems Breakthrough!
}

\section*{UDS Line-Powered Data Modems}
- 0-300 bps full-duplex (Model 103 LP) or 0-1200 bps half-duplex (Model 202 LP) over ordinary two-wire telephone circults
- No AC power required
- Fits under your telephone handset
- Easy hook-up
- Simplified controls - talk/data only
- Direct connect to dial-up network
- Better data integrity than acoustic couplers at the same (or lower) price

Patent Applied For

Your telephone, combined with a UDS 103 LP or 202 LP modem, can put your computer system in contact with the rest of the DP world.
Make three easy snap-in cable connections and either of these FCCapproved devices is ready to go on-line. A front-panel rocker switch lets you go from voice to data and back again whenever you wish. Either the 103 or the 202 fits comfortably under your telephone - you'll hardly know the modem is there.
The UDS units are compatible with Bell 103- and 202-series modems.

\section*{SPECIFICATIONS}

OPERATION - full-duplex (Model 103 LP) or half-duplex (Model 202 LP) on two-wire dialup telephone circuits.
DATA RATE - 0-300 bps (Model 103 LP) or 0 to 1200 bps (Model 202 LP).
DIGITAL INTERFACE - RS-232C on both models; TTY current loop on Model 103 LP. CONTROLS AND INDICATORS - Data/Talk switch; data ON light emitting diode.
DISTAICT OFFICES: Summit, N J, 201/522-0025-Blue Bell, PA, 215/643-2336 - Atlanta, 404/9523463 - Cricago, 312/441-7450 - Dallas, 214/385-0426 - Santa Ana, 714/972-4619-Sunnyvale, 408/738-0433.'Boston, 617/875-8868
"Confidence in Communications"

Listing 2：The main video－terminal－control routine．Appended at the end is the system monitor used in the checkout procedure．This code is stored in the 2716 read－only memory．The program was modified by the author from the original routine provided by Intel Corporation．

C CRTLF
8080 MAC：RO
8080 MACRO ASSEMFIEF，UEF 2.0 ERFORS \(=0\) FAGE 1
 ＊＊＊＊ ＊＊＊＊AS FEFi INTEL FEFIFHERAL．＊＊＊＊ ＊＊＊＊COF－YRIGHT INTEL CORF＇－ 1978 ＊＊＊＊ ＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊ ＊＊＊＊JANLJAFY 10， 1979 ＊＊＊＊ ＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊＊ CNCTL EQU 0001 H


COM EQU 060 H
\(\begin{array}{lll}\text { KDAT } & \text { EQU } & 060 \mathrm{H} \\ \text { CRCOM } & \text { EQU } & 051 \mathrm{H}\end{array}\)
CRILAT EQU 050 H
\(\begin{array}{lll}\text { PC2SA } & \text { EQU } & 044 \mathrm{H} \\ \text { FC2TC } & \text { ERU } & 045 \mathrm{H}\end{array}\)
FC3SA EQU 046H
\(\begin{array}{lll}\text { PC3TC } & \text { EQU } & 047 \mathrm{H} \\ \text { MICST } & \text { EQU } & 0\end{array}\)
MDS57 EQU 084H

COLI START T
JMF CRTGO \(\quad\) ，JUMF TO START OF FROGRAM

 ；8257 CHONE CLEAF

；JUMF TO START OF FROGRAM
；traf to monitor
；IOAD STACK FOINTER
；lDAL SCREEN START－1
－LDAD SCREEN STIARS

；TF NOT 1 OOP
； CHECK IF \(\mathrm{H}=87\)

；IF NOT I．DOF，ELSE TONF：

C 4 D 200
\(\mathrm{DB5} 51\)
\(00 \mathrm{C2}\) ；REAI CHAR IN 8251
；REAII 8275 STATUS Listing 2 continued on page 132

\section*{}
\＄SET UF 8257
＊CHECK FOR CHAR REC
rgi゙g NI ษӨHJ Iナヨタ！

BO8O MACRO ASSEMBLER，UER 2．0 ERRORS



\section*{ \\  \\  \\ \(\stackrel{1}{\infty}\)}

妾采齐픙\(\underset{A, O}{C R C O M}\)忽宊


0067
\(006 A\)
\(\begin{array}{ll}0064 & 2210080 \\ 0065 & 21008 \\ 0070 & 220687\end{array}\)
\(\begin{array}{ll}006 \mathrm{D} & 210080 \\ 0070 & 225687\end{array}\)
\(073-218087\)
003 E 6＜00
号

C8GコE\＆ 6800
 \(\begin{array}{ll}0095 & \text { L1351 } \\ 0097 & 3 E 4 F \\ 0099 & \text { 11350 }\end{array}\)

\title{
The VP-111 hobby computer: Start programming for only \({ }^{\$ 9} 9\).
}


New! VP-111
Microcomputer .... \(\mathbf{9} \mathbf{9}\) Assembled* and tested.

Features:
- RCA 1802 Microprocessor.
- 1K Bytes static RAM.

Expandable on-board to 4 K .
Expandable to 32 K Bytes total.
- 512 Byte ROM operating system.
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*User need only connect cables (included), a 5 -volt power supply and speaker.


\section*{New low price! \(\$ 199\)
VP-711, only......... \\ Completely assembled and tested.}

All the features of the VP-111 plus:
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- 8 Bit output port.
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Three comprehensive manuals:
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Please send me the items indicated.
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\(\square\) VP-711 The original VIP Microcomputer (See description above) ......... \$199
\(\square\) VP-44 RAM On-Board Expansion Kit-Fou 2114 RAM ICs. Expands VP-711 memory to 4K Bytes ............ . \$ 36
- VP-590 Color Board-Adds color. Four background and eight foreground colors
Smple Sound Board-.........
- VP-595 Simple Sound Board-Provides 256 programmable frequencies. For simple music or sound effects. Includes speaker
- VP-550 Super Sound Board-Turns

VP-111/711 into a music synthesizer! Two independent sound channels. Outputs to audio ................. \$ 49
- VP-551 4-Channel Super Sound-Includes VP-576 and demo cassette. Requires VP-550 and 4K RAM ............ \$ 7
- VP-570 Memory Expansion Board-Plug-in 4K RAM memory ........ \$ 9
\(\square\) VP-580 Auxillary Keypad-Adds two-player interactive capability. Connects 10 VP-590 or VP-585 two VP-580 Auxiliary Keypads to VP-111/711 ...............
- VP-565 EPROM Programmer BoardPrograms 2716 EPROMs. With sottware . ................... \$99
\(\square\) VP-575 Expansion Board-Provides 4 buffered and one unbuffered expansion sockets ..........
- VP-576 Two-Board Expander-Allows use of 2 Accessory Boards in either //O or Expansion Socket ........ \$20
- VP-700 Tiny BASIC ROM Board-BASIC code stored in 4 K of ROM ...... \(\$ 39\)
\(\square\) VP-701 Floating point BASIC for VP-711 on cassette. Requires 16 K Bytes RAM (avail. \(7 / 80\) ) . . . . . .
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- VP-720 Game Manual-II-Moregames .. \$ 15

\section*{ASCH keyboards.}

VP-601 Keyboard-128-character ASCII encoded alphanumeric 8 -bit parallel output
- VP-616 Keyboard-Same as VP-606 plus 16-key numeric keypad ......... \$119
万 VP-620 Cable-Connects VP-601/611 to VP-111/711 ...................... \(\$\)
Cable-Unterminated for VP-601/611 ...................... \$ 20
VP-626 Connector-Male "D" mates to VP-606/616 ..................... \$ 7

Enclosed is \$___for items checked plus shipping \& handling charge of \(\$ 3.00\). Add your state and local taxes \$___ Total enclosed \$ \(\qquad\)
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Master Charge Interbank No. \(\qquad\) Expiration date
Signature (required for credit orders):
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Street address:
 City:
State \& Zip: \(\qquad\) Telephone:( ) \(\qquad\)
Make checks payable to RCA Corp. Prices and specifications are subject to change without notice.


Listing 2 continued from page 130：
SERUICE 8251 andi enter chaf into disflay

； 8251 REAII Char subroutine
TSes woya y ；Character hanolitng rouitine

\[
\vec{\Sigma}
\]
\[
\begin{aligned}
& \text { LDA } \\
& \text { ANT } \\
& \text { JZ } \\
& \text { CALLI } \\
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；SERUICE 8257 IF INT
；CHECK FOR KEYFRESS ：MASK OFF ETT \(\&\)
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；LOAD yART CHAR ©MASK ALI EIIT BIT 687 OTISTILAY CHAF ROUTTINF ：loati laikt thaf
 ；CTEL CODE ROUTTNE ；FOINT TO ESC FLAB
；SET ESC SER FIAG

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USCHR
OKOH
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NXTZ
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NXTZ： ；ESCAPE SEDUENCE ROUTTNE
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ESREC：MUI A，O \(\begin{array}{ll}\text { OOC7 } & \text { E620 } \\ 00 \mathrm{CO} & \text { C46704 } \\ \text { OOCC } & \text { CIIAOO4 } \\ \text { OOCF } & \text { C3EEOO }\end{array}\) 8
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\(\qquad\) 8080 MACRO ASSEMFLER，UEF 2.0 ERRORS \(=0\) FAGF 4 ；LINAII UART CHAR ：MASK OFF HTGH 4 ETTS ；SHIFT I FDR GFFSET 30 of AHs．ato zow：
 \(\begin{array}{ll}\text { STA } & \text { XFLG } \\ \text { IITA } & \text { USCHK } \\ \text { ANI } & \text { OOFH }\end{array}\)
 \(\begin{array}{ll}\text { LXI } & \text { H，RSET1 } \\ \text { LXI } & \text { Ii，} \\ \text { MO } & \text { EVA }\end{array}\) \(\begin{array}{ll}\text { LXI } & \text { Mi，o } \\ \text { MiJU } & \text { E，}\end{array}\) ニばき



Eun





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\section*{CONTROL DATA LEARNING CENTER}

\begin{tabular}{|c|c|c|c|c|c|}
\hline O2LA & 215000 & NUJF : & LXI & H , 050 H & ; DATI BOTI TN HI \\
\hline 02 DH & 19 & & RIAD & 1 & ; Anti bode th locrio \\
\hline O2DE & 22E087 & & SHI. T & 1.00\%F\% & - Store sum tin hifm \\
\hline \multirow[t]{3}{*}{02E 1} & C3A102 & & .JMF' & İNOME: & : 1.00 F \\
\hline & & ; & & & \\
\hline & & ; COMPE & Ensa't & ton routinf & \\
\hline O2E4 & 2.20087 & COMFY: & L.HI. I & LOCFF & \#1 OCFF TO Hi. \\
\hline 02 E 7 & 1130 Fa & & L. XI . & [.1. OF83OH & : Comme val ue to de \\
\hline O2EA & 19 & & [1/AII & 1 & ;ADIS COMF TM lacer \\
\hline O2EB & 225087 & & SHI Ti, & LOCFF & ©STORF IN MEM \\
\hline \multirow[t]{4}{*}{O2EE} & C9 & & FET & & \\
\hline & & ; & & & \\
\hline & & ; 1.INE. & FEET & ROMTINF: & \\
\hline & & \(\dot{\beta}\) ¢ \({ }^{\text {¢ }}\) & & & \\
\hline \multirow[t]{3}{*}{02EF} & C35801 & CTRL...J: & .JMF' & ESCE & ;CukSOE DOWN 1 L.tive \\
\hline & & ; & & & \\
\hline & & ; CAFRT & IAGE K & Return routt & NE \\
\hline
\end{tabular}
\begin{tabular}{|c|c|c|c|c|c|}
\hline \multicolumn{6}{|l|}{Listing 2 continued from page 134:} \\
\hline 0263 & 19 & & datis & 0 & :SUMAFFOS OF 1ST CHAF GIN FRESENT FOW \\
\hline 0264 & \(22 E 087\) & & SHLIt & L.OCPR & ;STORE IN MEM \\
\hline 0267 & 3 E .87 & & MUI & A, 087H & ;87H TO A \\
\hline 0269 & EC & & C.MF & H & -COMFAFE TO 87H \\
\hline 026A & 127302 & & JNC & UAFi & ; NC=CONT COMFARE \\
\hline 02611 & CDE402 & & C.ALI & COMRY & ;COMPENSATE ROUTINE \\
\hline 0270 & C37F02 & & . MMF & FIN & \\
\hline 0273 & C27F02 & UAR: & JNZ & FIN & ; IF NE ENTI COMPAFE. \\
\hline 0276 & 3ECF & & MUI & A,OCFH & -CFH TO A \\
\hline 0278 & BII & & C.MF- & L & -COMFPARE TO LOW EYTE \\
\hline 0279 & 127F02 & & JNC & FIN & ;NC MEANS < \(=87 \mathrm{CFH}\) \\
\hline 0275 & CIE402 & & CALI. & compy & ¢COMFENSATTON ROUT. \\
\hline 0275 & 2AL1687 & FIN: & L.HLD & TOFAI & - TOF TO HL \\
\hline 0282 & 71 & & MOU & A, L. & : 1 OW EYTE TO A \\
\hline 0283 & FEOO & & CFI & 0 & ;ts rt zerop \\
\hline 0285 & C29702 & & JNZ & TROLL & ; if not to troll \\
\hline 0288 & 7 C & & MOU & A.H & ; high eyte to a \\
\hline 0289 & FEBO & & CPI & OBOH & ; IS IT 80H ? \\
\hline 028E & C 29702 & & JNZ & TROLL & ; IF NOT TO TROLL \\
\hline 028E & 218087 & & LXI & \(\mathrm{H}, 8780 \mathrm{H}\) & ; BOTTOM \(=8780 \mathrm{H}\) \\
\hline 0291 & 22E687 & & SHLII & BDTAI & ; Store in mem \\
\hline 0294 & C3A102 & & JMF* & GNOME & ; JUMP TO GNOME \\
\hline 0297 & 1180FF & TROLL: & L. XI I & D,OFFEOH & :-80¢ TO LiE \\
\hline
\end{tabular}
\begin{tabular}{|c|c|}
\hline & ;STORE IN MEM \\
\hline & ; ALIL -80n TO TOP \\
\hline & ©STORE IN MEM \\
\hline & ; EOR Char to a \\
\hline & ; LOCPR TO HL \\
\hline & - FOEF CHAF TO MFM \\
\hline & - LINW RYTE TO \\
\hline & ; TS IT 80 H \\
\hline & - TF NOT TO WITAF \\
\hline & ;HJGH RYTE TO \\
\hline & 刀ts tit 87H \\
\hline & ; IF NOT TO WITAF \\
\hline & \#FRESENT I.OC TO TIF. \\
\hline & ¢ROTTOM TO HI. \\
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\hline & ; 8000\% TO HL \\
\hline & ;SFT FOCFE TO 8000 H \\
\hline & \%1.00F \\
\hline & - ODCF'ti To JF \\
\hline & ¢ Botton to hi. \\
\hline & O1.0W EYTE TO A \\
\hline & - THOFS Fi=A \\
\hline & ;TF NDT TO NJIF \\
\hline & ; high gyte to a \\
\hline & - ITOFS TIM A \\
\hline & ; IF Not to NuF \\
\hline
\end{tabular}




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}


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Listing 2 continued from page 136:
\begin{tabular}{|c|c|c|c|c|c|}
\hline \multicolumn{6}{|l|}{} \\
\hline 032A & 210287 & COLRT: & LXI & h, cctan & ; COL ENT TO Hi \\
\hline 0325 & 34 & & INR & M & ; TiNF C,OL COUNT \\
\hline 032E & CD3203 & & call L & WF.75 & hi.oait cursor fos \\
\hline 0331 & C9 & & RET & & \\
\hline & & \[
\begin{aligned}
& i \\
& ; \\
& \text { i LOAII }
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\] & Curso & ( FOSITITON & ROUTINE \\
\hline 0332 & 3 EBO & WP75: & mui & \(\mathrm{A}, \mathrm{O8OH}\) & ;loail cur fos comíanim \\
\hline 0334 & \(\underline{1} 351\) & & OUT & CRCOM & \\
\hline 0336 & 3AD287 & & LIA & c.ctad & ;A=CLURSOR X FOSITION \\
\hline
\end{tabular}
\begin{tabular}{ll}
0339 & n350 \\
033B & 3AD587
\end{tabular}
\begin{tabular}{ll}
0341 & 3AD287 \\
0344 & FE4F \\
0346 & CA5003 \\
&
\end{tabular}
\begin{tabular}{|c|c|}
\hline  & Oi \\
\hline 3MM & \\
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\begin{tabular}{|c|c|c|c|c|c|}
\hline & & \[
\begin{aligned}
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& ; 8275 \\
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\end{aligned}
\] & \multicolumn{3}{|l|}{INTERRUFT SERUTCE REINITIAL. ITATION} \\
\hline & & ¢ \({ }^{\text {¢ }}\) & & & \\
\hline 0467 & 3E00 & RT75: & MUI & A MMICS5 &  \\
\hline 0469 & D34B & & OUT & FMDS 57 & ; OUT Tin RP57 \\
\hline 046E & 2 A0687 & & LHLD & TOPAII & ¢ TOF TO HL \\
\hline 046E & 71 & & MOV & ArL & -LOW FYTE TO A \\
\hline O46F & 1.344 & & OUT & PC.2SA & -CHAN 2 START ALIMIRESS \\
\hline 0471 & 7 C & & MOU & A, H & OHTSH EYTE TO A \\
\hline 0472 & D344 & & OUT & FCOSA & OCHAN 2 START ADLIRESS \\
\hline 0474 & 70 & & MOU & A,L. & OLOW BYTE TO A \\
\hline 0475 & 2 F & & CMA & & -COMPIEMENT A \\
\hline 0476 & \(6 F\) & & MOU & L, A & ¢MOUE TO L \\
\hline 0477 & 7 C & & MOU & A, H & ;HTGH EYTE TO A \\
\hline 880 M & 0 ASSEM & R , UER & 2.0 & ERFOFS \(=\) & F'ricF 14 \\
\hline
\end{tabular}







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}
ENTI OF FơW TEST ROUTTNF
\begin{tabular}{|c|c|c|c|c|c|}
\hline 03F1 & 2ADAB7 & EORT: & LHLII & L.OC01 & \#. OF. AF 15 ST C.HAF \\
\hline \(03 F 4\) & 7E & & MOV & A, M & -1ST CHAR TO A \\
\hline 03F5 & FEFO & & CPI & OFOH & OENTI OF ROLI CHAR \\
\hline \(03 F 7\) & C 0 & & RNZ & & : IF NOT EXTT \\
\hline 03 F 8 & 22 E 287 & & SHI...II & l.OCEIJF & -STORF 15T C.HAF AIOT \\
\hline 03 FB & C12.604 & & CALL & FILL & iFTLL. FOW WTTH SFACES \\
\hline \multirow[t]{2}{*}{03FE} & \multirow[t]{2}{*}{C9} & \multicolumn{3}{|l|}{RET} & \\
\hline & & ; SCRO & L Sul & ROIJT INE & \\
\hline O3FF & 2A11687 & SCROL: & LHLI & TOFAII & ¢TOF TO Hi \\
\hline 0402 & 22E2B7 & & SHLD & LOCEUF & \#STORE 1ST CHAR AOMTI \\
\hline 0405 & C12604 & & CALL & FILL. & ©FILIL ROW WITH SFACES \\
\hline 0408 & 2AD687 & & LHLI & TOPAD & ¢TOF TO HL. \\
\hline 040B & 71 & & MOV & A, L & \%LOW ETTE TO A \\
\hline 040C & FE80 & & CPI & 080H & -IS IT 8OH ? \\
\hline 040E & C21E04 & & JNZ & DUCK & ; IF NOT CONTINUE \\
\hline 0411 & 7C & & MOV & ArH & fHIGH EYTE TO A \\
\hline 0412 & FE87 & & CPI & 087H & ; IS IT 87H ? \\
\hline 0414 & C21E04 & & JNZ & пUCK & ¢IF NOT CONTTNUE \\
\hline 0417 & 210080 & & LXI & \(\mathrm{H}, 8000 \mathrm{H}\) & ¢IF 8087 TOF \(=8000 \mathrm{H}\) \\
\hline 041A & 221687 & & SHL.D & TOPALI & ¢STORE IN MEM \\
\hline 041 D & C9 & & RET & & \\
\hline 041 E & 115000 & DUCK: & LXI & D, 050H & ;801 T0 LE \\
\hline 0421 & 19 & & DAII & I & - Adİ bon to top \\
\hline 0422 & 221687 & & SHLD & TOPAII & istore IN MEM \\
\hline \multirow[t]{3}{*}{0425} & \multirow[t]{3}{*}{C9} & & RET & & \\
\hline & & ; & & & \\
\hline & & ; FILL & SURR & UTINE & \\
\hline 0426 & 2AE2B7 & FILL: & LHLD & LOCBUF & F1ST CHAR IN ROW \\
\hline 0429 & 115000 & & LXI & \(\mathrm{D}, \mathrm{O50H}\) & ¢LOAD 80D IN IJE \\
\hline 042 C & 19 & & IAD & 1 & FADD BOD TO IST CHAR \\
\hline 0420 & \(22 \mathrm{nc87}\) & & SHLII & L.0C80 & ;STORE IN MEM \\
\hline 0430 & 012020 & & LXI & \(\mathrm{H}, 2020 \mathrm{H}\) & OSPACES IN EC. \\
\hline 0433 & 210000 & & LXI & H, O & ; ZERO HL. \\
\hline 0436 & 39 & & DAD & SP & ;SP TO HL. \\
\hline 0437 & EB & & XCHG & & ;SP TO IDE \\
\hline 0438 & 2 ADC87 & & LHLD & L0C80 & ALOC OF LAST CHAR IN ; ROW TO HL \\
\hline 0438 & F9 & & SPHL & & - LAST CHAK LOC IN SF \\
\hline
\end{tabular}

SヨJ*dS HIIM 3MI7 7רI





\(256 \times 192\) graphics mode.

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All that computer for \(\$ 599\). The Imagination Machine, the personal computer from APF Electronics.
}

T
he Imagination Machine is more personal computer than you'd expect at \(\$ 599\).

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\section*{All that, plus user-programmability.}

We know sophisticated users aren't going to be satisfied forever using preprogrammed software. (Even though we offer a large library of educational, entertainment, home and business management programs.) So, we made The Imagination Machine user programmable, in both BASIC and MC6800 machine language. To simplity matters, we've just developed the first and only BASIC TUTOR course on cassette. With it, you can learn to program The Imagination Machine in BASIC, with hands-on training, right at the computer.

\section*{Some exceptional features.}

The Imagination Machine has several unique features that can help you use your time at the computer more effectively.

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Another special feature is The Imagination Machine's unique keyword system, which simplifies

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A third feature is Timed Response Monitoring, which automatically adjusts the computer's pace and level to your own. It makes "tutoring programs,' for instance, easier and more interesting to follow.
And then there are The Imagination Machine's three graphic display modes: 1. Alpha numerics, mixed with low-resolution graphics in as many as eight colors. 2. High resolution - up to eight colors \(-128 \times 192\) display. 3. High resolution graphics - up to four colors - with \(256 \times 192\) display.

\section*{And expandability.}

A personal computer that can't grow along with your growing requirements soon becomes obsolete. So, we designed The Imagination Machine to be expandable. By adding APF's optional "Expansion Box" and interface cartridges, you can hook up any compatible floppy disk or printer, or an additional 8K RAM memory cartridge.

\section*{full mini-floppy system \(\$ 995\).}


For small business and professional use, you may require a full mini-floppy
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\(\$ 199.95\)
BB-2. Expansion Box with floppy disk interface cartridge.
\(\$ 199.95\)
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RS232 cartridge.
\(\$ 99.95\)

Floppy-disk interface cartridge.
\$149.95
MIni-floppy Disk Drive.
\$399.95
\$599. Manufacturer's suggested retail price.



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Listing 2 continued from page 142:


\section*{COMPUTERS "Я" US MAILORDER DISCOUNTS}


APPLE II OR APPLE II PLUS

\section*{Appll Computan}

\section*{PGUNMITAL}

DISK II DRIVE \& CONTROLLER card . . . . 485 DISK II DRIVE ONLY GRAPHICS TABLET 425 ........ 655
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215 YYIEC LICHT PENSY SENEPATOR 215 SYMTEC SUPER SOUND GENERATOR. . 225 SVA 1 INCH DISK CONTROLLER CARD. 335 VERSA WRITER DIGITIZER SYSTEM. . . 215 VIDEX VIDEOTERM 80 COLUMN CARD . 315 VIDEX VIDEOTERM w/graphics ROM ... 335 LOBO DISK DRIVE ONLY................. . . 385 LOBO DRIVE wicontrolior card. .

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149 APPLESOFT II FIRMWARE CARd. . . . . . . . 149 INTEGER BASIC FIRMWARE card . . . . . . 140

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APPLE CLOCKICALENDER card . . . . . . . 225 SUPERTALKER SD200 SPEECH

SYNTHISIZER SYSTEM. \(\qquad\) 245
ROMPLUS wheyboard filter ................. 165
INTROLIX-10 BSA REMOTE CONTROL SYSTEM
-10 controler card only
ROMWRITER SYSTEM
ROMWRITER SYSTEM...................... 155
MUSIC SYSTEM (16 volcesistereo) ..... 485
A/DW/A 18 CHANNELS
EXPANSION CHASSIS (8 Blote) ............. 555

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(TRS-60, APPLE II, SORCERER) . . . . . . . 60 ABT NUMERIC INPUT KEYPAD
(epecify old or now kybrd).
... 115
(epecify old or now kybic SYNTHISIZER . . . . . . . . . . . . . . . . . 235
ALF MUSIC SYNTHISIZER.
.32
BRIGHTPENLIGHTPEN ........................ 32 GPITHMETIC PROCESSOR ............... 259 SPEECHLINK 2000 ( 84 Word Voceb.) . . . 215 MAR SUP-R-MOD TV MODULATOR ...... 25 MAR SUP-R-TERMINAL 80 column card . 335 MICROSOFT Z-80 SOFTCARD SYSTEM
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MICROWORKS DS-85 DIGISECTOR .... 339

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PASCAL with LANOUAGE SYSTEM .... 425 FORTRAN for UEE wIth LANGUAGE SYSTEM .............................. CPMA for use with MICROSOFT
Z-00 SOFTCARD (incl.).
.185
008 3.3..................................... . . 3
THE CONTROLLER General Busineas . . . 49 Syetem.................................... 519 THE CASHIER Retall Managoment \({ }^{\text {a }}\) Inventory Syetem APPLEWRITER Word Proc............. . . . 198 . POST MALINQ LIST Syetem. . . . . . 45 APPLEPLOT Graph \& Plot System........ 60 DOW JONES PORTFOLIO EVALUATOR . . 45 CONTRIBUTED VOLUMES 1 THRU 5 whanuals.. . . . . . . . . . .................. 30 VISH-CALC by PERSONAL SOFTWARE. . 120 DESKTOP/PLAN bY DESKTOP

COMPUTERS .
CCA DATA MANAGEMENT SYSTEM
By PERSONAL SOFTWARE. .............. 85
APPLEBUG ASSEMBLERIDISASSEMBLER75 APPLE DOS TOOLKIT. ........................... 65


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\section*{ATARI AccIssenins}

ATARI 400 COMPUTER . . . . . 438
ATARI 820 PRINTER . . . . . . . . 488
ATARI 810 DISK DRIVE ..... 575
ATARI 410 Program Recorder. 89 ATARI 16K RAM MODULE . . . 145
ASSEMBLER/EDITOR . . . . . . . . 45
STAR RAIDERS
40

S-100 EXPANSION UNIT ..... 375
WORD PROCESSING PAC. . ..... 179
DEVELOPMENT PAC ..... 89
PRINTERS
ANADEX DP8000. ..... 775
ANADEX DP9500 ..... 1350
BASE 2 ..... 599
CENTRONICS 737. ..... 825
MPI 88-T ..... 699
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NEC SPINWRITER ..... 975
TRENDCOM 200 ..... 519
SILENTYPE w/int ..... 515


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BATTLESHIP COMMANDER \({ }^{\text {w }}\) by Erik Kilk and Matthew Jew. A game of strategy. You and the computer each start out by positioning five ships of different sizes on a ten by ten grid. Then the shooting starts. Place your volleys skillfully - a combination of logic and luck are required to beat the computer. Cartoons show the ships sinking and announce the winner. Sound effects and flashing lights also add to the enjoyment of the game. Requires at least 32 K of RAM. Cassette: \(\$ 14.95\) Diskette: \(\$ 19.95\)



FASTGAMMON \({ }^{\text {e }}\) by Bob Christiansen. Sound, hi res color, and cartoons have helped maked this the most popular backgammon-playing game for the Apple II. But don't let these entertaining features foot you - FASTGAMMON plays serıous backgammon. Requires at least 24 K of RAM.
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Listing 2 continued from page 150 :
\begin{tabular}{|c|c|c|c|c|c|c|c|}
\hline L & 0005 & LOAII & 0623 & LOAEX & 0235 & LOC01 & 8714 \\
\hline L0C80 & 87DC & LOCAD & 8708 & L.OCEU & 87E2 & LOCPR & 87E0 \\
\hline LOCXX & 87 LE & LOOF & OORE & M & 0006 & MLIC57 & 0000 \\
\hline MDS57 & 0084 & MOVE & 0710 & NUF & 02IIA & NXT2 & 0055 * \\
\hline NXTA & 0185 & NXTCM & 0397 & NXTX & OOEI & NXTY & 00F9 \\
\hline NXTZ & 0105 & OUTAD & 065F & FC2SA & 0044 & PC2TC & 0045 \\
\hline PC3SA & 0046 & PC3TC & 0047 & FMLI57 & 0048 & P5W & 0006 \\
\hline RCTAD & 8703 & RDF51 & 0009 & ROWIN & 0310 & ROWUF' & 02FE \\
\hline RT75 & 0467 & SCROL & 03FF & SIN & 0677 & SOUT & 0689 \\
\hline SP & 0006 & S5S2 & 05E9 & START & 05F6 & T1 & 0626 \\
\hline TOPAD & 8706 & TROLL & 0297 & USCHR & 87E5 & U5Z & 0485 \\
\hline UAF & 0273 & WIZAR & 02CE & WP75 & 0332 & X7 & 0659 \\
\hline X8 & 0655 & XFLG & \(87 E 4\) & XIN & OGAE & XMIT & 0449 \\
\hline XOUT & 0656 & XSTAD & 03A3 & XX & 0703 & XY & 0698 \\
\hline 21 & 0601 & ZETA & 0185 & ZIN & 0605 & ZOUT & 06E6 \\
\hline
\end{tabular}


Photo 6: The complete terminal system with keyboard, monitor, power supply, and main circuitry.

Text continued from page 128 :
but you cannot get the monitor operating with the checkout terminal, then most likely your problem is in the 2114 programmable memories, the decoder circuitry for the 2114 s , or the 8212 buffers for the 2114 s . Other problems could be caused by the temporary interface or data rates that differ.

\section*{Using the Monitor}

After your built-in monitor is working, you can jump to it for use in debugging the remainder of the circuit. Opening the TRAP switch will cause the 8085 microprocessor to transfer control to the monitor. To return to the terminal-control software, the 8085 microprocessor is reset. To facilitate this, I have connected the BREAK switch on my keyboard to the 8085 RESET IN line (pin 36). This connection is also useful for resetting the video terminal just after it is turned on, or for easy
clearing of the screen. One of the most useful functions of the system monitor is its ability to load into memory and run short programs that will read the status registers of the peripheral circuits to determine whether or not they are operating properly. This includes the 8251 , 8257, 8275, and the 8279 integrated circuits.

The system monitor commands are as follows:

D (Dump): Type the letter \(D\) followed by two 4-digit hexadecimal numbers that represent addresses in the system. Memory contents between the two addresses will be printed on the checkout terminal in hexadecimal with 16 bytes on a line. The line will begin with the address of the first byte in that line. A dump can be aborted by pressing the ESC key.

F (Fill): To fill a block of memory with a specified value, type an \(F\) followed by two 4-digit hexadecimal addresses which are the inclusive
locations in memory to be filled. Lastly, type the 2 -digit hexadecimal number that the block of memory is to be filled with.

G (Go): Typing a G followed by a 4-digit hexadecimal address will transfer that address to the program counter, and program execution will continue from that location. After a short program has been loaded into memory, the Go command can transfer execution to this program.

L (Load): To load sequential memory locations with arbitrary values, type an \(L\), followed by a 4-digit hexadecimal address. The system will prompt the user with sequential addresses, after which the user can type in the desired contents in the form of 2 -digit hexadecimal numbers. You can exit from the load routine by typing any nonhexadecimal character.
M (Move): The Move command can write blocks of data from one memory location to another. After the M is typed, three 4 -digit hexadecimal addresses must be typed in. The first two addresses enclose the block of data in memory to be moved, and the third address is the beginning location of the area where the block of data is to be written.

Any time a character other than \(D, F, G, L\), or \(M\) is typed in response to the " \(?\) " prompt, the monitor will simply reissue the prompt character. When the appropriate response should be a hexadecimal character and another character is typed instead, the monitor will cancel the command and reissue the prompt character.
No carriage returns are necessary after typing in data to the system monitor. When the monitor has the correct amount of data it will execute the command.

\section*{Keyboard Assembly}

I used the sixty-three-key unencoded keyboard offered by Jameco Electronics, 1021 Howard Ave, San Carlos CA 94070. The cost was \(\$ 29.95\). This is a good-quality keyboard for the price. Each pair of switch contacts protrudes from the bottom of the keyboard by about an eighth of an inch, making it necessary to mount the unit on a printed-circuit board. Because of the complexity of the switch matrix, a complete printed-circuit layout would have to

Text continued on page 156


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Listing 3：A hexadecimal object dump of the video－terminal－ control routine．

C CRTBO
：03000000C34000FA
：03002400C3E90528
：1000400031FF8721FF7F2336207DFECFC246007C13 ：10005000FE87C24600210000221138722E28722D8F1 ：100060008722DAB722DC8722DE8722E08721008050 ：1000700022D6B721808722E6873E0032口2B732D57A ： \(100080008732 E 48732 E 5873 E 7 E[13013 E 2713013 E A A\) ： 100090003 FI3613E00D3513E4F［13503E58［I3503EE4 ：1000A00089п3503E59n3503EB0D3513E0OII3503E69
 ：1000C000E602C4D200NB51E620C46704CDA004C311I ：1000D000EE00CDD900CDE100C9DH00E67F32E58767 ： 1000 E 000 C 93 AE 487 E FFCAENOOCIIOB01C93AE587EE ：1000F000E660CAF900C14103C93AE587E610C205RA ：1001000001CD2301C921E4873601C93E0032E487CD ：100110003AE587E60F0721C1041100005F195E234II ： 1001200056 EBE 93 AE 587 E 60621 E 1041100005 F 1984 \(: 100130005 E 2356 E E E 92 A 13877\) IIFEOOCA4201CIIFE3TI ：1001400002C97CFE00CA4C01CDFE02C921800722F： ： \(10015000 \mathrm{H} 3873 \mathrm{E} 18320587 \mathrm{Cn} 3203 \mathrm{CG2ALB} 377 \mathrm{IFF} 97\) ： \(1001600080 \mathrm{CA} 6801 \mathrm{CH1003C97CFE07CA7201C[1098}\) ： 1001700003 C9CD3203CDFF03C93AN287FE4FCAR5EA ：1001800001CD2A03C92AD3877DFE80C294017CFE5F ：1001900007CA9L013E0032L287CII003C93E003？OE ： 1001 A000П287C．13203CDFF03C．93AN287FEOOC．AB54C ： \(1001 \mathrm{B00001CL2203C92AL38771FEOOC2C4017CFEB3}\) ：1001C00000CACDO13E4F32H287CTUFE02C921800741 ： 1001 100022 \(103873 E 4 F 32 \mathrm{H} 2873 \mathrm{E} 1832 \mathrm{H} 587 \mathrm{CH} 3203 \mathrm{~A} 5\) ： 1001 E 000 C 921000022 I 3873 E 0032 D 29732 I 587 C 185
 ： \(10020000 \mathrm{BCD20A02CD2002C31602C216023ECFBIEG}\) ： 10021000 H 21602 CD 20022 ADE 8722 E 2 B 7 CH 2604 C 92 FF ： 100220002 AIIE871130F81922DEF87C93EF00619113F ： \(1002300050002100807719051: 235022100002211329\) \(: 100240008721008022 \mathrm{I} 68721808722 \mathrm{E} 6873 \mathrm{E} 0032 \mathrm{EG}\)
 ： 100260002 A 13871922 E 0873 E 87 ECN 9302 CLF 402 EH ：10027000C37F02C．27F023ECFBIIU27F02CNF4022AFTI ： 10028000 П6877［IFE00C297027CFF80C29702218045 ： \(100290008722 \mathrm{E} 687 \mathrm{C} 3 \mathrm{~A} 10211 \mathrm{BOFF} 2 \mathrm{ALI6E} 71922 \mathrm{E} 67\) A ：1002A000873EF 02AE087777IFE80C2CEO27CFE870か ：1002B000C2CB02ER2AE6877חВНС2С2027CBAC2C2B5 ： 1002 C 00002 C 92100802 ZE 087 C 3 A 102 EE 2 AF 6 B 77 AT 4 ：10020000FBC2DA027CEAC2TAO2C92150001922E09C ：1002E00087C3A1022AE．0871130F81922F087C．C．329 ：1002F0005B013E0032I287CH3203C9C3A9012A［I3A4 ： 100300008711 BOFF 1922 L 38721158735 CH 1203 C 994 ： 10031.0002 An3871．150001922［138721058734C［3253 ： \(1003200003 C 921\)［128735Cn3203C921 II28734CTIS2rIA ： \(100.3300003 C 93 E B O[13513 A L 28713503 A[5871350 A 0\) ：10034000C93AD287FE4FCA5003C17403C10B103C959 ： 100350002 AII， 3877 DFE8OCA 6003 CI 7403 CNB903C95B ：100360007CFE07CA6DO3CD7403C币F903C9C币7403F8
 ：100380002100003AL2876F1922II8873E87RC0297C6
：1003900003CLDCO3C3A303C2A3033ECFBTI2A3039B ：1003A000CDNC03CDF10321E5877EEG3F2ALI88777B0
 ： \(1003 \mathrm{COOO871150001922H38721158734C132030934}\) ：1003D0003E0032以287CH3203CIFF03C92AL188711．20 ： \(1003 \mathrm{E} 00030 \mathrm{~F} 81.922 \mathrm{LB} 872 \mathrm{ALAB71.30F8192?OAB7EF}\) ： \(1003 F 000 \mathrm{C} 92 \mathrm{AIIAB77FFEFOCO22F287002604C92AOB}\)
 ：10041000047CFER7C21E04210080220687C91I50A9
 ：1004300001202021000039EB2ALIC87F 9C5C5C5C59C ： \(1004400005 \mathrm{CSC5C5C5C5C5C5C5C5} 5 \mathrm{C} 50505 \mathrm{CSC5SC}\) ： \(1004500005 \mathrm{CSC5C5C505050505} 505050505 \mathrm{C} 50540\) ： 10046000 C 5 C 5 C 5 C 5 EFF 9 C 93 E 00 H 3482 ATI687711139E ： 10047000447 CIU3447TI2F6F7C2F672311CFG71911C4 ： \(100480000080197 \mathrm{LO} 3457 C 13452100807 \mathrm{HE} 3467 \mathrm{CF} 7\) ： 10049000 n34621CF877LIH3477CH3473E84II348C9F9 ：1004A000HB61E607CBCLAA904C9 LIF60EECO2LE 00421 ：1004B0001100005F 1901FO1F601CAE以O47ET1300C．95 ：1004C000C9C00435015B017901A9012B02C004C038 ；1004D00004E101C0045CO2F301C004C004C004COIA ： 1004 E 00004 FEO RFF02F202C0041F31323334353 i．？ ： 1004 F000000951574552545900004153444647485н ： \(1005000000005 A 584356424 \mathrm{~F} 20000002 \mathrm{~F} 2 \mathrm{ELC4T1}\) н
 ：100520007F5C3T2T130393837081821402324255E60 ： 1005300000095157455254590000415344464748 j 9 \(: 1005400000005 A 5843564\) 24E200000003F3E3C4IIAA ： \(10055000000 \mathrm{II} 7 \mathrm{D} 223 \mathrm{~A} 4 \mathrm{C4E4AOOOA}\) C5 1504 F 4955 F 4 ：100560007F7E RH5C29282A26081B0000000000゙ひ04： ： 100570000009111705121419000001130406070809 ：1005800000001A180306020E200000000000000LIF ： \(100590000000000000 \mathrm{COFOAOOOA1C1HLOOFO915AF}\) ： \(1005 \mathrm{SOOO7F} 1 \mathrm{CO} 000000000000800000000000000 \mathrm{OAB}\) ：10058000000000000000000000000000000000003F
 ：100551000000000000000000000000000000000001 B ： \(1005 E 000000000000000000003 \mathrm{E} 40 \mathrm{H} 3013 \mathrm{FFFII} \mathrm{BAL}\) ： \(1005 \mathrm{~F} 000013 \mathrm{E} 27 \mathrm{~L} 301 \mathrm{~F} 331 \mathrm{FFB7CLA} 3063 \mathrm{E} 3 \mathrm{FCLG9GF}\) ： 1006000006 CL17706E67FFE 4CCA \(2306 F E 44 C C 3606 A F ~\) ：10061000FE46CCF 306FE4［ICC1007FE47C2F SOSCDLI

 ：100640000FC24706CNSF063E20C089067ETUFらOからず
 ；10066000A3067CCDE6067IICTE 606C9CNA．306CTUFOG ： \(100670000667 \mathrm{CDD5066FC9LHO1E602CA77065H0047}\) ： 10068000 FE 1 RCAF 605CD8906C9F5NEO1E602CA9R4C． ： \(10069000060 \mathrm{HOOFE} 1 \mathrm{HC}, \mathrm{AF}\) 605IHOIE KO1 CA9BO6F 17 FF ：1006A000П300C93EOLICLIB90ん3EOAC II890AC9CII775 ： 1006 F00006FE3OLIAF 605FE3ATIBFE4 1 TIAF \(605 F E 47 \mathrm{CB}\) \＄1006C00002F605C609C9E60FC630FE3ALALIDGC62E ： \(1006000007 \mathrm{CHB906C9CTIAFO6OFOFOFOFFGFO47C[147}\) \＄1006E000AEO6E6OFBOC．OFSOFOFOFOFCIIC606F 1 CN90
 ： \(10070000 \mathrm{D} 064770237 \mathrm{CBAC203077} \mathrm{IIREC20307C965}\) ： \(100710001603 C[6 B 06 E 515 C 21207 E 1 C 103 \Pi 11\) A77A6 ；01107200013237AB8C21E077BE9C21E07C999 ：0000000000

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\section*{CEMTRDNILS*PRIMTERS the advantage}

Text continued from page 152:
be double sided and include platedthrough holes. Since producing this type of printed-circuit board is beyond the capabilities of most amateur builders (including myself), I opted for a single-sided board with additional wire-wrap pins and connections to complete the wiring. The wiring diagram of the switch matrix is shown in figure 5, and an illustration of the printed-circuit layout is given in figure 6. A 24-pin wire-wrap socket
was mounted at the top of the printed-circuit board and serves as a plug for the interconnecting cable. The cable is a 36 -inch long DIP jumper with a 24 -pin plug on each end. The Vector board also has a 24-pin wire-wrap socket to mate with the cable.

\section*{Install and Check Out the Video Circuitry}

The remaining half of the components can be installed at this point.

Check the video-dot-timing circuitry thoroughly to be sure that the correct frequencies are being generated at particular points in the circuit. After resetting the 8085 microprocessor, make sure that the 8224 is oscillating at 22.68 MHz . Pin 5 of IC15 (the 7474) should show the dot rate of 11.34 MHz as well as pin 2 of IC21 (the 74163) and pin 7 of IC22 (the 74166). You should measure a frequency of 1.620 MHz , which is the Text continued on page 160


Figure 5: Schematic diagram showing detail of the keyboard matrix. A sixty-threa-key unencoded keyboard from Jameco Electronics was used. The BREAK key is connected to the RESET IN line of the 8085 processor.

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- New editing commands that allow program lines to be deleted from one location and moved to another or to allow the duplication of a program line with the deletion of the original.
- Enhanced and improved RENUMBER that allows relocation of subroutines.
- Powerful chaining commands.
- Print Spooler
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As with 2.1, NEWDOS/80 relies on the TRSDOS and Disk Basic Reference Manual published by Radio Shack. NEWDOS/80 documentation supports its enhancements and upgrades only.

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Figure 6: The printed-circuit board layout for the keyboard matrix, shown here reduced to \(82 \%\) of actual size. Use of a single-sided board makes some additional wire-wrap connections necessary.


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Text continued from page 156:
character clock rate, on pins 6 and 8 of IC14 (the 7410), pin 12 of IC21 (the 74163), on pin 9 of IC23 thru IC27 (all five 74175s), pin 1 of IC21 (the 74163), pin 15 of IC22 (the 74166), and pin 30 of IC9 (the 8275). Pin 7 of the 8275 should measure \(16,200 \mathrm{~Hz}\), the horizontal line frequency, and pin 8 should be at 60 Hz , the frame frequency. Do not proceed until you can measure all of these frequencies correctly. If your display shows something quite distorted, torn, or scrambled, it is probably a problem in the video timing. An incorrect horizontal or vertical sync frequency can greatly disrupt a display.

\section*{Final Checkout}

At this point, your terminal should be working. If it is not, double-check the following:
- On opening the TRAP switch, does the 8085 microprocessor branch to the monitor program and issue a carriage return, line feed, and question mark from the 8251?
- Are all of the frequencies listed above for the video timing correct in your circuit?
- Check the output of pin 35 of the 8275. This is the video-suppression (VSP) output which is active high during horizontal and vertical retrace at the top and bottom rows of every character, and in certain other cases involving end-of-row or end-of-series codes. Video suppression is also turned on if a direct-memory-access underrun occurs. If video-suppression is producing a logical 1 and has no activity on it, a direct-memoryaccess underrun is most likely your problem. This means that the software is not reinitializing the 8257 at the end of each video frame. The video-suppression line should show a frequency of 12 kHz on it. Pin 37 of the 8275 (the light-enable output) will have a frequency varying from 28 to 32 Hz .
- After the 8085 microprocessor has been reset and before data is sent to the video terminal, IC18 (the 74LS138 peripheral decoder) should be putting out pulses at constant rates. Pins 9,10 , and 15 should show a frequency of about 23 kHz , and pin 11 should show

\section*{600 Hz .}
- The address-enable line on the 8257 (pin 9) should show a frequency of 1.5 kHz , and the address strobe (pin 8) should be 135 kHz . Again, these frequencies should be measured by a counter using a full 1 -second gate time, since the duty cycles of pulses of these lines are not constant. This is especially true of the addressstrobe output of the 8257 .
Using a frequency counter and an oscilloscope to check for the correct activity on the various pins of integrated circuits is an effective method of troubleshooting your circuit. It is possible that a single wiring mistake is your only problem. Using an ohmmeter as a continuity tester and checking every connection is often worth the effort. I turn the circuit board over and put the ohmmeter probes on the pins of the integrated circuits themselves. This also serves to check for a bad socket connection. Draw over the connecting lines on your progress-checking schematic with a different colored pen as you make each check.

\section*{Possible Additions}

Some readers may wish to make further modifications to my design. Here are some possibilities:
- Lowercase letters could be added fairly easily if the 7 by 10 format for each character is retained. The +5 V 2513 character generator is also available with a lowercase set of letters. The second character generator could be added by using the full 7-bit ASCII code in memory. Only six bits are stored in memory in this design. The most significant bit could be used to select which character generator would be enabled. The characterhandling routine in the terminal control software would also have to be modified. If a larger format for characters was desired (eg: that used by the Motorola 6571 character generator), the entire dot timing would have to be changed, as well as the initialization of the 8275 in the software.
- The 8275 Video Display Controller has provisions for light-pen detection. Very little hardware would be needed to add this feature; only a small switch and a small light-
sensor circuit using a phototransistor. When the raster sweep reaches the light sensor, it presents a signal to the light pen (LPEN) input, and the row and character positions are stored in a pair of registers in the 8275 . These registers can be read on command. Modification of the control software would be necessary to read the registers and act upon their contents.
- Character- and field-attribute codes can also be handled by the 8275. Character-attribute codes are used to generate graphics symbols without the use of the character generator. These symbols can also be programmed to blink or be individually highlighted. Field attributes are codes that affect the characteristics of a field of characters. These characteristics are blink, highlight, reverse video, underline, and two general-purpose outputs that can be user defined. The Intel Peripheral Design Handbook gives details on implementing these features in both hardware and software.

\section*{Conclusion}

This terminal is not a suitable project for a beginner or for those who are inexperienced in microprocessor hardware. Time and patience will be indispensable in completing this project. I spent about three months assembling the parts and building the circuit. A month of this time involved debugging both hardware and software, due to the many changes I made in the original Intel design.

I would appreciate hearing from those readers who complete this project. Descriptions of any modifications made would also be welcome. \(\quad\)

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 single board at a cost of under \(\$ 300\). The Superboard II reçeived rave reviews by microcomputer experts such as:
"We can heartily recommend the Superboard II computer system for the beginner who wants to get into microcomputers with a minimum of cost. Moreover, this is a 'real' computer with full expandability.''

POPULAR ELECTRONICS MARCH, 1979

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KILOBAUD MICROCOMPUTING FEBRUARY, 1979
"The Superboard II and its fully dressed companion the Challenger \(1 P\) series incorporate all the fundamental necessities of a personal computer at a very attractive price. With the expansion capabilities provided, this series becomes a very formidable competitor in the home computer area."

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"The Superboard is an excellent choice for the personal computer enthusiast on a budget.'

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Since the introduction of Superboard II, the cost of personal computers has actually gone up with new models by major manufacturers ranging from \(\$ 1000\) to well over \(\$ 4000\) due to the general cost of inflation and the increasing functionality included in these computers. Today Cleveland Consumer Computers is offering you the original Superboard II at its original price of just \(\$ 279\). In today's economy this is by far the best buy
in personal computing ever!
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powerful scientific calculator, made possible by its advanced scientific math functions and built-in "immediate" mode which allows complex problem solving without programming! This computer can actually entertain your children while it educates them in topics ranging from naming the Presidents of the United States to tutoring trigonometry - all possible by its fast extended BASIC, graphics and data storage ability.
The machine can be economically expanded to assist in your business, remotely control your home, communicate with other computers and perform many other tasks via the broadest line of expansion accessories in the microcomputer industry.
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The Superboard II comes fully assembled and tested. It requires +5 V at 3 Amps and a video monitor or TV with RF converter to be up and running.
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\section*{Standard Features:}
- Uses the ultra powerful 6502 Microprocessor.
- 8K Microsoft BASIC-in-ROM. Full feature BASIC runs faster than currently available personal computers and all 8080 based business computers.
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onal Computing is Still Here.
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\section*{R adio Shack's New} Products: This fall, Radio Shack will offer a \(\$ 399\) terminal/modem combination called the Videotex. This product will be billed as "the world's first low-cost home/office two-way infor-mation-retrieval system," and will allow a user to access CompuServe's MicroNet information utility and similar services.

The Videotex will connect directly to a telephone line and to the antenna terminals of a standard television set (not supplied).

A \(\$ 30\) software package will be required for a TRS-80 Model I to use the MicroNet system. In a radical departure from its past marketing policy, Radio Shack will also sell versions of the access software for non-TRS-80 computer systems such as the Apple II computer.

The MicroNet service will be accessible from 235 sites in the United States, providing news, syndicated columns, and sports, as well as access to creditcard verification and limited banking services.

Observers of the microcomputer industry have been expecting an announcement of three new Radio Shack computer products at any time now. A replacement for the TRS-80 Model I is due, and anticipation of more advanced systems is mounting.

\footnotetext{
Sharp To Introduce Under-\$125 Computer:
Sharp Corporation, of Japan, plans to introduce in 1981 an under- \(\$ 125\) handheld computer, which is
}
programmable in BASIC. It will store up to 400 program steps and have twenty-six memory locations for data storage. It will have an alphanumeric keyboard and a one-line LCD (liquid-crystal display). Optional printer and cassette interfaces will also be offered. Sharp is presently marketing a similar, but more powerful, machine in Japan, for \$175.

> I apanese Show Personal Computers in US: Several Japanese companies showed personal-computer systems at the recent National Computer Conference (NCC) in Anaheim, California. Nippon Electric Company (NEC) displayed a Z80-based system that currently sells for \(\$ 730\) in Japan. It includes a 12 -inch color monitor, up to 64 K bytes of programmable and read-only memory and uses Microsoft BASIC.

> Casio presented a system with \(4 \frac{1}{2}\)-inch video display and 4 K bytes of main memory, expandable to 32 K. SDC International Corporation said it is preparing to market an S-100-based system.

\section*{S8000, Where Art Thou?}

Two computer-system manufacturers have reported to me that they are in a "holding" position on 68000 -based 16 -bit microcomputer-system development. They claim that Motorola has still not clearly defined some of the operation codes and will not commit to delivery on anything other than sample
quantities. These manufacturers contend that similar problems occurred with the 6809 microprocessor. At this point, it does not appear likely that any 68000 products will become available this year.

\section*{W anted: One And A Half Million Program-} mers: "There could be a demand for over one million computer programmers by 1990," said Andrew S Grove, Intel's president, in a recent interview. Datamation magazine has gone even further. In a recent article it reported that new software breakthroughs will cause the number of software programmers to increase \(10 \%\) per year from 563,000 in 1980 to 1.5 million in 1990.

\section*{J} apanese Memories Superior? According to a report made by Richard W Anderson, manager of Hewlett-Packard's Data Systems Division, Japanese 16 K memory devices are superior to US-made devices. According to Anderson, Japanese 16 K components showed a zero failure rate on incoming inspection compared to a 0.11 to \(0.19 \%\) rate on USmade devices (ie: 100 failures out of 50,000 ). Further, field failures for 1000 hours of operation were 0.010 to \(0.019 \%\) for Japanese parts versus 0.059 to \(0.267 \%\) for US-made parts.
orld Computer Chess Champlonship:The third
world computer chess championship is scheduled to take place this month in Linz, Austria, from
September 25 thru 29.
The former world champion program, Kaissa (from the Moscow Institute of System Studies), will provide strong competition for the best programs from the West. The current World and North American champion, Chess 4.9 (written by David Slate and Larry Atkin) will defend its title alongside other entries from the United States such as Belle, Chaos, and Duchess. The current European champion, the program Master, is also expected to compete.

As in previous tournaments, David Levy will be the Tournament Director. Mr Levy is an International Master of chess and has been noted for his own play versus computer programs.

Where Com I Store Ten Gigabits? Optical disks are expected to be the next major advance in highdensity mass storage. Capacities of \(10,000,000,000\) bits ( 10 gigabits) are expected by 1982, \(10^{12}\) bits (l terabit) by 1985 , and \(10^{14}\) (100 terabits) by 1989. Videodisk technology is also advancing rapidly, but one shortcoming is that video disks are not erasable, limiting them to archival storage. Some systems now being designed are said to offer 10 billion bytes of storage on a 12 -inch disk with 250 ms access time.

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Inside Calif.
Outside Cont. USA


TERMINALS
\begin{tabular}{|c|c|}
\hline Televideo 912 & \$745 \\
\hline Televideo 920 & 795 \\
\hline Soroc IQ-120 & 700 \\
\hline Zenith A-19 & 850 \\
\hline Hazeltine 1410 & 750 \\
\hline Hazeltine 1420 & 830 \\
\hline Hazeltine 1500 & 950 \\
\hline Hazeltine 1510 & 1050 \\
\hline
\end{tabular}

\begin{tabular}{|c|c|c|}
\hline BOARDS & KIT & ASM \\
\hline CB2 280 CPU & \$168 & \$220 \\
\hline VB1B VIDEO & 125 & 170 \\
\hline VB2 VIDEO & 135 & 188 \\
\hline VB3 VIDEO & 300 & 347 \\
\hline 104 INTERFACE & 135 & 188 \\
\hline SBISYNTHESIZER & 161 & 227 \\
\hline \multicolumn{3}{|l|}{MEASUREMENT SYSTEMS MEMORY} \\
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\hline DM6400 64K 4MHz & DYNAMIC & . 640 \\
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Bubble Memory Update:
The first bubble-memory components were introduced in 1977 by Texas Instruments and Rockwell International. The number of bubble-memory suppliers has now increased substantially and includes Intel, Fujitsu, National Semiconductor, and Hitachi Furthermore, Motorola and Siemens are secondsourcing the Rockwell device. It is likely that several other semiconductor makers will also enter the market.

Intel was the first to introduce a l-megabit bubble-memory device, last year. Texas Instruments followed a few months later with its 1-megabit unit, and Rockwell is expected to announce its unit shortly.

Further, several manufacturers are also supplying support integrated circuits for simpler construction of the bubblememory controller.

At this time, the major problem to acceptance of these devices is the lack of standardization. The available devices and support circuits from different manufacturers are not compatible. A Joint Electron Device Engineering Council (JEDEC) committee is currently holding discussions toward establishing standards on device design, reliability, testing, interfacing, and terminology. There still is no agreement as to whether the standard should apply to the device or to the controller level. Hence, it seems that a bubblememory standard is still some time off, and we are unlikely to see bubble memory in wide use for some time to come.

\section*{K Viewdack: Far hundred} Viewdata: One hundred Kentucky farmers are trying out a Viewdata-type service to get information on markets, local crop conditions, and weather. The service is called the
"Green Thumb
Agricultural Weather

Marketing Project." Using a box attached to a television set and phone line, a farmer can request information from the State's HP-3000 time-sharing computer, by means of a menuoriented prompting system augmented by local county Z80-based computer systems. Up to eight items may be requested per telephone call. Currently one hundred farmers are testing the units made by Motorola in cooperation with Radio Shack.

\section*{Perox, DEC. And Intel} Join Forces For Office Network: Xerox, Digital Equipment Corporation, and Intel have joined forces in an effort to create a new internal data-communications network for business offices. Called Ethernet, it is intended for large or complex business offices. It will link together different types and makes of automated office machines (eg: terminals, intelligent copiers, word processors, etc) into a single system. Xerox holds the basic patents and will license others to manufacture compatible Ethernet products. A prototype system with several hundred machines is reported to have been operating for five years.

\section*{Targe-Size Flat Display} Technique Announced: RCA Laboratories, one of the leaders in display technology, has disclosed a new technical concept for building a wall-mounted 50 -inch (diagonalmeasure), color, flat-panel television display. A paper presented at the recent annual Society of Information Display conference estimated that the display could be in production by 1990. The display would consist of forty l-inch-wide by 30 -inch-high modules fastened together, side by side, to form a display 40 inches wide by 30 inches high. Each module would contain an electron gun and beam-guide system.

0thello Tournament Results: The best human player of the game Othello can still beat the best Othello-playing computer programs. This we conclude from the results of the First International ManMachine Othello Tournament, held on June 19, 1980, on the campus of Northwestern University in Evanston, Illinois. Six of the best computer programs and the top two human players participated in a seven-round roundrobin tournament. Mr Hiroshi Inoue, the current world champion from Tokyo, Japan, defeated five of the programs and the other human entry, Mr Jonathan Cerf of New York, New York, to win the tournament. Mr Cerf is the United States' Othello champion and is considered to be second-best in the world, although he placed third in this tournament.

The second-place finish was obtained by the computer program written by Dan and Kathe Spracklen of San Diego, California, who are well known for their chess-playing program, Sargon. The Spracklens' program defeated Cerf in the fourth round of the tournament; this defeat was somewhat ironic because Mr Cerf had given the Spracklens help in refining their gameplaying algorithms.

Mr Inoue was narrowly defeated by only one opponent, a program called "The Moor" written by David Levy, Michael Stean, and Michael Reeve, all of London, England. This defeat, like the defeat of Cerf by the Spracklens' program, took place in the fourth round. Since the fourth round took place immediately after lunch, many observers have speculated that digestive factors may have impaired the performance of the human players. Oddly enough, The Moor was soundly beaten by programs which were themselves soundly beaten
by Mr Inoue.
Fourth place in the final standings went to the program Odin, written by Peter Frey of Northwestern University. Fifth place was occupied by the program Iago, written by Paul Rosenbloom of CarnegieMellon University, followed by The Moor in sixth place. Peter Nachtwey, a US naval officer stationed in Newfoundland, Canada, entered his program Reversi Master which ended up in seventh place. Last place was occupied by a program written by Tom Truscott and Dennis Rockwell of Duke University.

Look for a full report on this tournament in a future issue of BYTE. (The name Othello is a trademark of Gabriel Industries, a subsidiary of CBS, Inc.)

\section*{AMSAT-OSCAR Phase III Satellite Crashes:}

When the first stage of the French Arriane rocket exploded during launch on Friday, May 23, 1980, the OSCAR Phase III satellite was lost. The spacecraft had an equivalent value of \(\$ 250,000\) and had required thirty man-years of effort for design and construction. The launch was not insured, so the Radio Amateur Satellite Corporation (AMSAT) has had to absorb a major loss.

The Phase III spacecraft appeared on the cover of the November 1978 BYTE and was discussed in Joe Kasser's article "The Sky's the Limit: Use Ham Radio Bands for Intercomputer Communication" (November 1978 BYTE, page 48). Part of the planned use of the satellite was to have been relaying of computer data by amateur radio operators in personal computer networks.

AMSAT is determined to build a second spacecraft (Phase III-B) to replace the lost unit, but the new satellite may take two years to complete. Fortunately, some material was left over from the original construction and may be used now.

\section*{The Empire has expanded!}


\section*{New Mainframe opens more areas for development}

In one quantum leap Tarbell has expanded its popular Empire (the vertical disk subsystem) into a full line. This entire series now encompasses 5 variations. Each one contains different components so the S-100 system designer, hobbyist, or serious business user can arrive at the exact custom state he wants and needs.
The basic Empire still includestwo Shugart or Siemens 8" disk drives: the compact cabinet with fan and power supply: a Tarbell floppy disk interface; CP/M*; Tarbell BASIC; the necessary cables, connectors and complete documentation. Naturally, it's fully assembled and Tarbeil tested.
The new, top of the line Empire contains the basic model's components with the Tarbell designapproved Mainframe. Beside the 8 -siot S-100 motherboard with an active terminated bus, there's a cardcage with card guides and a double-density interface.

\section*{You're the master of your Empire}

You can call the shots in the Empire. Tarbell's made sure of that by offering them as complete subsystem packages . . . or, as separate units. For example, the mainframe may be ordered with 1, 2 or no drives. Whichever way you go, however, you always get the reliability of Tarbell tested components and leadershipengineering.
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AMSAT is continuing to develop software to be used by ground stations in the satellite networks and is seeking support from personal computer users in this software-development effort and in other areas of the rebuilding program. Information on AMSAT and its programs may be found in Orbit, which is published every two months and received by all members of the AMSAT group. A year's membership may be obtained for \(\$ 10\) from
AMSAT, POB 27. Washington DC 20044.
The AMSAT space program is not a complete loss, however. The Phase II OSCAR-8 satellite continues in orbit, and a group of radio amateurs from the University of Surrey in England will launch the scientific-research satellite UOSAT in late 1981. Carrying a coherent highfrequency beacon, a magnetometer, and a slow-scan television camera, the "bird" will provide opportunity for ham radio and personal-computer users to gain experience in tracking satellites and monitoring telemetry.

Random Bits: It is interesting to note that IBM, via its Science Research Associates subsidiary, is marketing the Atari personal computer to educational users. In fact, IBM is offering a special sale. If you buy one Atari Model 800 system, they will give you an Atari Model 400 system free....Avalon-Hill, well known in the war gaming field for its historical simulation board games, has introduced a line of microcomputer-assisted games for the TRS-80, Apple II, and Commodore PET....The sales of the Texas Instruments (TI) Model 99/4 personal computer have been so disappointing that in the Los Angeles area TI has started offering \(\$ 100\) worth of free software plus a \(\$ 100\) cash rebate....Apple Computer Company has shifted its

Apple II production from Silicon Valley to Carrolton, Texas, a mere 30 miles away from the new 100,000-square-foot plant Tandy has built to make TRS-80s....A record 82,000 people attended the Na tional Computer Conference (NCC), in Anaheim, California, this past May. The NCC is the largest computer show in the world. When it was held in Anaheim two years ago, 55,000 attended, which set the record just smashed....Data General has begun selling its business-oriented microcomputer systems through independent computer stores nationwide....Fujitsu America Inc, Lake Bluff, Illinois, has announced a plug-in "Bubble Memory Cassette." It provides a portable, detachable, read/write block of 64 K bits. Fujitsu has also introduced a new fully-formed-character printer with speeds up to 80 cps (characters per second), nearly twice the speed of conventional daisy-wheel machines. The printer is currently offered as a \(\$ 4500\) option to a word-processor system....Texas Instruments is now making the voicesynthesizer components used in the Speak \& Spell and talking Language Translator available separately at \(\$ 13\) in OEM (original equipment manufacturer) quantity....Shugart Technology, BASF, Control Data, and Erwin International, Ann Arbor, Michigan, are all expected to have 5-inch Winchester hard-disk drives available by the year's end....Commodore will be the first US manufacturer to use the new low-cost Shugart/Matsushita 5 -inch floppy-disk drive....Zilog and Mostek have both announced that 6 MHz versions of the Z 80 microprocessor will be available in production quantities next year.
handom Rumors: It is rumored that Commodore
will soon introduce two lowend personal-computer systems. One will be a black-and-white unit for under \(\$ 500\) and the other a color unit for under \(\$ 800\)....Apple may be working on a low-end consumer computer that will compete with Mattel's Intellivision....Personal Software, Sunnyvale, California, the folks who brought out Microchess and VisiCalc (probably the two largest-selling personalcomputer software packages to date) are rumored about to release VisiText, a superpowerful text editor with features never before seen....NEC (Nippon Electric Corporation) is rumored to be investigating selling its Model PC-8000 microcomputer here in the US, after selling it in Japan for some time.

\section*{I \\ BM Demonstrates Continuous Voice} Recognition: IBM research scientists, at the Thomas J Watson Research Center in Yorktown Heights, New York, have demonstrated that continuous speech can be recognized by a computer with an accuracy of \(91 \%\). In continuous speech there are no pauses between words. In the IBM experiment, the computer transcribed normal-speed speech into printed form. The program took 100 minutes to display or type a transcript of a 30 -second sentence. In other words, it has a 200:1 response-time ratio. The experiment proves that continuous speech recognition by computers is possible.

\section*{U CSD Pascal} Controversy Continues: Several former University of California, San Diego (UCSD) Pascal licensees are threatening to file suit against UCSD and its new exclusive licensee, SofTech Microsystems. The licensees charge that UCSD violated the "fair use doctrine" in arbitrarily cancelling their licenses
only a short time before the software would have entered the public domain.

About thirty organizations, mostly computer hobbyist clubs, paid \(\$ 200\) to \(\$ 300\) for a UCSD Pascal license that permitted distribution of the software to their members and, after two years, would have placed no restrictions on copying the software.

These licensees are also upset over what they charge to be software developed with public funds now being sold by a private organization. SofTech counters this charge by asserting that it is merely an agent of the university and that it intends to spend as much money on developing UCSD Pascal as did the university.

One UCSD Pascal purchaser had an uncancellable license: Apple Computer Company. Its license, however, is restricted exclusively to use of the software on Apple Computer systems.

Terminal Gets Voice Input: Heuristics Inc of Sunnyvale, California, has introduced a speechrecognition system which works with a Lear Seigler ADM-3A video terminal. The unit, called VOCON 5000, recognizes 64 words or phrases that can control a program being run on the computer. A \(99 \%\) recognition rate is claimed for the unit, which sells for \(\$ 2000\).

MAIL: I receive a large number of letters each month as a result of this column. If you wish a response, please include a stamped, selfaddressed envelope.

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\section*{LECTURE PROGRAM}

A schedule of free lectures is available to all visitors. Lectures run about 50 minutes each, including, in most cases, some time for questions from the floor. Some topics are given twice, and, in some cases, topics of related interest are given on the same day for the visitor's convenience. (Program is subject to change without notice, but lectures will be posted daily in the show lobby.)

\section*{THURSDAY, OCTOBER 30}
\(\left.\begin{array}{clc}\text { Noon } & \begin{array}{l}\text { Introduction to Small Systems for } \\ \text { Business, Stan Veit, Associated Computer }\end{array} & 1 \text { p.m. } \\ \text { Industries } \\ \text { Mailing Lists: Several Directions, }\end{array}\right)\)

4 p.m. Using FORTRAN on a Microcomputer, Richard A. Zeitlin
4 p.m. Investment Analysis of Stocks and Commodities on a Microcomputer, Fred Cohen, Shearson Loeb Rhoades, Inc.

\section*{FRIDAY, OCTOBER 31}

Noon Introduction to Small Systems for Business, Stan Veit, Associated Computer Industries
BASIC Programming, Michael Mulcahey, Worcester Stage College
Selecting a Small Computer for Business, David Benevy, Computer Mart of New Jersey
Videoprints: Full-Color, Low-Cost, HardCopy Computer Graphics, Warren Sullivan, Image Resource Corp.
Mailing Lists: Several Directions, Dr. Norman I. Agin, Mathtech, Inc. Business Applications Software Development via Data Base Management, Dr. Andrew Whinston, Micro Data Base Systems
Application of PASCAL to Small Systems for Business, Panel, Stan Veit, Moderator, Associated Computer Systems


\section*{A NEW WORLD OF SMALL COMPUTERS IS AT YOUR FINGERTIPS THIS FALL}

\begin{abstract}
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\end{abstract}

Show ever produced.
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Registration fee is only \(\$ 10\) per day, and all registrants have free access to the hourly lectures.
3 p．m． Investment Analysis of Stocks andCommodities on a Microcomputer，Fred Cohen，Shearson Loeb Rhoades，Inc．
4 p．m．Advantages of Distributed Processing andMulti－Processing，John Steefel，Q1 Corp．
4 p．m．To be assigned．
SATURDAY，NOVEMBER 1Noon Educational Software：The Good，the Bad，the Ugly．Jo Ann Comito，S．U．N．Y．atStony BrookNoon Introduction to Personal Computing，RCA－Solid State1 p．m．Computer－Assisted Mathematics Courses，Dr．Frank Scalzo，QueensboroughCommunity College

1 p．m．Artificial Intelligence Update，Prof．Peter Kugel，Boston College
2 p．m．Compiling and Retrieving Personal Medical Data，Dr．Derek Enlander，St．Luke＇s Hospital
2 p．m．The Present State of CP／M Compatible Software，Tony Gold，Lifeboat Associates High Volume Date Handling：An Introduction to File Processing，Prof．Peter Kugel，Boston College
3 p．m．Connecting the Computer to the Outside World，Prof．James Gips，Boston College Educational Applications in the Home， David Ahl，＂Creative Computing Magazine＂
4 p．m．
4 p．m．Household Applications－Some New， Dr．Dennis J．McGuire

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\section*{Levels to Bits}

\section*{Dear Steve,}

I have been shopping around for the analog-todigital (A/D) converter integrated circuit that you used in your wood-stove interface (see "A ComputerControlled Wood Stove," February 1980 BYTE, page 32), but it does not seem to be readily available. C W Vuaun

I try to avoid specifying components that are not commonly available. While I obtain parts through industrial distributors rather than surplus outlets, I check the latter often to see what is available. In the case of the \(A D C 0808\), the time-lag is greater than I expected. However, in the meantime
there is a sixteen-channel version, the ADC0816CCN, which is the same in every respect (except that it has twice as many channels). It is available from Digi-Key Corporation, POB 677, Thief River Falls MN 56701. Their toll-free phone is (800) 346-5144. Call or write them for the current price. Steve

\section*{More Power}

Dear Steve,
I noticed your comment on UPSs (uninterruptible power supplies) in the June 1980 BYTE (see "Ask BYTE," page 86), and thought I would mention that they are commercially available in sizes small enough to be useful to

personal-computer users (see the Hardside catalog, page 34). I do not know who the actual manufacturer is, but I would like to know more about these items. The devices I am concerned with have specifications that accommodate 60 and 120 Hz power, with and without surge protection, and supply 150 or 200 W . The trade name is 'Mayday." R M Sanford

Thank you for pointing out the Mayday UPS. It is manufactured by SunTechnology Inc, which is located in New Durham, New Hampshire. The Mayday UPS is available from Hardside, 6 South St.
Milford NH 03055,
(800) 258-1790. According to the Hardside catalog, prices begin at \(\$ 168 . .\). Steve

\section*{A Hot Tip}

Dear Steve,
The solid-state sensor you described for your wood stove (see "A ComputerControlled Wood Stove," February 1980 BTYE, page 50 ) is very interesting. I have constructed the circuit, but I am having trouble calibrating the device for a range of -18 to \(+100^{\circ} \mathrm{C}\). Ron Goodmaster

The circuit you refer to can be calibrated in a number of ways. There is an offset and gain adjustment included for this purpose.

In normal practice, say for a range of \(0^{\circ} \mathrm{C}\) to \(100^{\circ} \mathrm{C}\), we would adjust for offset so that the output was 0 V with the temperature probe in an ice bath and adjust the gain so that the output is 1.00 V when it is placed in boiling water. To have it actually read \(-18^{\circ}\) as -0.18 V you will have to modify the circuit slightly. Presently the 50 k offset-
adjustment potentiometer is connected between +12 V and ground. By connecting it instead between +12 V and -12 V you can impress a negative current flow into IC2 such that it has a negative offset. The gain of the circuit will now have to be adjusted for a 118-degree span instead of 100 degrees. The trick is that to accurately calibrate the unit you should have a \(-18^{\circ} \mathrm{C}\) standard when you set the low end. Substituting a voltage source for the LM334 will only give you a relative calibration, but it may be all you need....Steve

\section*{Remote Control at Home}

\section*{Dear Steve,}

The other day I was thumbing through a BYTE magazine and I came across the article you wrote about using the TRS-80 and the BSR X-10 home-control system. (See "Computerize a Home," January 1980 BYTE, page 28.) I had been working on the same project in my spare time, and I had been using opto-isolators for interfacing; however, your method is well above the idea that I was attempting. Your article was very informative and the accompanying software was excellent. I have since looked up your articles in other BYTEs, and I must say that you never fail to come up with interesting and practical pieces.

I have decided to use your method, and I will shortly be purchasing a "Busy Box" from MicroMint in Woodmere, New York.

Whenever I have my TRS-80 up and running, the Sears home-control-unit operation is either marginal or nonexistent. The minute I turn the TRS-80 off, the home-control unit works fine. I assume that the prob-


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lem is RFI (radio-frequency interference), but I am not quite sure how to cope with the problem. I know the TRS-80 is a great noise generator, but I know little of how to deal with the problem. If you can give me any help along these lines, I would appreciate it very much. Thanks.
Robert G Romppel

Radio-frequency interference (RFI) is so pervasive among personal computers and consumer electronic gadgets that the Federal Communications Commission ( \(F C C\) ) has extended the long arm of the law. See Terry Mahn's article "FCC Regulation of Personal Computers and Home Computing Devices" on page 180 in this issue.

As for now, there are various alternatives open to you. First, try plugging the BSR unit into a different wall socket than the TRS-80. The range of the Busy Box is 30 feet, so it doesn't have to be right next to the computer anyway. (Avoid extra long extension cords and use a plug strip for the computer and peripherals.) The noise from the computer is being radiated into the power line; therefore you want to put as much electrical distance between the TRS-80 and the \(X-10\) as possible. While there may be five wall outlets in an average room, they are rarely all on the same circuit breaker. For the noise to reach an appliance plugged into another circuit loop, it must first travel back to the breaker box. This is a lot of wire and the resulting inductance will diminish some of the interference.
If that doesn't work, next try to kill the noise at the source (the computer) by placing capacitors at the outlet. I suggest using three \(0.1 \mu F 600\) V disc ceramic capacitors, one from each side of the \(A C\) line connected to a good earth ground and another across the line. Ordinarily, you
would also connect the computer chassis to ground but this is not advisable on the TRS-80.
To really eliminate line noise, you need a combination of inductance and capacitance. Rather than trying to wind your own coils, it is better for you to buy a commercial noise suppressor. You want one that covers at least a range of 100 kHz to about 200 MHz . They are about \(\$ 20\) and up. One company that lists a few in its catalog is: Hardside, 6 South St, Milford NH 03055, (800) 258-1790.

If none of this works, then encase the entire thing in copper screening and run it on a battery! ...Steve

\section*{Remote Control on the Farm}

Dear Steve,
I am a graduate business student at Colorado State University working with David R Miller, Sun Up Angus Farms, Smithville, Missouri, in establishing an in-house computer system for his ranch. This will also be the topic of my thesis.

Presently the main areas that we see a need for a computer are:
1) cattle inventory -pedigree, calving dates, breeding dates, calf weights;
2) customer service-date, identification, and price of animals purchased, commercial or registered breeder, size of herd, etc;
3) accounting system -basically following the Internal Revenue's 1040 form with some variations;
4) various other programs for feed-ration analysis, investment analysis, profitability, etc.

I am interested in any existing computer programs or any information on the hardware available. Also, if you have any information about the cost, complexity,
 RAM XX, our latest static memory board, satisfies discriminating users of ALL S-100 machines while allowing you to keep pace with the state of the art. It's compatible with standard S-100 systems (Altair, IMSAI, etc.): bank select systems (Cromemco, Alpha Micro, North Star, etc.); and IEEE compatible extended addressing systems. RAM XX boards are addressable on 4 K boundaries and also have the capability to block out 4 K windows. Don't throw away your memory boards when you upgrade with RAM XX, simply change a few dipswitch settings and you're on-line with some of the best memory in the business.


As with all our memory products, you derive the benefits of \(4 / 5 \mathrm{MHz}\) operation, fully static design to eliminate dynamic timing problems, IEEE spec compatibility, low power, extensive bypassing, and careful thermal design. In addition, boards qualified under our Certified System Component high-reliability program run at 8 MHz typical and are guaranteed to run with 6 MHz Z80A CPUs; 32K CSC boards draw less than 1.5A guaranteed, with typical standby current of less than 1A.

f you're looking for specs - if you're looking for performance - if you're looking for exceptional value and insurance against obsolescence, look no further than the RAM series from CompuPro.


\section*{COMPUPRO S-100 MOTHERBOARDS: DESIGNED FOR THE FUTURE, AVAILABLE NOW}

Specifically designed to handle the new generation of 5 to 10 MHz CPUs coming on line (as well as present day 2 and 4 MHz systems), these advanced motherboards feature faraday shielding between all bus signal lines to minimize crosstalk, active termination that splits the termination load between each end of every bus line, and mechanical compatibility with Codbout, Vector, Imsai, TEI, and similar enclosures. Available in "unkit" form (edge connectors and termination resistors presoldered in place for easy assembly). or fully assembled and ready to go.
\[
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\]
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NOTE: MOst CompuPro boards are availabte in unkit form (sockers, bypass caps presoldered in place), assembied, or qualified under the Certified System Component (CSC) high-reliability program (200 hour burn-in, more).

\section*{ENHANCED/ADVANCED Z-80A S-100 CPU BOARD}

Superior design in a true ieet-compatible board (timing specs available on request) gives the power for future expansion as well as system flexibility. Includes all standard Z-B0A features along with power on jump/clear, on-board fully maskable interrupts for interrupt-driven systems, selectable automatic wait state insertion, provision for adding up to 8 K of on-board EPROM, 4 MHz operation, and IEEE compatible 16/24 bit extended addressing. \(\mathbf{\$ 2 2 5}\) unkit, \(\$ 295\) assm, \(\mathbf{5 3 9 5}\) CSC.

\section*{SPECTRUM S-100 COLOR GRAPHICS BOARD}

Includes BK of IEEE-compatible static RAM; full duplex bidirectional parallel I/O port for keyboard, joystick, etc. interface; and 6847-based graphics generator that can display all 64 ASCII characters. 10 modes of operation, from alphanumeric/semigraphics in 8 colors to ultra-dense \(256 \times 192\) full graphics. 75 Ohm RS-170 line output and video output for use with FCC approved modulators. \(\mathbf{\$ 3 3 9}\) unkit, \(\mathbf{\$ 3 9 9}\) assm, \(\mathbf{\$ 4 4 9}\) CSC. You don't have to settle for black and white graphics or stripped-down color boards; specity the CompuPro Spectrum.
Want graphics software? Sublogic's 2D Universal Graphics Interpreter (normally \(\$ 35\) ) is yours for \(\$ 25\) with any Spectrum board purchase.

\section*{OTHER S-100 BUS PRODUCTS}

\section*{Godbout Computer Enclosure .}

Active Terminator Board.
2708 EPROM Board (less EPROMs)
Memory Manager Board.
25 "Interfacer \({ }^{\prime \prime}\) " \(/ O\) Board.
3P Plus \(S\) "Interfacer II" VO Board.
Mullen Extender Board. .
Mullen Relay/Opto-Isolator Control 8oard.
Vector 8800V 5-100 Prototyping Board. . .
. \(\$ 289\) desktop, \(\$ 329\) rack mount \(\$ 34.50\) kit
\$85 unkit
\(\$ 59\) unkit, \(\$ 85\) assm, \(\$ 100\) CSC . \(\mathbf{\$ 1 9 9}\) unkit, \(\mathbf{\$ 2 4 9}\) assm, \(\mathbf{\$ 3 2 4}\) CSC . \(\mathbf{\$ 1 9 9}\) unkit, \(\mathbf{\$ 2 4 9}\) assm, \(\mathbf{\$ 3 2 4}\) CSC \(\$ 59\) kit
\(\$ 129\) kit, \(\$ 179\) assm
.\(\$ 19.95\)

\section*{NEW! S-100 DUAL PROCESSOR CPU BOARD}

The Dual Processor Board is here . . . and CPU boards will never be the same again. 8088 CPU gives true 16 bit power with a standard 8 bit \(\mathrm{S}-100\) bus; an 8085 gives compatibility with \(\mathrm{CP} / \mathrm{M}\) and 8080 software. Accesses up to 16 megabyte; of memory, meets all IEEE \(\mathrm{S}-100\) bus specifications (timing spers available on request), runs 8085 and 8086 code in existing mainframe as well as Microsoft 8086 BASIC and Sorcim PASCAU/ \({ }^{\mathrm{m}}\), runs at 5 MHz for speed as well as power, and is built to the same stringent standards that have established our leadership in 5 - 100 bus components. \(\$ 385\) unkit, \(\mathbf{\$ 4 9 5} \mathbf{~ a s s m}, \mathbf{\$ 5 9 5} \mathbf{C S C}\).
8085 single processor version of above: \(\mathbf{\$ 2 3 5}\) unkit, \(\mathbf{\$ 3 2 5}\) assm, \$595 CSC.

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satisfaction, or problems encountered in such a system, I am sure I would find it very useful.
My main problem in trying to choose a computer system is in deciding between two very diverse opinions. One opinion is that for a system as I have specified, I need a computer with 64 K bytes of memory and two 8 -inch doubledensity floppy-disk drives for about one million bytes of storage. This would run in the neighborhood of \(\$ 8000\) in hardware (computer, printer, and terminal). The other major opinion is that I could get by with 50 K bytes of memory and 50 K bytes of storage; ie: a system that would sell for \(\$ 1500\) (such as the Intecolor 3600 Series from Intelligent Systems Corporation).
If you could give me any answers these questions, I would greatly appreciate it. Thank you for your time. Laurie A Miller

It looks to me as though you already have a good idea what kind of computer you need. At least 48 K , preferably \(64 K\), bytes of memory are required plus dual disks. If your data base is exceedingly large, or a large portion of it must be on-line at one time, make sure you choose a system that is expandable. This could include two more floppy-disk drives or a 10-megabyte or larger Winchester hard disk. If because of finances you choose to start small, select a system that does not require a
masters degree in electrical engineering to expand. Time of execution is generally the only real difference between large and small computers. The more disks you have to sort through to find the data you want, the longer it takes to get an answer. The software you want sounds like specific applications of generally available accounting and data-base management programs.
Hardware is only one part of the consideration however. Be aware that you are configuring a classic small-business system and the inventory and data-base management programs would be similar to, say, a dairy cooperative. While the choice of the hardware is important, adequate software and system maintenance are more signficant in the long run. Once the computer is installed it is very easy to become dependent upon it working.
There are many computers on the market that will satisfy your requirements: Cromemco, Hewlett-Packard, and Data General to name a few. The larger computer stores not only sell equipment like this, but offer custom programming and on-call field service as well. Take the time to evaluate the post-sale support for your computer, and check to see if your software will be compatible with other systems.
I do not know much about cattle, but the complaints I've herd-oops!-heard from smallbusiness computer users have been registered.
..Steve■

In "Ask BYTE," Steve Ciarcia answers questions on tiny area of microcomputing, The most representative questiond received each monith will be answered and published. Do you have a nagging problem? Send your inquiry to:

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\section*{NEW:!! \\ MOD-II NEWSLETTER \\ }

\title{
FCC Regulation of Personaland Home-Computing Devices New Rules After a 3-Year Study
}

\author{
Terry G Mahn \\ Wewer \& Mahn PC \\ 1762 Church St NW \\ Washington DC 20036
}

If you have been reading BYTE within the last half year, you are probably aware that the FCC (Federal Communications Commission) has handed down a set of regulations prohibiting the sale of personal computers that emit unacceptable levels of RFI (radio-frequency interference). But the FCC has changed its regulations several times, and in any case, information on and interpretation of these rulings have been scarce. I hope to clarify these most recent FCC regulations and to describe how (and when) they will affect you as a

\footnotetext{
About the Author
Terry G Mahn is a principal in the law firm Wewer and Mahn PC in Washington DC, where he specializes in intellectual property protection and licensing, and the legal, regulatory, and policy issues affecting the data processing and telecommunications industries. He has previously served as general counsel to the Computer and Communications Industry Association and as a computer specialist for the US House of Representatives Committee on House Administration. Currently, he is regulatory counsel to MITA (Microcomputer Industry Trade Association).
}

\section*{It is current FCC policy for computer manufacturers to bear the associated costs of their technology.}
personal-computer user or vendor and the industry in general.

It is a common misconception by many in the computer industry that the FCC is empowered by the 1934 Communication Act only to regulate communications providers and users-that is, common carriers, broadcasters, and Citizens Band radio users. This misconception emanates from the nearly decade-old controversy surrounding the Commission's so-called "Computer Rules." First adopted in 1971, these regulations attempted to define the technological boundary line between common-carrier communications and data processing, to identify the FCC's jurisdictional perimeter under Title II (common-carrier services) of the Act. Recently, the computer rules have undergone a major revision in an effort
to halt FCC encroachment into the traditionally nonregulated computer and data-processing industries.

The FCC's regulatory reach into the computer industry, however, is not as limited as the Computer Rules might seem to indicate. Title III of the Act (radio services) specifically empowers the FCC to protect communications systems from RFI, from whatever source derived. Insofar as virtually all computing devices emit spurious radio frequencies that can potentially interfere with radio or television services, manufacturers and vendors of such equipment come directly within the FCC's Title III jurisdiction.

It is not axiomatic that where federal authority exists, industry regulation and increase of the cost of doing business is sure to follow. (Under Chairman Ferris, for example, the FCC has been particularly notorious in reducing regulation of American industry.) Nevertheless, the FCC has chosen to regulate in this area for purely economic reasons. Because the radio spectrum is a valuable, but limited resource that can be used in various but incompatible ways, simple economic efficiency suggests that such resources be employed in their

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LEVEL "A" SPECIFICATIONS
Explorer/85's Level "A" system features the advanced Intel 8085 cpu , an 8355 ROM with 2 k deluxe monitor operating system, and an advanced 8155 RAM I/O all on a single motherboard with room for RAM/ROM/ PROM/EPROM and S-100 expansion, plus generous prototyping space.
PC Board: Glass epoxy, plated through holes with solder mask. - I/O: Provisions for 25-pin (DB25) connector for terminal serial I/O, which can also support a paper tape reader .. . cassette tape recorder input and output . . . cassette tape control output . . . LED output indicator on SOD (serial output) line ... printer interface (less drivers) . . . total of four 8-bit plus one 6-bi I/O ports. - Cryatal Frequency: 6.144 MHz . Contro Switches; Reset and user (RST 7.5) interrupt . . . additional provisions for RST 5.5. 6.5 and TRAP interrupts onboard. Counter/Timer. Programmable, 14-bit bi nary. - System RAM: 256 bytes located at F800, idea for smaller systems and for use as an isolated stack area in expanded systems ... RAM ex
via \(\mathrm{S}-100\) bus or 4 k on motherboard.
System Monitor (Terminal Version): 2k bytes of deluxe system monitor ROM located at Fyep. leaving didn free for user RAM/ROM. Features include tape load with labeling ... examine/change contents of memory . . . insert data . . . warm start. . . examine and change all registers . . . single step with register display at each break point, a debugging/training feature . . go to execution address . . . move blocks of memory from one location to another . . . fill blocks of memory with a constant . . . display blocks of memory . . automatic baud rate selection to 9600 baud ... variable display line length control ( \(1-255\) characters/line) ... chan nelized I/O monitor routine with 8-bit parallel output for high-speed printer . . . serial console in and console out channel so that monitor can communicate with I/O ports
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single step with register display at each break point go to execution address. Level " \(A\) " in this version makes a perfect controller for industrial applications. and is programmed using the Netronics Hex Keypad/ Display. It is low cost, perfect for beginners.
HEX KEYPAD/DISPLAY SPECIFICATIONS Calculator type keypad with 24 system-defined and 16 user-defined keys. Six digit calculator-type display. that displays full address plus data as well as register and status information.
LEVEL "B" SPECIFICATIONS
Level "B" provides the S-100 signals plus buffers/ drivers to support up to six S-100 bus boards, and includes: address decoding for onboard 4k RAM expansion selectable in 4 k blocks . . . address decoding for onboard 8 k EPROM expansion selectable in 8 k blocks
address and data bus drivers for onboard expansion wail slate generator (jumper selectable). to allow the use of slower memories . . . two separate 5 volt regulators.
LEVEL "C" SPECIFICATIONS
Level "C" expands Explorer/85's motherboard with a card cage. allowing you to plug up to six S-100 cards directly into the motherboard. Both cage and card are neatly contained inside Explorer's deluxe steel cabinef. Level "C" includes a sheet metal superstructure, a 5 -card, gold plated S-100 extension PC board that plugs into the motherboard. Just add required number of S-100 connectors.


Explorer/85 With Level "C" Card Cage.

\section*{LEVEL "D" SPECIFICATIONS}

Level "D" provides \(4 k\) of RAM, power supply regulation. filtering decoupling components and sockets to expand your Explorer/ 85 memory to 4 k (plus the origi-
nal 256 bytes located in the 8155A). The static RAM can be located anywhere from do EFFF in \(4 k\) blocks.
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most valuable way-namely, in the way that yields the greatest public benefits. Just as raising a crop of corn and grazing cattle are incompatible uses of the same plot of land, so too may the operation of a computing device and the transmission of television signals present incompatible uses of the electromagnetic spectrum. The FCC, therefore, is forced to balance the demands placed on electromagnetic spectrum usage by American businesses and consumers: the difficulty arises in determining which use will yield the greatest public benefits.

Consider, for example, the follow-
ing possible public-cost/benefit scenarios involving computing devices and communications services:
- A suspected criminal is being pursued by police through winding city streets. Several patrol cars begin converging on the suspect from different directions as information on the suspect's location and movement is relayed over the police radio band. Suddenly, the suspect makes an abrupt turn through the parking lot of a cocktail lounge. Before the pursuing car can communicate the suspect's sudden

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movement, however, interference crackles over the police band, drowning out all communications for several seconds. When the band finally clears, the police learn that they have lost track of the fleeing suspect. Later, the police investigate the cause of the interference on their restricted band and learn that one of the coin-operated video games in the cocktail lounge was the source of the interfering radio frequencies.
- An airplane pilot finds himself caught in bad weather and is forced to make an "instrument" landing. As the pilot approaches the airfield, he asks his copilot to render a quick computation to better gauge their position. The control tower, which has the plane on radar, warns the pilot of an approaching larger aircraft. Suddenly, before the tower's automatic collision-avoidance instructions are received, interference drowns out the radio channel. While waiting for the channel to clear, the pilot nearly collides with a commercial airliner but manages to land safely. The FAA (Federal Aviation Administration) later conducts an investigation and learns that the electronic calculator used by the copilot emitted the RFI that caused the interference on the restricted aeronautical-frequency band.
- A young mathematics student receives a personal computer for his fifteenth birthday. Shortly thereafter his entire family begins to use the computer for various applications: the father does tax and financial planning for his insurance clients; the mother stores cooking recipes and addresses and telephone numbers of friends and relatives; and the younger brother plays electronic video games. Soon, even the family's home-security and energy-control systems are being run by the computer. Meanwhile a neighbor complains to an FCC field office that he has been experiencing interference each evening over one of his local television channels. The field office investigates and learns that the personal computer is the source of the RFI. The family is told to correct their computer or discontinue its use. Since the
manufacturer's warranty does not cover RFI defects, the family is forced to undertake expensive corrective measures of their own.

While these examples may seem a little contrived, in fact, each concerns a theoretical situation with which the FCC is concerned.

Moreover, in every case brought to the FCC's attention involving RFI from computers, the FCC has routinely decided that radiation from such devices is a less valuable use of the spectrum than the radio-communication services which might be interfered with. Stated another way, it is current FCC policy for computer equipment manufacturers to bear the associated costs of their new and beneficial technology.

\section*{Computing Device Interference}

Computers and other similar devices emit potentially harmful radio-frequency signals. Inside a computer, very rapid electrical signals and pulses are generated and used to regulate sequences of events and to carry out the control and logic functions of the computer. These rapid electrical pulses produce highfrequency emissions that "float" around inside the cabinet of the computer. Unless this energy is somehow contained or filtered, it is radiated into space to be picked up by radio or television receivers.

Computers have been reported to cause harmful interference to almost all radio services, particularly those services below 200 MHz , including police, aeronautical, and broadcast services. Several factors that have contributed to the recent increase in computer-interference complaints include:
- the proliferation of digital electronic equipment in both businesses and homes;
- the development of higher-speed computers, which require designers to contend with problems of radio-frequency emission never before experienced;
- the increased replacement of steel cabinets with plastic cabinets, which provide little or no RFI shielding.

To the extent that computing devices are harmful in terms of their potential for generating RFI, and because
private mediation between interfering uses is considered highly unlikely, the FCC becomes the final arbiter of spectrum interference.

Part 15 of the Commission's Rules specifically addresses these concerns by setting forth various technical and administrative specifications for all devices that generate or use radiofrequency energy. Computer and other digital devices not intended to radiate RFI are defined as restrictedradiation devices. Until very recently, however, restricted-radiation devices were subject to technical performance standards first drafted by the FCC in
1938. In further complication of matters, under these 40 -year-old rules, personal computers are subject to vastly different technical standards depending on whether they contain their own video displays or connect to an external television set.

Three years ago the FCC initiated a rule-making procedure to modernize its Part 15 rules and to render them more workable and nondiscriminatory in our evolving electronic society. The proceeding was recently concluded with the adoption of new regulations that will affect all computer manufacturers. Hardest hit,

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however, will be the personalcomputer industry.

\section*{FCC Classification of Computing Devices}

In order to establish RFI standards that are appropriate for a given computer's actual harm-causing potential, the Commission has classified all computing devices under a binary scheme: Class A devices are defined as computing devices used in commercial environments, and Class B devices are defined as those used in a residential environment or widely marketed to the public.

The basis for this dual classification scheme is rooted in the theory that Class B (consumer) devices are located in closer proximity to radio, television, and (in many cases) landmobile radio services and thus have a higher potential for causing interference than do Class A (commercial) devices. Additionally, the Commission has reasoned that consumer products usually do not contain the technical sophistication found in commercial equipment, nor do they receive the same level of preventive maintenance.

In recognition of these important differences, between consumer and commercial products, the FCC has imposed technical standards on consumer equipment that are ten times more stringent than those standards imposed on commercial equipment. More importantly perhaps, the Commission is requiring manufacturers of consumer devices to register their products with the FCC by January 1, 1981 or cease all marketing; no similar rule applies to manufacturers of commercial computing equipment.
(In addition, the FCC rules further distinguish between Class B "personal computing" devices that contain their own video displays and those that connect to a standard home television receiver (so-called Class I TV devices), with the latter being subject to somewhat stricter rules. Such distinctions between personalcomputing devices should soon disappear, pending the successful completion of an on-going rulemaking in this area.)

\section*{The Regulatory Scheme for Computing Equipment \\ The FCC's regulatory scheme for}
computing devices consists of both technical standards and administrative procedures. The technical standards are designed to minimize the likelihood that computing devices will cause interference with any FCCauthorized communications services. Therefore, standards for radiation as well as conduction (ie: through a building's wiring) limit the amount of radio frequency that computing devices will be permitted to emanate during their normal operation.

The administrative procedures adopted by the FCC are intended to ensure that manufacturers comply with the appropriate technical standards; these procedures also apprise the users of each class of equipment of its interference potential and what to do in case of technical failure. Most important, however, are the compliance deadlines that manufacturers must meet in order to continue (or begin) advertising and marketing their computing equipment. As explained more fully below, the rules differ substantially between commercial and consumer equipment, with the latter being subject to more stringent requirements.

\section*{Class Definition Distinctions}

The FCC defines a "computing device" to be any electronic system that generates timing signals or pulses in excess of 10,000 cycles per second ( 10 kHz ) and uses digital techniques. This definition includes, among other things, digital telephone equipment or any device that generates radio frequencies for the purpose of performing data-processing functions such as "electronic computations, operations, transformations, recording, filing, sorting, storage, retrieval, or transfer." The Commission notes that computer terminals and peripherals also fall within this definition but that other components and subassemblies do not.

Class A devices are further defined as any computing devices that are marketed for use in a commercial, industrial, or business environment. Class B devices are defined to be computing devices marketed for use in a residential environment in spite of their potential use in commercial environments. Examples of Class B devices are electronic games, personal computers, calculators, and similar electronic devices marketed to the general public. Temporarily exempt-

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ed (pending further rulemaking by the FCC) from the specific Class B technical and administrative requirements are microprocessors utilized in transportation vehicles, home appliances, test equipment, and electronic power or control systems utilized in industrial plants.

\section*{Compliance Verification Procedures}

Class A device manufacturers are required, prior to marketing, to verify that their devices meet the technical provisions set forth in the FCC's rules. In contrast, manufacturers of most Class B devices on the market (eg: electronic video games and personal computers) must certify to the Commission that their devices comply. Herein lies the heavy burden to be shouldered by the personal computing industry under the FCC regulations. (For, if any lesson is to be learned from the FCC's "Part 68 Program" for certification of telephone devices, it is that federal regulations of this type are both costly and time consuming for manufacturers.)
Verification (for commercial devices) is basically an approval procedure based on the honor system, whereby a manufacturer tests his equipment to verify to the public that it complies with the appropriate technical standards. Although no FCC notification is imposed, manufacturers are still required to maintain records of their testing procedures and results.

By comparison, certification (for consumer devices) is an arduous equipment-authorization procedure which requires manufacturers to test their product for compliance and submit the test information to the FCC along with a completed application (FCC Form 731), photographs, and fees. After the FCC reviews the submissions, a certification number is issued for the tested equipment; the manufacturer must affix this number to every model thereafter imported, advertised, or marketed. Any subsequent change in the circuitry or operation requires that the equipment be recertified to the FCC.

Due to their high potential for causing RFI, the Commission has determined that only the following devices must be certified: electronic games, including coin-operated video games (but excluding handheld games that do not use a television
(1a) RADIATION - Maximum field-strength limits
\begin{tabular}{lcc}
\begin{tabular}{c} 
Frequency \\
\((\mathrm{MHz})\)
\end{tabular} & \begin{tabular}{c} 
Distance \\
(meters)
\end{tabular} & \begin{tabular}{c} 
Field \\
Strength \\
\((\mu \mathrm{V} / \mathrm{m})\)
\end{tabular} \\
30 to 88 & 30 & 30 \\
88 to 216 & 30 & 50 \\
216 to 1000 & 30 & 70 \\
30 to 88 & 3 & 100 \\
88 to 216 & 3 & 150 \\
216 to 1000 & 3 & 200
\end{tabular}
\begin{tabular}{lcc} 
(1b) CONDUCTION • Maximum voltage levels & \\
& \begin{tabular}{c} 
Frequency \\
\((\mathrm{MHz})\)
\end{tabular} & \begin{tabular}{c} 
Maximum RF Line \\
Voltage \\
\((\mu \mathrm{V})\)
\end{tabular} \\
Class A & 0.45 to 1.6 & 1000 \\
& 1.6 to 30 & 3000 \\
Class B & 0.45 to 30 & 250
\end{tabular}

Table 1: Radiation and conduction standards for computing devices. Table 1a sets the maximum permissible level of radiated radio-frequency emissions for both Class A (commercial) and Class B (consumer) devices. Table 16 does the same for conducted emissions impressed on the electrical-power network.
receiver for display); personal computers (excluding digital clocks, desktop calculators, and handheld calculators); and peripherals and terminals capable of being attached to a personal computer. All other Class B devices need merely be verified by manufacturers prior to their marketing.

\section*{Technical Standards}

The technical standards imposed by the new rules are designed to provide a "reasonable degree" of protection for radio and television receivers. Since unwanted interference from computing devices can result from radiated as well as conducted RFI, the standards regulate both types of emmission. (See table 1.) Radiation testing requires manufacturers to measure the radio-frequency emanations at specified frequencies and distances from their equipment to ensure that certain maximum energy levels are not exceeded. Conduction testing is designed to ensure that equipment will not impart more than a maximum level of energy over a specified frequency range into the electrical-power network. [For example, this restriction will apply to devices that use house wiring to remotely control appliances....GW] (The actual equipment-test proce-
dures to be used by manufacturers are the subject of a current rulemaking before the FCC. Until final rules are issued, the Commission has approved certain conventional industry test procedures.)
Together, both tests protect against interference frequencies as low as 450 kHz (just below AM radio) to frequencies as high as 1000 MHz (above UHF television signals). As stated previously, the standards for Class B equipment are ten times more stringent than those for Class \(A\).

\section*{Labeling and User Information}

Complex rules notifying users of their computing devices' potential (or lack thereof) for interference with radio communications and spelling out corrective action to be taken are key aspects of the FCC's administrative regulations. In essence, all computing devices will require some type of labeling or warning after January 1, 1981; however, these regulations will vary depending on the classification of the device as well as the device's mandatory-compliance date. All Class A equipment (unless certified under the Class B standards) must warn users that its operation in a residential environment may cause interference for which the user will be held accountable.

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\section*{Mandatory-Compliance Dates}

With regard to the new rules' effective dates, here too, the Commission's regulations are complicated and confusing. Originally, the Commission proposed a single deadline, July 1, 1980, after which all manufacturers of computing devices would have to comply with the appropriate rules or cease marketing their equipment. However, it soon became obvious to the Commission that several factors made a unified effective date impractical; these factors include the apparent lack of trained personnel to perform the necessary tests, the large number of devices in production that would have to be tested, and the shortage of emission-suppression components.

Upon reconsideration, therefore, the FCC adopted the following schedule of mandatory effective dates for compliance with its Part 15 rules (see table 2):
- Personal computers and other devices requiring certification (eg: video games, peripherals, and terminals) must meet the Class B standards by January 1, 1981.
- All other computing devices (Classes A and B) must comply with the appropriate device standards if first manufactured after October 1, 1981.
- If such (noncertificated) devices, however, are placed into production before October 1, 1981, compliance will not be required (for subsequently produced devices) until October 1, 1983.

Any device failing to meet these mandatory-compliance dates cannot lawfully be marketed, imported, or advertised for sale in the United States.

\section*{Special Rules for Subassemblies and Peripherals}

Components and subassemblies of computing devices are not required to comply independently with the Commission's technical standards. In addition, peripherals supplied as part of a computing device do not need to be considered separately. Nevertheless, because all end products must comply, systems vendors and integrators can be expected to pressure their components suppliers into indirect compliance with these new rules.

On the other hand, peripherals marketed independently from their associated computing devices must comply directly with all technical and administrative standards. Peripherals marketed as part of any personal computing systems (which are in the Class B certified category) therefore must be certificated; all other peripherals (in the Class B noncertified and Class A categories) need merely be verified. In addition, peripherals sold separately from their computing systems also must be individually labeled.

\section*{Enforcement of Computing Device Rules}

Lest there be any question as to the Commission's experience or commitment in enforcing its interference regulations as they pertain to the mass distribution of consumer devices, you need only recall the regula-

Compliance Date
January 1, 1981

October 1, 1981

October 1, 1983

Any device failing to meet these mandatory compliance dates cannot lawfully be marketed, imported, or advertised for sale in the US.

Table 2: Dates of mandatory compliance for computing devices.

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tory crackdown that accompanied the Citizens Band radio craze of a few years ago. There, the Commission revealed that it had adequate power over both manufacturers and retailers to prevent users from gaining access to equipment that was improperly engineered or tested.

The FCC can enforce its rules through either civil or criminal proceedings. For simple violations of any rules, the FCC has the power to issue cease-and-desist orders (ie: administrative injunctions) commanding the violator to comply with the rules or possibly face severe consequences. The severe consequences may be in the form of court-ordered injunctions or, in the case of willful violations, felony prosecutions with possible fines and prison terms of up to 2 years. Needless to say, criminal sanctions are rarely imposed by the Commission.

The FCC is hoping, rather, for manufacturers and vendors to comply willingly with its rules to avoid developing a reputation for selling customer equipment that results in widespread interference. Should large-scale noncompliance result, however, more vigorous standards
and more troublesome equipment-authorization procedures could very likely be adopted by the Commission and imposed on the entire industry.

\section*{Conclusion}

As with any FCC rulemaking that involves evolutionary consumer products, the Commission's activities to date may reveal only the tip of the iceberg. The protracted FCC proceedings involving telephone-equipment registration bear strong witness to this observation. New microproces-sor-based devices may create unforeseen RFI problems not addressed in the new rules, changing work patterns will slowly blur the environmental distinctions between the home and office, and evolving communication services will continue to place additional demands on spectrum usage. Indeed, the Commission's fundamental assumption for its classification of computing devices (ie: proximity to RF receivers) is already starting to erode as radio receivers become increasingly utilized in commercial environments for the provision of Teletext and direct (rooftop) broadcast satellite services.

With new rules come new costs-
whether they be costs of equipment redesign, costs of RFI-suppression components, or costs of testing, labeling, and FCC-certification delays.

The FCC is currently in the midst of a rulemaking proceeding to develop the Part 15 equipment-testing procedures. Slated for possible future rule amendments are handheld calculators, home appliances, microprocessor-based transportation systems, and other similar devices. Manufacturers of these types of equipment, therefore, should adapt to the idea that the FCC represents a cost of doing business that cannot be avoided-from now on.

Incidentally, the FCC's rules seek only to prevent interference between computing devices and (FCCapproved) communications services. Interference between incompatible devices utilized in the home (eg: wireless intercoms, burglar- and firedetection systems, wireless switches, etc) is probably beyond the FCC's jurisdiction. Thus, it will be up to the industry itself to resolve among its own members-possibly through the newly-formed Home Bus Standards Association-these emerging interference issues.


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\section*{Relocating Assemblers and Linking Loaders}

Ottmar E Bochardt, 4560 Decarie \#301, Montreal PQ H3X 2H6, Canada

Relocating assemblers and linking loaders are two pieces of assembly-language-oriented software that are probably unfamiliar to the average computer enthusiast. As a matter of fact, the very words relocating and linking (especially the latter) sometimes conjure up ideas of some vague, unspecified process. In reality, though, relocating assemblers and linking loaders are companion pieces of software that are easy to understand. The purposes of this Technical Forum are to:
- explain the relocating and linking processes;
- compare the two major linking methods;
- demonstrate how the assembly process is made slightly more complicated by relocating and linking;
- comment on the microprocessor-software standard proposed by Formaniak and Leitch.

My machine-language examples are all based on the MOS Technology 6502 processor. The Technical Forum "A Proposed Microprocessor Software Standard" by Peter Formaniak and David Leitch appeared on page 34 of the July 1977 BYTE.

\section*{Relocating and Linking Process}

A relocating assembler is one which assumes that your program will be stored beginning at location zero in memory. In addition to object-module records that give the assembled machine-language code, the relocating assembler also generates extra information in relocation records to indicate which parts of the object module must be changed if the code is loaded beginning at some location other than zero.

A relocating loader, then, need only be slightly more intelligent than an ordinary (or absolute) loader. It must be able to:
- separate the input stream into individual object modules;
- assign a relocation address to each module;
\[
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& \text { MICROPOLIS DO } \\
& \text { THINGS IIKE } \\
& \text { EVERYONE ELSE? }
\end{aligned}
\]

Listing 1: Example output from a relocating assembler. The code followed by the symbol \(R\) indicates a relative address, one that will be changed if this code is relocated to any starting location other than hexadecimal 0000 . The code followed by the symbol \(G\) or \(G^{\prime}\) indicates an external address, one that will have a known value only when this module is linked with other modules of code.


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- load each object module in correct relation to the new beginning address;
- read the relocation records to determine which memory locations must be changed to point to correct locations within the relocated code.

The example given in listing 1 , which is source code to be processed by a hypothetical relocating assembler, will help illustrate these functions.

Suppose that the object module is to be loaded at hexadecimal location 0500. The effect of changing the load point of each object module by adding the relocation address shows that all relative addresses (those marked by an R in column 5 of the address) are offset by the amount hexadecimal 500; ie: hexadecimal 500 is added to each of these addresses.
Certain addresses within a portion of code are referred to in the code itself. If the code is moved (or relocated) to a different location, all references to these addresses (which are called relative addresses) must be changed so as to point to the correct location within the newly relocated code. Specifically, if the relocatable machine code is written to begin at memory location 0000, all references to a relative address must be replaced by the sum of the original address plus the relocation offset (which is equal to the beginning address of the code in its new location).
An example of this is the JMP LOOP instruction at hexadecimal location 0055 in listing 1. When the code is written to begin at hexadecimal location 0000, the label LOOP refers to memory location 0040. However, when this code is relocated to location 0500, LOOP becomes location 0540, and the JMP LOOP instruction now at 0555 is 4 C 4005 ( 4 C is the JMP op code, and 4005 is the address 0540 , as stored in the computer, low byte first). In the example of listing 1, all data flagged with an R will be incremented by 0500 .
(Note, however, that a relative address is not to be confused with assembly-language relative addressing. The latter refers to a mode of addressing available in the instruction sets of most microprocessors, where the byte being addressed is specified by how far away that byte is from the beginning of the next instruction. A relative addressing displacement byte is usually limited to a signed, one-byte quantity. A relative address, as part of a relocatable object module, is a two-byte address (for all 8 -bit microcomputers) that must be changed when the module is relocated to another beginning address.)

An absolute address is an address that is not modified during the relocation process because it refers to a portion of memory outside the area being relocated. In our example of listing 1, the three bytes at 0100 are designated as being absolute (because they follow the .ASECT or absolute section pseudo-operation). When this section of code is relocated to hexadecimal 0500, the data bytes will still be at 0100 . Thus, the reference to DATA (in the STA DATA line) still points to location 0100. This is because the data at 0100 has not been relocated.

Often assembly-language modules are written separately and are meant to be combined at a later time. In many cases, these modules reference each other. A label used in one program but defined in another is called
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an external symbol. When the modules are combined into one program, not only must they be relocated to separate memory areas, but they must also be linked; ie: the relocated values of each of the external symbols must be known by all of the modules. This means that the external symbols must be declared as such within the assembly-language source file.

In the sample program of listing 1, the purpose of the .ENTRY pseudo-operation is to declare that the value of the label SUB 1 (ie: the address of the routine's entry point) is to be made available to other assembly modules. The character string "SUB1" and its value will be included in the object module, as part of an internal symbol record.
The next three statements indicate that the symbols SUB2, COMMON1, and VAL001 are referenced but not defined by this module (they will be defined later, when the modules are linked). These external symbols must be defined as internal symbols by exactly one of the assembly modules present at linking time. All listing lines flagged with a \(G\) or \(G^{\prime}\) have an associated entry in an external symbol record, which includes the label name and a pointer to the label's use within the module. For example, the load module used with the module in listing 1 will have an external symbol record that associates the symbol "SUB2" with the address 0053R.

\section*{Implementing the Link Process}

As an example, let us look at the format of object modules (ie: the machine-language module created by assembling a source module) resulting from the Mostek SDB-80 assembler. (A description of this standard is given by Formaniak and Leitch. See references.)

For each external symbol found, only one object record
is produced. All references to a given symbol are linked together with the external-symbol record containing the address of the head of the list and the last entry in the list containing the hexadecimal value FFFF. (See figure 1.) In other words, when the SDB-80 assembler encounters an external reference, it uses that two-byte memory location to indicate to the loader where to find the previous reference to that symbol.
In terms of object-file size, this is probably the most efficient way to store linkage information, because it guarantees that only one external-symbol record per symbol will be used, regardless of how many times the symbol is referenced. It follows that, since the number of records being processed is smaller because of the link process, the time taken to link a series of object files will be minimized.

In the case of assembler source code (especially when written for a 6502 or similar processor), this linkage technique has several drawbacks. First of all, there is no provision for handling single-byte values, because two bytes of memory are required within the object code for the pointers. This is a serious deficiency for machines like the MOS Technology 6502 and the Motorola 6800, because these processors allow heavy use of page-zero addressing; in this manner the user can specify an address with one byte. Also, it is convenient to define smallvalued parameters externally (such as VAL001 in listing 1) for use in two-byte instructions; the Mostek and other assemblers do not allow this.
Another point: it is impossible to specify an external symbol as having an absolute address. This is due to the fact that the internal symbols (symbols that have an address equated with them, such as SUB1 and LOOP in listing 1) do not contain a flag to indicate whether the


Figure 1: Keeping track of external symbol use with a linked list. When the source file of an assembly-language module (consisting of the columns marked with an asterisk) is assembled into an object module of machine-language bytes, an external symbol record is created which points to the last place that the symbol is used (ie: the last memory location that must be filled with the address of the symbol, once that address is known-after linking). Within the data records that contain the object code for the routine, each reference of the external symbol points to the address of the previous reference, with a value of hexadecimal FFFF terminating the chain; this is shown by the arrows in the second column.

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Listing 2: Use of a separate page-zero assembly module. Use of a module like this on computers that have a set of special page-zero addresses allows page-zero addresses (such as XNOW) and system parameters (such as XMAX) to be defined in a central location.


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defined symbol is relative or absolute. This could be changed by adding a flag byte to the internal-symbol record or by splitting the external-symbol record into two types: one for relocatable external symbols, the other for absolute-valued external symbols.

Also notice that code cannot be placed in absolute locations, because there is only one kind of data record and it is subject to relocation.

In all fairness, I would like to point out that there is a way around most of the problems mentioned above. A separate page-zero assembly module could be created to define both the addresses of all page-zero locations, which would probably have to be done anyway, and the values of all parameters that the system designer might want to change. This idea is demonstrated by the example given in listing 2.

Any good assembler should have some sort of copy command that instructs it to accept in-line source text from a separate file; this could be used to easily include a zero-page module like listing 2 wherever it is needed. A less convenient alternative would be to always prefix the page-zero module to the assembler input stream. This method of information binding (ie: giving a symbol its final value; see references, Elson) has the advantage of forcing the designer to define all assembly variables centrally, rather than having them scattered throughout the source code. Unfortunately, a major redefinition of the page-zero module would require reassembly of all associated programs. Also, the additional I/O (input/output) for the page-zero module could prove to be time- and resource-consuming on limited systems.

I have one more criticism about the proposed standard: it does not allow external variables to be referenced in an operand-arithmetic expression. This can be a strong drawback when referring to many fixed-data structures. Consider the following external declaration, written in FORTRAN:

\section*{COMMON / STATUS / XNOW, YNOW, XVEL ... /}

An external declaration in any compiled language will take this form. Quite obviously, it should be possible to directly address any one of the variables in the common block. However, only the value of STATUS (the beginning address of the common block) is available using the proposed Mostek standard; the instruction would be .EXTRN STATUS. This means that a reference to XVEL, for example, could be done only through an address computation (ie: its address is equal to that of STATUS plus a certain number of bytes). Needless to say, the result is a waste of machine time, memory, and perhaps microprocessor facilities (eg: an index register). This problem directly affects the assembler programmer, since his coding style is interfered with.

The most practical alternative would be to allow offsets in external references. The offset could then be stored in the target location, to be adjusted at link time (the method shown in the program of listing 2). This will necessitate one entry in an external symbol record for each reference to that symbol in a source program. The result is, of course, increased object-module size and increased time taken to link or load a given set of modules.

It is possible to decrease both program size and execution time by separating the linking loader into a linker program (which links together a set of object modules, creating one file of fully defined machine code) and a simpler loader program (which loads the already linked machine code).

\section*{Relocating Assemblers}

To an absolute assembler, all variable names are alike; ie: each represents a known value. On the other hand, a relocating assembler must be able to distinguish between three types of entries in its symbol table:
- absolute symbols
- relative symbols
- external symbols

When a relocating assembler encounters an arithmetic expression containing more than one symbol, it must determine several things: whether the expression is valid or not; and if it is valid, what its value is and whether an external or a relocation record (if any) need be written. Also, the use of arithmetic operators is limited by the combination of symbols being worked upon. For example, REL + EXT is valid if an external record is generated for the resulting sum; REL - REL is always valid; but REL - EXT is always invalid. (REL and EXT refer to a relative and an external symbol, respectively.) The actual rules for combination of symbols are more complicated and must be taken into account when designing a linking assembler.

An additional difference is that a relocating assembler must be able to recognize specialized directives. The ones that I have used in this article are:
\[
\begin{array}{ll}
. \text { ASECT } & \text { enter absolute mode } \\
. \text { CSECT } & \text { enter relative mode } \\
. \text { ENTRY } & \text { define a list of internal symbols } \\
. \text { EXTRN } & \text { define a list of external symbols }
\end{array}
\]

In addition to these, there should be a directive to explicitly declare a one-byte external symbol, so that the assembler will know whether or not to generate a short (page-zero) form of an ambiguous instruction. As previously noted, this is most relevant to 6502- and 6800-type processors.
As shown in the previous section, a relocating assembler need be only slightly more complex than an absolute assembler, and allows the use of modular soft-ware-generation techniques. Unless the system being developed is extremely small (eg: 512 bytes or less), its advantages easily outweigh its drawbacks.

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\title{
Varieties of Threaded Code for Language Implementation
}

\author{
Terry Ritter Gregory Walker \\ Motorola Inc, Mail Drop M2880 \\ 3501 Ed Bluestein Blvd \\ Austin TX 78721
}

Between a high-level language (HLL) and its underlying machine architecture lurk many language implementation techniques. These include the older techniques of interpretation and compilation, as well as newer ones like intermediate languages and threaded code. In this article, we will present four types of threaded code techniques for implementing intermediate languages. We will examine how these four logically equivalent techniques offer various trade-offs of execution speed, program storage, and use of processor resources.

\section*{Implemention of a Language}

The implementation of a high-level language on various logical or physical machine architectures involves such characteristic trade-offs as size of the language implementation, size of generated code, and speed of program execution. We will bypass other issues of high-level language use (eg: interaction, debugging, testing, etc) and concentrate on language implementation considerations.
Language implementation techniques can be logically divided into two categories: translation and interpretation.

Translation: Translation techniques replace elements of higher-level syntax with lower-level instructions that perform an equivalent operation. The resulting transla-

\footnotetext{
About the Authors
Terry Ritter and Gregory Walker are software engineers at the Motorola Microprocessor Design Group, where their exploration into the structure of computer languages led them to examine FORTH and other threaded languages for use as a possible software tool. Terry Ritter is one of the co-architects of the MC6809 microprocessor and has been involved with personal computing since 1974. Gregory Walker is on the IEEE floating-point standards committee and has been involved with microcomputers since 1975.
}
tion is then executed in order to run the program. A compiler is a computer program that translates high-level language programs into instructions of another language. Traditionally, assemblers and compilers translate their input into machine-level code.

Interpretation: Interpretation techniques directly execute the high-level language program. The interpreter is a program that sees the high-level language source program as a series of operation (op) codes used to guide its execution. The interpretive system appears to the user as a "virtual machine" that has the architecture of the highlevel language.

Any form of interpretation offers significant opportunities for implementing debugging tools. Tests performed as each command is interpreted can result in a programmer-controlled display of debugging information. This is the basis for trace or breakpoint facilities that can be included in the interpreter.

Combinations: Combination techniques may translate the sequence of characters representing a high-levellanguage keyword into a form that is easier to interpret. Most BASIC interpreters translate the BASIC keywords into one-byte tokens that are easier to identify. This technique avoids the continual string searches of a traditional interpreter, but executes a language that is syntactically unchanged from the high-level-language source program. (For our purposes here, the term syntax will specifically refer to the structural relationship between language elements.)

Intermediate language: Intermediate-language (IL) techniques translate the high-level-language programs into a language that is simultaneously easier to deal with and syntactically different from the original. Many compilers translate a high-level-language program into an intermediate language, which is then translated into AT QUASAR DATA PRODUCTS
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Intermediate-language techniques offer the advantage of machine independence of the source language.
machine code. When used in this manner, the intermediate language can allow global code-optimization techniques to be more easily applied.

Since the translation into the intermediate language is independent of the target machine, different compilers for the same target machine need only produce the simpler code of the intermediate language. Similarly, different code generators (which translate the intermediate language into machine language) can allow the same compiler to produce code for different computers. Inter-mediate-language techniques offer the advantage of machine independence of the source language and allow program portability, the ability to execute the same source program on widely different computers.

The intermediate-language representation of a program might also be interpreted instead of translated to machine code. To minimize interpretation overhead, we need complex and powerful machine-language routines. But machine independence is best accomplished by having simple, easy-to-write machine-language routines. This same trade-off of machine independence versus execution speed must be made in the design of any intermediate language. An example of this use of intermediate language is the pseudocode ( p -code) used to implement most versions of Pascal.

This article is principally concerned with a class of intermediate-language representations particularly suited to interpretation; these are known as threaded codes. Naturally, the intermediate-language code will be generated by a compiler or by some other translation program. We will not discuss the translation process, which is a function of the syntax of the high-level language and other programming considerations; rather, we will discuss the resulting intermediate language and its interpreter.

\section*{Aspects of Intermediate-Language Architecture}

An intermediate language is composed of a set of primitive operations (which, in combination, can express any algorithm) and storage capabilities for both internal and program data. In particular, it must be possible to pass data values between routines that make up the intermediate language. The intermediate-language program can use a fixed number of memory locations to simulate general-purpose registers, but then routines are needed that load (and store) each register from memory, as well as routines that simply move values between registers. If the intermediate language approaches the complexity of the original machine language, its use is of dubious value.

One approach that simplifies an instruction set is a "zero-address" or stack architecture. In this architecture, all operations will obtain values by pulling them from the stack and results will be returned by pushing them onto the stack. Only two operations with memory are now required: the "pull (from stack) and store (to memory)" operation and the "load (from memory) and push (on the stack)" operation. By designing a zero-address architec-

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next call return
\begin{tabular}{|c|c|}
\hline next & \begin{tabular}{l}
1. copy current list item to temporary storage \\
2. point list pointer to next list item \\
3. jump to machine code at address in temporary storage
\end{tabular} \\
\hline call & \begin{tabular}{l}
1. push current list pointer onto stack \\
2. load list pointer with address of the intermediate-language subroutine list \\
3. do "next"
\end{tabular} \\
\hline return & \begin{tabular}{l}
1. load list pointer with top of stack \\
2. do "next"
\end{tabular} \\
\hline
\end{tabular}

Figure 2: Diagram of direct-threaded code (DTC). Here, "temporary storage" refers to a memory location that is used to hold the address of the machine-code routine associated with the current unit of code.

1. copy current list item to indirect temporary storage
2. point list pointer to next list item
3. load code temporary storage with item at address in indirect temporary storage
4. jump to machine code at address in code temporary storage
call
1. push current list pointer onto stack
2. point indirect temporary storage to next list item
3. load current list pointer from indirect temporary storage
4. do "next'
return
1. load current list pointer from top of stack
2. do "next'

Figure 3: Diagram of indirect-threaded code (ITC). Here, "indirect temporary storage" and "code temporary storage" store the indirect and direct pointers to the machine code routine associated with the current unit of code.
ture into the intermediate language, the parameter transfer location is implied and need not be part of the intermediate language representation. (A stack architecture is certainly simpler than other architectures, but that does not mean it is better; many complex trade-offs that are beyond the scope of this article are involved.)

\section*{Threaded Code}

Threaded code is an intermediate-language implementation technique that organizes the control of program flow into a sequence of subroutine invocations. No other aspects of the language are represented in threaded code. Threaded code is especially applicable to interpretation; the interpretation process consists of transferring control to the routines selected by the threaded-code op codes. The functions available in the intermediate language are provided by the subroutines that are invoked and are not an inherent part of the threaded code itself.
[The characteristics of the language FORTH are independent of its current implementation via threaded code. FORTH enthusiasts often blur the distinction, attributing the language's speed and compactness to the language instead of to its threaded-code implementation. I think this is an important point to remember when talking about the advantages of FORTH....GW]

Threaded-code intermediate languages are especially applicable to the implementation of virtual machines embodying zero-address architectures. As such, the technique of using threaded code to implement a language can be applied to, for example, Pascal (using the p-code intermediate language), LISP interpreters, or, of course, FORTH. We classify four varieties of threaded code: subroutine, direct, indirect, and token.

All varieties of threaded code consist of a data structure that is a sequence of unique subroutine identifiers. Traditionally, threaded code has been kept close to the machine level and has included actual pointers to the subroutines (which themselves may be either intermediate language or machine code). Also traditionally, a portion of the processor resources-in particular, processor registers-has been dedicated to the use of the threaded-code interpreter. As we shall see, neither absolute pointers nor register resources need be used to implement threaded code.

\section*{Implementing Threaded Structures}

We will now describe the structures associated with the various types of threaded code. Figures 1 through 4 present diagrams of subroutine-, direct-, indirect-, and tokenthreaded code structures, respectively, along with a description of the three operations, next, call, and return, which make up the complete threaded-code interpreter. In the diagrams, the notation " \(\rightarrow \mathrm{A}\) " means a pointer to the memory location labeled " A ".

Subroutine-threaded code: A sequence of subroutine calls with no other embedded instructions implements an intermediate language. Each subroutine call may be considered a single intermediate-language operation, which need not be related to the underlying machine architecture. Subroutine-threaded code (STC) is a control mechanism that is widely supported at the machinehardware level.

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subroutine calls is rarely used by programmers (who have no reason to resist obvious opportunities for optimization), but it is sometimes used by compilers. It is the most general intermediate language possible, and it retains the advantages of machine independence by not generating in-line machine language. (The difference in the form of subroutine call and return instructions on various computers is usually trivial.)
Subroutine-threaded code will incur less execution overhead than most intermediate languages because its interpretation is handled by hardware rather than by a sequence of instructions. Furthermore, subroutinethreaded code can be optimized by using in-line machine code for operations where subroutine overhead is excessive, an advantage unobtainable with other types of threaded code. Of course, the resulting optimized code is no longer machine-independent; the additional translation step converts the intermediate language into object code for a particular machine.
Direct-threaded code: Direct-threaded code (DTC) may be considered a sequence of machine-language subroutine calls with the "call" op code removed. This results in a list of addresses, each of which points to a machinelanguage subroutine. Since the direct-threaded program includes no op codes, a short machine-language program must be written to read the next address in the list and transfer control to that address. Traditional direct-


Figure 4: Diagram of token-threaded code (TTC). Since tokens can be made shorter than addresses, this makes the threaded code more compact, but the table lookup makes the resulting code slower. Here, the "indirect token" is the contents of the table entry that matches the current token of code.
threaded code implementations do not allow the use of true subroutines at the machine level but instead require that each routine terminate by executing the next operation.
In order to call direct-threaded routines (see the instructions for "call" in figure 2), machine-language code (executing the instructions for "call") must be included at the beginning of each direct-threaded routine to put the current value of the list pointer on an address stack, load the list-pointer register with the start address of the list of routine addresses for this just-begun, direct-threaded routine, and execute the next operation.
The next operation (coded here as in-line machine code) causes the computer to execute the routine pointed to by the list pointer, regardless of whether the routine pointed to is another intermediate-language routine or a machine-language routine.
In order to return to a higher level of nesting, the last list item in an intermediate-language routine points to the code for the return operation. When executed by the next operation, this operation recovers the previous value of the list pointer from the stack, then executes the next operation, which in turn executes the first routine past the routine the computer just returned from.
Thus direct-threaded code is implemented in three operations: next, call, and return.
Indirect-threaded code: Indirect-threaded code (ITC) consists of a list of addresses, but each address points to another address which then points to the machine-code routine. (See figure 3.) As compared to direct-threaded code, in indirect-threaded code, the interpreter must go through an extra level of indirection. Indirect-threaded intermediate-language subroutines do not contain ma-chine-language code for the call operation, and one advantage of indirect-threaded code is that a compiler using it need only produce pointers. By manipulating only pointers, the compiler generates intermediate-language code that does not include machine-language code itself; thus it is independent of the target machine. However, a disadvantage of indirect-threaded code is that the interpreter has the overhead of an extra level of indirect addressing.
Token-threaded code: The varieties of threaded code previously mentioned contained pointers that were actual addresses of the subroutines in memory. Using memory addresses to select routines wastes storage because the number of subroutines in the system is far smaller than the number of memory locations. A savings in inter-mediate-language program size can be obtained by using short tokens to identify the subroutines to be invoked. Typically, token-threaded code (TTC) can be implemented by using the current token to index into a table of subroutine addresses. (See figure 4.)

\section*{High-Level Descriptions of Threaded-Code Interpreters}

Listings 1 thru 3 illustrate the logical implementation of direct-, indirect-, and token-threaded code, respectively. The program descriptions are written in a high-level language that is similar in appearance to Pascal. It differs from Pascal in that the variables are not declared as standard Pascal data types. Also, the next, call, and return operations are not written as Pascal procedures; this was done to remain faithful to actual implementations where

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\hline PL/M & 1.71 \\
\hline PASCAL COMPILER & 1.89 \\
\hline \begin{tabular}{l} 
PASCAL \\
lNTERPRETER
\end{tabular} & 17.07 \\
\hline \begin{tabular}{l} 
BASIC \\
INTERPRETER
\end{tabular} & 34.57 \\
\hline
\end{tabular} \(\mathbf{l}\)
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these three code segments are reached by jump instructions rather than by subroutine calls.

Several other notational conventions used in these listings may also need explanation. The data type pointer means an actual machine address. If ip is a pointer variable, then \(\rightarrow\) ip means the value at the location which is pointed to by the address in variable ip. Therefore, the statement
\[
\text { goto } \rightarrow \text { ip; }
\]
means jump to a new location using the contents of variable ip as the address at which to proceed with execution.

\section*{Implementation Concerns}

The traditional implementations of threaded-code interpreters have had one or more machine registers dedicated to the exclusive use of the interpreter; implementations on microcomputers have tended to use all microprocessor resources. One problem with these implementations is that all machine-language routines (where all real computation is done) must save processor registers before modifying them and must restore them before returning to the interpreter.

Additionally, this use of machine resources, simply for the transfer of control, obstructs the use of standard machine-language subroutines that pass parameters through the registers. In the context of microcomputer


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Listing 1: Description of a direct-threaded code interpreter in a Pascal-like language. See figure 2.
```

const pointer_length = (length of an addrets pointer);
cal1_code_langth = (length of "cali" code segment);
var list_pointer: pointer; { interpreted progrem counter }
list_item: pointer; \& contains threaded-code item b
label nert,call, return;
nent: list_item:= ^list_pointeri
ifst_pointer : = list_pointer + pointer_length;
goto nlist_item;
call: push_on_stack(list_pointer),
C The value of list-item was se.t by the preceding
list_pointer := list_item + call_code_lengthr
< The following code duplicates the "next" operation. }
list_item := ^list_pointeri
list_pointer := list_pointer + pointer_lengthi
goto^^ist_ittom;
return: list_pointer := pop_from_steck();
The following code duplicates the "next" operation. z
list_item:= Alist_pointar;
list_pointer := list-pointer + polnter_length;
goto al1st_item;

```

Listing 2: Description of an indirect-threaded code interpreter in a Pascal-like language. See figure 3.
```

const pointer length = (length of an address pointer) s
var list_pointer: pointer, ( interpreted program counter
list_Item: pointer; {containt threaded-code item j
code_ointer: pointer, (points to ectual machine code, ,
label next,call,returni
next: list_item := ^list_pointer;
list_pointer : = list_pointer + pointer_length;
code_pointer := Alist_itam; { here is the extracion }
goto ^code_pointer;
call: push_on_stack(list_pointer);
The value of list_item was set by the }
preceding "ment operation
list_pointer:= list_item + pointer_length;
The following code duplicates the "next" operation. ?
list_item:= ^list_pointer;
list_pointer := list_pointer + pointer_lengths
code_pointer := ^list_item;
goto ncode_pointer;
return: list_pointer := pop_from_stack();
\& The following code duplicetes the "next" operation. }
list_item:= nlist_pointeri
list_pointer := list_pointer + pointer_lengths
code_pointer := ^list_item;
goto ncode_pointer;

```

Listing 3: Description of a token-threaded code interpreter in a Pascal-like language. See figure 4.
```

const token_length = (length of token),
token_length m (length of tokme), (length of "eall" code segment),
call_code_length = (length of "esil" code segment):
label next,cell,return;
nert: token_item:= ^list_pointer;
list_pointer := list_pointer + tolmn_length;
code_pointer : m tableftoken_iteml;
token_iten : = ^code_pointeri
code_painter : = tobleltolen_iteml;
goto Acode_pointer;
call: push_on_stack(list_pointer),
The value of the code_pointer was 有t by the preceding;
"mext" operation.
list_pginter :=code_pointer + cald_code_length;
Thepointer following code duplicates the "next" operation.
token_itom:= ^list_pointer:
list_pointer := list_pointer + token_lengthi
code_pointer := table[token_item];
goto acode_pointer;
return: list_pointer : pop_from_stack();
UTn: list,olnter, code duplicatase the
Thefollowing code duplicetas the "nert" operation.
token_item:= Alist_pointer;
listpointer := list-pointer t tok
codepointer := tab
list pointer : = list pointer + token length;

```
```

                                    { 8-bit token, 
    ```
```

```
                                    { 8-bit token, 
```

```


```

```
    listpointer: pointer; { interproted program counter } {
```

```
```

```
    listpointer: pointer; { interproted program counter } {
```

```


```

```
    table: arrayli.. toknumber
```

```
```

    table: arrayli.. toknumber
    ```


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Listing 4: A simple direct-threaded code interpreter for the MC6809 microprocessor.

systems (which may want to use read-only memory modules), this limitation requires that special "header" and "trailer" code be written to move data values used by the intermediate language to and from the registers used by previously written machine-language code.

It is also possible to eliminate the use of processor resources in an intermediate language by storing the interpreter's "registers" in memory; this leaves the processor free for use by machine-language code at the expense of additional overhead during interpretation. [This overhead consists of having to move these registers between memory and the hardware registers of the host processor when you want to manipulate the contents of the interpreter registers....GW] The use of absolute locations in memory would itself be a problem, because these locations can then conflict with locations used by other software packages. By saving the intermediate-language registers on the stack, the language may be made inde-


Listing 5: A simple indirect-threaded code interpreter for the MC6809 microprocessor. In this and listings 6 thru 8, each block of information in lowercase is a "stack picture"-ie: a diagram of what is on the stack at that particular place in the code.


Listing 6: A more complex direct-threaded code interpreter for the MC6809 microprocessor. Execution of the intermediatelanguage subroutine starts at the label ENTRY.


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It is possible to conceive of a mass market for software; such a market would allow high-quality programs to be distributed at low cost. We will assume that such code will be distributed in the form of read-only memory modules, so that a purchaser actually receives a physical product for his money. Furthermore, the memory needed to store the program is included in the purchase price, a characteristic not obtained with distribution on magnetic media. Software piracy will be possible for advanced hobbyists, but these represent only a small portion of the consumer market.
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able to do so. Given machine-language distribution, the market is already limited to those users with a particular processor; it should not also be limited to those users with a particular computer system.

Software can be written such that it functions properly on systems that use different locations for programmable memory, read-only memory, and input/output (I/O) devices, as well as systems that use completely different I/O devices. The system-independent read-only memory must be written in code that is position independent, and it must also include features for linking to other similar modules. These criteria can be satisfied with machinelanguage code (on certain processors) or with a correctly designed intermediate language. Widest distribution requires such properly written code.

\section*{Machine-Language Examples of Threaded-Code Interpreters}

Here we present assembly-language code for the Motorola MC6809 microprocessor which implements complete interpreters for direct-threaded code, indirectthreaded code, and token-threaded code. Most of these listings are punctuated by "stack pictures" (typed in lowercase) that represent the current state of the stack at various points in the listing; visualization of the stack is often crucial to understanding the interpretive process.

An illustration of subroutine-threaded code (using subroutine jump and return instructions) would be trivial, and thus is not included. However, it should be noted that a position-independent form of subroutinethreaded code is available on computers with long rela-

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21 = UPDATE END MONTH FILES MAINTENANCE
\(22=\) PRINT CASH FLOW FORECAST
\(23=\) ENTER/UPDATE PAYROLL (NOT YET AVAILABLE)
\(24=\) RETURN TO BASIC
WHICH ONE? (ENTER 1-24)
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tive branch instructions (eg: the LBSR, long branch-tosubroutine, and RTS, return-from-subroutine, instructions on the MC6809).
Listing 4 illustrates a very simple implementation of a direct-threaded code interpreter. This particular implementation is very fast, but it has the following undesirable properties:
- it requires a special machine-language return instruction (ie: JMP \([, Y++\) ]);
- it reserves the \(Y\) register for use by the interpreter;
- it requires that the interpreter location (the address of RETURN) be known to the compiler, making the resulting intermediate-language code definitely position-dependent.

In operation, the Y register points to the next address in a direct-threaded code list; that address, of course, points directly to machine code. Executing the operation JMP \([, Y++]\) (indirect, autoincrement by 2) causes the machine to start execution at the address contained in the list element; simultaneously, the Y register is updated to point at the next item in the list of addresses.

The single instruction JMP \([, Y++\) ] ends each machine-language subroutine. By reserving a processor register for use as the current thread pointer, a speed advantage is obtained; transfer of control using JMP \([, \mathrm{Y}++]\) requires nine machine cycles (on the MC6809), while a JSR-RTS pair requires thirteen.

The situation becomes more complex when control is transferred to a subroutine composed of intermediatelanguage statements. Machine-language instructions are included at the beginning of the intermediate-language subroutine to perform the call operation. The Y register may be thought of as the topmost location of the stack of intermediate-language return addresses; its contents are pushed onto the stack, and \(Y\) is loaded with the address of the start of the intermediate-language subroutine list.

The last item in an intermediate language list is the address of the return routine. This recovers an old inter-mediate-language pointer from the stack and continues interpretation where it left off when it did a subroutine call.

In listing 5, we show a very simple indirect-threaded code interpreter. As in the previous example, the interpretation process is fast, but again it has the following limitations:
- it must use a position-dependent, machine-language return instruction (eg: JMP NEXT);
- it uses the Y register to hold the list pointer;
- it still requires that the compiler generate positiondependent pointers to the CALL and RETURN routines.

Listing 6 is an example of a moderately complex directthreaded code interpreter. It is somewhat slower than the simple interpreter in listing 4, but it uses a standard RTS instruction to return from machine-language routines. Thus, the machine-language routines need not contain pointers to the next operation. Still, this advantage is bought at the expense of additional machine-language code in each intermediate-language subroutine. The intermediate-language subroutines themselves do have

Listing 7: An improved direct-threaded code interpreter for the MC6809 microprocessor. This interpreter does not use any of the microprocessor registers.
\begin{tabular}{|c|c|c|}
\hline & \(s\) - Pptr to new addr of "ne old thread & \[
\begin{aligned}
& \text { thread } \\
& \text { axt" } \\
& \text { ptr }
\end{aligned}
\] \\
\hline \multirow[t]{9}{*}{CALL:} & PSHS D & SAVE D \\
\hline & LDD 2, 5 & GET NEW PTR \\
\hline & STD 4, 5 & THREAD PTR \\
\hline & \(s->d\) & \\
\hline & space & \\
\hline & new thread & \(p \mathrm{tr}\) \\
\hline & old thread & ptr \\
\hline & PULS D & RECOVER D \\
\hline & LEAS 2, 5 & DELETE SPACE \\
\hline \multirow[t]{2}{*}{NEXT:} & LEAS -4, 5 & MORE SPACE \\
\hline & \[
\begin{aligned}
& s->\text { space } \\
& \text { space } \\
& \text { thread ptr }
\end{aligned}
\] & \\
\hline \multirow[t]{19}{*}{RETURN:} & PSHS X, D & SAVE \(X, D\) \\
\hline & \multicolumn{2}{|l|}{\(s \rightarrow>d\)} \\
\hline & \multicolumn{2}{|l|}{\(\underline{ }\)} \\
\hline & \multicolumn{2}{|l|}{\multirow[t]{2}{*}{space}} \\
\hline & & \\
\hline & \multicolumn{2}{|l|}{thread ptr} \\
\hline & LDX 8, 5 & GET THREAD PTR \\
\hline & \[
\text { LDD , } X++
\] & GET NEXT MACHL ADDR \\
\hline & STX 8, 5 & STACK THREAD PTR \\
\hline & STD 4, 5 & STACK ROUTINE ADDR \\
\hline & LEAX NEXT, PCR & GET ADDR OF "NEXT" \\
\hline & \[
5 T X 6,5
\] & SAVE AS MACHL RETURN \\
\hline & \multicolumn{2}{|l|}{\(5->d\)} \\
\hline & \multicolumn{2}{|l|}{\(\times\)} \\
\hline & \multicolumn{2}{|l|}{\multirow[t]{2}{*}{machl routine}} \\
\hline & & \\
\hline & \multicolumn{2}{|l|}{addr of "next"} \\
\hline & PULS D, X, PC & GO TO MACHL ROUTINE \\
\hline & \multicolumn{2}{|l|}{s Maddr of "next" thread ptr} \\
\hline I-CODE: & JSR CALL <inst & 13 ... (RETURN) \\
\hline
\end{tabular}

Listing 8: Token-indirect token-threaded interpreter for the MC6809 microprocessor. Because of the use of two levels of lookup, this interpreter is completely position independent.


Listing 8 continued on page 222

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Listing 8 continued:
\begin{tabular}{|c|c|c|}
\hline LDU & 10, 5 & GET TABLE ADDR \\
\hline LDX & 14, 5 & get thread ptr \\
\hline LDB & , \(\mathrm{X}+\) & GET INDIRECT TOKEN \\
\hline STX & 14, 5 & SAVE THREAD PTR \\
\hline CLRA & & : \\
\hline ASLB & & : TWO BYTES PER TOKEN \\
\hline ROLA & & \\
\hline LDX & D, U & TABLE-RELATIVE INDIRECT PTR \\
\hline ADDD & 4, 5 & NOW ABSOLUTE \\
\hline TFR & D, \(x\) & \\
\hline LDB & , \(\mathrm{X}+\) & GET TOKEN \\
\hline STX & 12, 5 & SAVE INDIRECT PTR \\
\hline CLRA & & \\
\hline ASLB & & \\
\hline ROLA & & \\
\hline LDD & D. U & TABLE-RELATIVE MACHL ADDR \\
\hline ADD & 4, 5 & NOW ABSOLUTE \\
\hline TFR & D, X & \\
\hline STX & 6, 5 & SAVE AS UPCOMING PC \\
\hline LEAX & NEXT, PCR & ADDR OF NEXT \\
\hline STX & 8, 5 & SAVE FOR MACHL RTS \\
\hline PULS & D, X, U, PC & RECOVER REGS + GQ! \\
\hline
\end{tabular}
```

s -> addr of "next"
table addr
indirect
thread ptr

```

CALL: PSHS D
SAVE D \(5->d\)
                                    addr of "next"
                                    table addr
                    indirect
                thread ptr
\begin{tabular}{ll} 
LDD 4, S & GET TABLE ADDR \\
STD \(2, S\) & MOVE IT \\
PULS D & RECOVER D \\
BRA NEXT &
\end{tabular}

RETURN: PSHS D SAVE D
                s \(->\mathrm{d}\)
            addr of "next"
            table addr
                old indirect
                thread ptr 1
                thread ptr 2
\begin{tabular}{lll} 
LDD & 4, S & GET TABLE ADDR \\
STD & 6,5 & MOVE IT \\
LDD & 0,5 & RECOVER D \\
LEAS & \(6, S\) & DISCARD JUNK
\end{tabular}
pointers to the return operation, of course (making the code position-dependent), and the interpreter reserves the \(Y\) register for its own use.

Listing 7 illustrates a direct-threaded code interpreter that does not reserve any processor registers; this interpreter also allows the return from machine-language routines by means of a standard RTS instruction. The absolute locations of the interpreter call and return routines must be included in each direct-threaded code subroutine; this usually precludes the distribution of such subroutines in read-only memory.


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\begin{tabular}{|c|c|c|c|c|}
\hline Type of Threaded Code & MC6809 Machine Cycles Used & Ratio of Cycles Used & Relative Size of Resulting IntermediateLanguage Code & Can this Code Be Marketed to All Users of a Given Microprocessor? \\
\hline Subroutine-threaded code & 91 & 1.0 & 3 & no \\
\hline Relative subroutine-threaded code & 98 & 1.1 & 3 & yes \\
\hline Simple direct-threaded code (listing 4) & 93 & 1.1 & 2 & no \\
\hline Simple indirect-threaded code (as in listing 5) & 371 & 4.1 & 2 & no \\
\hline Moderately complex directthreaded code (as in listing 6) & 228 & 2.5 & 2 & no \\
\hline Improved direct-threaded code (as in listing 7) & 552 & 6.1 & 2 & no \\
\hline Token-threaded code (as in listing 8) & 1083 & 11.9 & 1 & yes \\
\hline
\end{tabular}

Table 1: Comparison of threaded-code techniques. Notice that only two forms of threaded code, the relative subroutine-threaded code and the token-indirect token-threaded code are sufficiently system-independent to be used for mass distribution to (potentially) all users of a given microprocessor.

A possible alternative would be to modify the directthreaded code interpreter in listing 7 to use strictly selfrelative pointers. Then by including code for call and return in each read-only memory device, a form of distributable direct-threaded code might be obtained. However, because the read-only memory still contains machine-dependent code, the use of direct-threaded code in a read-only memory environment offers little advantage.
The improved direct-threaded code interpreter allows the use of most previously coded machine-language modules and allows these routines to pass parameters through the processor registers. Routines cannot pass parameters on the hardware stack (which is used to maintain the state of the interpreter), but could easily use the user stack of the MC6809 microprocessor for parameter transfer.
A similarly improved interpreter could be built for indirect-threaded code, but the position-independence problem is inherent in this intermediate language as well. Each indirect-threaded subroutine must include a pointer to the call routine, thus making the resulting
intermediate-language code unsuitable for distribution in read-only memory.
However, it is possible to build a token-thread interpreter that has a completely position-independent intermediate-language representation. Listing 8 shows one implementation that achieves these goals. Notice the increased complexity and overhead when compared to our original direct-threaded code interpreter.
This token-thread interpreter produces intermediatelanguage code that is more compact than that produced by previously mentioned interpreters. The advantage of a compact representation need not affect execution speed severely; remember that the overall efficiency of any interpretation scheme (including the hardware interpretation of op codes) depends more upon the work actually accomplished than the time spent in the interpretation process itself.

This particular implementation is essentially a tokenindirect token-thread interpreter. Two levels of token lookup are involved so that neither machine-language nor absolute addresses need be included as part of the intermediate-language subroutine. Of course, perhaps

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other, more advantageous forms of token-threaded code interpreters are possible. However, we have shown that there is no longer a question whether positionindependent threaded code is possible; now the question is: "at what cost?"

\section*{The Cost of Implementation}

The claims made for threaded-code techniques in an intermediate-language implementation include reduced program storage and high speed of execution. Unfortunately, these claims are justified only in certain limited contexts. The original implementations of threaded code, which occurred on the Digital Equipment Corporation PDP-11, made use of the instruction JMP @(Rn)+; this instruction jumps through a memory pointer while retaining the location of next in a register. This is equivalent to the MC6809 instruction JMP [, r++] .

The instruction JMP @(Rn)+ does not save a return address on a memory stack and thus is faster than a JSR instruction. In the environment of a single intermediatelanguage program that calls only machine-language subroutines, stacking and unstacking of the return address need not occur. Of course, when intermediate-language programs call intermediate-language subroutines, such stacking must occur in a process that will take longer than a normal JSR. Thus, for maximum speed, the threaded-code intermediate-language program should not call intermediate-language subroutines.

On the other hand, the instruction JMP @(Rn)+ does eliminate the in-line 16 -bit JSR op code for a \(50 \%\) code reduction (on the PDP-11). But the \(50 \%\) code reduction


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achieved on the PDP-11 (which uses a 16 -bit JSR op code) is only a \(33 \%\) code reduction on most microcomputers, which have 8-bit JSR op codes. (The LBSR instruction can be used in the case of the MC6809.) And if the motivation for threaded code is reduction of the intermediate-language code size, token-threaded code implementations can improve the storage efficiency by another \(50 \%\).

The two traditional forms of threaded code (direct and indirect) are optimized for the environment of a particular computer architecture that is represented by the PDP-11 (and also reflected in the MC6809). Consequently, many microcomputer threaded-code implementations have provided neither maximum code efficiency nor maximum speed and have devoured virtually all of the machine-level microprocessor resources. Comparisons of the four types of threaded code demonstrate that it is unlikely that the speed and code-efficiency maxima will ever coincide.

The main factor affecting code compaction is the use of subroutines instead of in-line code; but the use of subroutines inherently increases interpretation overhead. Since all methods of threaded-code implementation allow the use of subroutines, effects due to the use of subroutines can be disregarded and the efficiency of the implementation methods can be compared directly. Table 1 shows this comparison with values from the machine-language routines developed earlier (based on six next operations for each call and return operation).

\section*{Conclusions}

Languages that have been historically associated with threaded code will probably continue to use these techniques when implemented on microcomputers. New implementations should take advantage of the interpretive nature of threaded code to provide extensive debugging facilities. However, there is no excuse for the threaded-code implementor to prohibit the use of previously coded machine-language modules by eliminating parameter passage through microprocessor registers. Either the interpreter can be designed to keep these registers free, or special routines must be written by the implementor to save and restore these registers when using library routines stored in read-only memory.

Similarly, the motivation for distributing software in an open market (to many different users with many different systems) leads directly to the requirement for position independence. While the MC6809 directly supports position-independent code at the machine-language level, it is also possible to devise threaded-code intermediate languages that are position independent. But any intermediate language or interpreter that requires particular absolute storage locations is so obnoxious as to be unworthy of discussion in polite programming society. Absolute-address storage requirements are simply unacceptable in code written for mass distribution.

Within these constraints, the various forms of threaded code offer different trade-offs of speed and code efficiency. Because these forms are logically equivalent, a single compiler could be used to generate any of them at the user's choice. Thus, without changing the source program, a threaded-code technique could be selected that would give the desired trade-off between speed and code efficiency for a particular situation.

In the end, threaded-code implementation techniques
are neither particularly compact nor are they particularly fast. Continued development of direct-threaded code structures could result in a language representation that would look more like Pascal p-code than threaded code. Threaded code does offer a conceptually simple and general control-transfer technique that displays a clear boundary between interpretation and language. However, threaded code is probably not an optimal representation for any particular language, including FORTH. I

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\title{
New Cultures from New Technologies
}

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Seymour Papert, Project LOGO, Massachusetts Institute of Technology, Artificial Intelligence Laboratory, 545 Technology Sq, Cambridge MA 02139
}

When I was asked to write this Education Forum for BYTE, I was in the process of correcting the proofs of my book, Mindstorms: Children, Computers and Powerful Ideas. (See reference 1.) There I struggled to present in two hundred pages a vision of a few ways in which computers might affect how children learn; it is challenging now to find the right 3000 words to convey something of the same vision. What images, what metaphors best capture for me the essence of the computer as it might enter the lives of children?

I start with an image, more general than the computer, that has helped me to think about how the world takes up any new technology. The first movies were made by setting the newly invented motion-picture camera in front of a stage where a play was performed just as plays always had been. Only after some time did cinema become more than theatre plus camera. When it did, what emerged was

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something original and unique, a whole new culture with new modes of thinking and new breeds of people-stars, directors, scriptwriters, cameramen, critics, and audiences whose sensitivities, expectations, and ways of seeing were quite different from those of the theatre-goers of the past.

So too with the computer. The first instinct of educators is to couple the new technology to their old methods of instruction. My vision is of something much grander. So I dream of using this powerful new technology not to "improve" the schools we have always known (and, to be honest, hated) but to replace them with something better. I do not believe that this something will look anything like what is now known as "computer-aided instruction" (CAI). I think it will be more like the growth of a new culture, a "computer culture" in which the presence of computers will have been so integrated into new ways to think about ourselves and about the subject matters we learn that the nature of learning itself will be transformed.

In thinking about the nature of such potential transformation, the LOGO group of the Massachusetts Institute of Technology (MIT) Artificial Intelligence Laboratory has been guided by the idea of creating computer-based environments in which mathematics and other areas of "formal" learning can be learned in a natural fashion, much as a child learns to speak; and applying concepts from artificial intelligence to children's learning, to help children become articulate about, and thus gain control over, the learning process. Before developing these ideas, I would like readers to clear their minds of a misleading but common image. People generally think about computers in schools as a scarce resource to which students have occasional access. It is time we learned to think in terms of a computer for every child, and we should think about children having access to computers from infancy. If we think in these terms, we begin to recognize that there is a clear discontinuity between the current ideas about using computers in schools and the situation of the future. I really believe that almost everything being done today is only relevant to the future in that it sets a bad example so that people become accustomed to primitive models.

A natural place to begin a search for "something new in education" is to look for examples of highly successful learning. For me the most dramatic image of successful learning is the way children learn to talk. This learning contrasts with school learning in many ways, of which I think two are most important. First, it is highly successful: all children learn to speak the colloquial dialect in which they grow up. Second, it has none of the technical paraphernalia of schooling-no curriculum, no set lesson times, no quizzes, no grades, no professional teachers. It is part of living. I call it learning-without-teaching or


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Piagetian learning (after the Swiss philosopher-scientist Jean Piaget who has done more than anyone else to show us how very much children learn in this way).
Much of the work done to date in the whole area of computers and education-eg: CAI-has promoted a style of learning that gives the impression of a child being "programmed" by the computer. Our approach has been diametrically opposed to that. By striving to make the computer's processes as transparent as possible and creating activities in which children "teach" (ie: program) computers in a well-structured, procedural language like LOGO, we have aimed toward putting children in control of their own learning. Obviously, I cannot hope to explore these ideas in much depth in a short space. What I shall try to do is to describe a couple of learning environments we have created which I believe challenge the fundamental assumptions our society makes about children and learning.

\section*{Mathland}

The belief that only a few people are mathematically minded is a truism in our culture and a cornerstone of our educational system. It is therefore sobering to reflect on the flimsiness of our reasons for believing it. In fact, the only evidence is crass empiricism: look around and you will see that most people are very poor at mathematics. But look around and see how poor most Americans are at speaking French. Does anyone draw the conclusion that most Americans are "not French-minded?", that they are not capable of learning French? Of course notl We all know that these same people would have learned to speak French perfectly well had they grown up in France. If there is any question of lack of aptitude, the aptitude they lack is not for French as such but for learning French in schools.

Could the same be true of mathematics? Could there be a place, a "mathland," which is to mathematics as France is to French, where children would learn to speak mathematics as easily and as successfully as they learn to speak their native dialect?

I believe that the answer is Yes. In Mindstorms I suggest that the world we live in contains pockets of mathland, which explains why all children learn some mathematics spontaneously (eg: one-to-one correspondences, conservation of number, reversibility of logical operations) and some children become very good at it. Here I have space only to talk about some ways in which the world could become much more of a mathland for everyone.

Computers are the Proteus of machines: they take on many different forms. One of their manifestations is as mathematics-speaking beings. If children grew up surrounded by such beings, the learning of mathematics might very well be much like the learning of spoken language. Developing and testing this image has become a central research question for us at MIT: under what conditions will children talk in mathematical languages to mathematics-speaking computers? The results have already convinced us that the idea of mathland is fundamentally sound and that, indeed, what the mathematics schools fail to teach can be learned successfully on the model of picking up living languages.

But computers do not automatically create that result. For example, instructing computers in FORTRAN to


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manage inventories is of no interest to the average child. Babies brought up in IBM computer centers will be no better at mathematics than any others. They may even be worse (and their other lapses of culture might be more disturbing). In order for computers to play the role of mathland for a child, two conditions are necessary: the computer must understand a language a child can learn (and love to learn), and the computer must be able to do something for the child.

\section*{Euclidean Geometry \(\rightarrow\) Cartesian Geometry \(\rightarrow\) Computational Geometry}

Turtle graphics is this kind of mathland. It was first developed in our laboratory as part of the programming language LOGO and then taken over by several other languages including Smalltalk and UCSD-Apple Pascal.

A lot of experience has taught us that computer graphics can be a great turn-on. People of all ages enjoy putting images on the screen, and when these images can be made to move and change color, they acquire a dimension completely lacking in conventional pencil-and-paper drawing. At the heart of the work on turtle graphics is the idea of developing a new kind of geometry-"turtle geometry"-which provides powerful and yet easily accessible means to manipulate shapes and motions. To put this in perspective, recall that you probably encountered at school at least two styles of doing geometry: Euclid's style (primarily logical in structure) and Descartes' style (primarily algebraic). Turtle geometry is a new style matched to the computer: it is a computational style of thinking about geometry. The difference in spirit is illustrated by how one thinks about a familiar geometric object in Cartesian and in turtle geometry. Descartes taught us to think of the circle as an equation such as:
\[
x^{2}+y^{2}=R^{2}
\]

In turtle geometry it is possible to use such equations, but the natural way to think about a circle is as a process. To do this, turtle geometry adopts as its fundamental concept an entity called a turtle whose properties include its position (as does the point in Euclidean and Cartesian geometry) and also its heading. At any particular time, it is at a position and is facing in a particular heading. The position and the heading are changed by commands that are built into a programming language. Among these are FORWARD < some number> which causes the turtle to move in the direction of its heading without changing the heading, and RIGHT <some number \(>\) which causes the turtle to change the heading while keeping the position fixed; ie: to pivot in place. Given these commands, a program in LOGO to draw a square of a certain fixed size takes the simple form:

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\section*{TO SQUARE SIZE REPEAT 4 [FORWARD :SIZE RIGHT 90]}

Now we can think of a circle as generated by:

\section*{TO CIRCLE REPEAT 360 [FORWARD 1 RIGHT 1]}

More sophisticated programming leads to circles of variable diameter and even to letting the number of steps go to the limit, but the simple example will illustrate the main point I want to make here. Children can solve the problem of drawing a circle by using a very powerful heuristic principle: play turtle, walk out yourself what you want the turtle to do and describe what you did in turtle language. The children are practicing a lot of powerful ideas. They are exposed to the idea of using heuristic knowledge, they are learning to think of formal mathematics as rooted in (not opposed to) intuitive bodymathematics, and they are using mathematics as a language; moreover, they are learning to think about mathematics not as a ritual to be learned by rote but as an instrument to be used for personal ends.

\section*{Computer as Pencil}

This image refers to the many uses of the pencil: it is used to scribble, to doodle, to draw, to write, to work sums, or to chew on. It is used for illicit notes as well as for official assignments. I see the computer in the life of the child as equally ubiquitous and equally versatile. I also see it as equally personal. Children own pencils, they are not intimidated by them. This should be equally true of the child's personal computer.
The metaphor of the pencil is a good way to summarize some of the ways the image of the computer I am building up here differs from the one that is becoming established in schools.

Suppose that the only access children had to pencils (which I take in a generic sense including pens, crayons, and the like) was at school, and even there "pencil time" had to be scheduled on the one or two pencils available to each classroom. This might (or might not) be better than having no pencils at all, but clearly under those conditions the pencil would not play the important role it now does in the intellectual development of children from infancy onwards. In my vision the computer will become as free a resource as the pencil now is.
Second, there is the question of the power of the computer to be used flexibly for many purposes. The microcomputers in schools today can barely be used flexibly by those few who have the inclination to become virtuoso programmers in BASIC. This is very different from the model of the pencil that can be picked up by everyone -even the one-year-old infant-and also used by the most sophisticated writer or artist. LOGO and Smalltalk are only first steps toward programming languages that will truly satisfy our slogan: "No threshold and no ceiling." A child of five or less should be able to write a program in the first few minutes of contact with the computer and a computer scientist should find the system congenial and rich.

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\author{
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}

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Third, I mention the use of the pencil and of the computer as writing instruments. The computer is rapidly becoming the standard writing instrument. Most journalists use word processors, as do increasingly many offices. I am using one as I compose this article. But the schools are not offering children this facility, although one could argue that it is children who are in most need of writing aids. The reason is clearly linked to the ratio of computers to students. One or two computers per class simply does not give enough access for the computer to become the primary writing instrument. On the other hand, one computer per child, which is how I think we should be thinking about the future, could lead to massive changes in the way children develop writing skills. A well-designed text editor makes editingsubstitution and deletion of words, shifting of sentences or paragraphs, and so on-an easy and aesthetically acceptable process. Compare the situation of a child attempting such a task with paper and pencil: the mess of multiple erasures and labor of rewriting means that the first draft is almost always the final copy. I have seen children who hated writing become avid writers when they have a text editor at their disposal. Wide availability of computers with text-editing capabilities might lead to even more fundamental changes in children's relation to alphabetic representation of language. Consider the implications of the following story:

Recently I observed the first group of nursery-school children working with a computer called the Lamplighter Computer (a Texas Instruments 99/4 personal computer with additional memory to support an extended version of LOGO and a real-time text-editing system) developed over the past few years through a collaboration between our research group at MIT and Texas Instruments. A four-year-old girl (I shall call her Robin) was working with some dynamic graphics programs that allowed her to make shapes appear on the screen, move, change color, and stick together by pushing one or another of some fourteen keys on the keyboard. The plan was that when Robin was tired of using a program she would ask the teacher to set up a new program. And this is in fact what she did for the first few times. But then Robin took charge of the whole process and began typing the control characters necessary to interrupt a program she no longer wanted and typing the names of the programs she did want, even though this was at a measured rate of about two characters per minute. In breaking out of the role of dependence on adults, Robin symbolized the fact that computers will enable children to break out of many of the roles into which technological primitivity and social custom have cast them.

We should not pass too quickly over the significance of the simple fact that Robin could make things happen by typing words. It might well be the first time in her life that alphabetic language actually served a real and personal purpose. The spoken language and its precursors enter from the first year of life into a significant process of interaction with the world. Learning to speak empowers the child. But for most children the act of writing serves at most to gain the approval of adults. Could this be the reason children learn to talk so easily and so young


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CET INTO THE TIME MACHINE. DIGITAL PATHWAYS
while they learn to write with so much difficulty so many years later? Watching Robin left me more firmly convinced than ever of a conjecture I have pursued for quite a few years. Children could learn to write as early and as easily as they learn to speak if the environment in which they lived gave as much support to the alphabetic language as ours does to the spoken language. I have no doubt that if Robin had her own computer and could use it whenever she wished, and if this computer gave her access to enough exciting things to do, she would within weeks have mastered the keyboard, the alphabet, and enough of spelling and syntax to put her firmly on the road to the kind of mastery of written language that usually comes, if at all, well into the school years.

\section*{Meaning Versus Ritual in Learning}

The fundamental question for education is not how to improve schools but how to understand why schools are necessary. Why is some knowledge (like learning to talk) picked up so easily and naturally from the culture, while other kinds of knowledge seem to require deliberate, organized instruction 7 In Mindstorms I explore the many factors that make a difference. Here I have space only for one. Children learn to speak because it is a meaningful activity, a meaningful part of their lives. It is not surprising that children do not learn to write when writing serves no real purpose in their lives. I think the computer can change this. For Robin, alphabetic communication was beginning to become purposeful. As computers become increasingly available to children I would expect many children to share Robin's experience of writing as a meaningful activity. This shift-from meaningless ritual imposed from above to purposeful, self-directed ac-tivity-is also true of Mathland. No activity in school is experienced as more devoid of meaning than the parody of mathematics known as school math.

The harm done by making children learn ritualistically goes very deep. It develops the worst possible habits of learning. It undermines the individual's self-confidence as an independent intellectual agent: it infantilizes the child. A shift to more meaningful learning of fundamental subjects could have far deeper consequences than improved mastery of these subjects. It could mean that children become more effective learners with greater intellectual self-respect. And if this happens, not only the nature of children's learning but also the role of children in society may have changed.

I have hinted at a vision of profound, even revolutionary, change in how children learn. I think this might happen. We have the technology to make it possible. But there is nothing inevitable about it. Society has a very bad track record in making intelligent use of new technologies, and, in this case, many vested interests are threatened by the changes I envision. The "system" will react by defending its old ways. Already in schools we see computers being used to reinforce instead of displace the most ritualistic teaching methods. I believe that the most profound effects of computers on how children learn could occur outside of schools. In fact, I think that computers would tend to make schools as we know them obsolete. But most of my "official research" is concerned with how to use computers in schools. Research funds are easily available for the reformist goals of improving schools. I believe that the most profound effects of com-
puters could be to develop a new respect for children as independent intellectual agents. But most people in our country like to think of children as intellectually dependent.

How will it all work out? It is futile for me to play prophet, but worthwhile to bear some ideas in mind when thinking about the future. I want to end by mentioning an idea that encouraged me to think positively. I can best introduce it by comparing the education market with markets for other products. Suppose you invent a new kind of kitchen machine. If you can prove that there is a market of a million people, you will easily find the capital to develop the idea and get it out into the world. But if you invent a new approach to learning mathematics, the fact that a million people want it may be of no avail-a million people across the nation may still be a tiny minority with no clout in every school district. But once there are a few million owners of home computers capable of carrying powerful learning methods, you will have access to a market of individuals ready to spend personal dollars for the good of their children. The importance of this fact is not that it will enable good ideas now collecting dust on shelves to get out into the world. It will encourage inventive and ambitious people to enter the field of educational innovation in unprecedented numbers. It will be part of the creation of a new class of professionals and of entrepreneurs and perhaps even of "stars" analogous to what happened in the course of the emergence of cinema as a culture. The history of cinema has been the history of that culture. The future of computers in education will be indissociable from the story of the people who will make the computer culture.

\section*{References}

For more about Turtle Geometry see S Papert, Mindstorms: Children, Computers and Powerful Ideas. New York, Basic Books, 1980 (ISBN 0-465-04627-4, \$12.95). Also see H Abelson and A diSessa, Turtle Geometry, MIT Press, Cambridge MA (to appear 1981). For a bibliography of the LOGO group's internal publications, write to LOGO, clo MIT Artificial Intelligence Laboratory, 545 Technology Sq, Cambridge MA 02139. (Please include \(\$ 1\) for handling.)

Editor's note: \(A\) note in the introduction to the July 1980 BYTE editorial incorrectly states that Education Forum articles by Seymour Papert and James Garson were to appear in the August and September BYTEs, respectively. However, because of unavoidable scheduling considerations, Seymour Papert's article is appearing this month, and James Garson's article will appear in a future issue. We apologize for any inconvenience this change might have caused....CM

> Education Forum is an occasional feature in BYTE intended to foster debate about the uses of personal computers in the schools and colleges. We encourage reader participation. Contributors should supply their full names and addresses for publication, along with their telephone numbers, which will not be published.

\title{
Finally...Serious Expansion for the Alu-65
}

\title{
Khachiyan's Algorithm, Part 2: Problems with the Algorithm
}

\author{
G C Berresford, A M Rockett, and J C Stevenson \\ Dept of Mathematics \\ C W Post Center, Long Island University \\ Greenvale NY 11548
}

\section*{Numbering of figures, tables, listings, and equations is continued from Part 1.}

A paper published by the Soviet mathematician Leonid Khachiyan received widespread publicity in late 1979 as a revolutionary new solution to linear programming problems. In Part 1 last month, we discussed the details of Khachiyan's algorithm and its corresponding geometric interpretation. This month in Part 2, we will look at the practical problems in using the algorithm and will examine a BASIC program that uses the algorithm.

\section*{A Linear Programming Example}

The Whiz-Golly Computer Board Company makes two kinds of video boards: the Ohwow and the Hohum. Each board is handmade by Jim and then tested by Jack. Each Ohwow board takes Jim two days to complete, while he can make one Hohum board each day. Jack can test an Ohwow board in one day, but he needs two days for each Hohum. Like most basement entrepreneurs, Jim and Jack have many other things to do with their time. Jim will not make boards for more than four days a week; Jack will test them for no more than three days a week. If the profit is two dollars for each Ohwow board and three dollars for each Hohum, how many of each should they make per week to obtain the greatest profit?

This is a linear programming problem. It consists of a quantity to be maximized, the objective function, which is subject to a list of linear inequalities called constraints. If we let \(x_{1}\) denote the number of Ohwow boards made per week and \(x_{2}\) the number of Hohums made per week, the problem then is to maximize \(P=2 x_{1}+3 x_{2}\), where \(P\) is the profit per week in dollars.

Since Jim cannot make a negative number of Hohums in a week, the first constraints we find are the nonnegativity conditions: \(x_{1} \geq 0\) and \(x_{2} \geq 0\). In addition, we have the constraints imposed by the number of days that Jim and Jack work per week: for Jim, we have that \(2 x_{1}+\) \(x_{2} \leq 4\); while, for Jack, we have that \(x_{1}+2 x_{2} \leq 3\).

This problem may now be written in matrix form as:
\[
\begin{gather*}
\text { to maximize } P=\left[\begin{array}{ll}
2 & 3
\end{array}\right] \cdot\left[\begin{array}{l}
x_{1} \\
x_{2}
\end{array}\right]  \tag{7}\\
\text { subject to }\left[\begin{array}{ll}
2 & 1 \\
1 & 2
\end{array}\right] \cdot\left[\begin{array}{l}
x_{1} \\
x_{2}
\end{array}\right] \leq\left[\begin{array}{l}
4 \\
3
\end{array}\right] \\
\text { and }\left[\begin{array}{l}
x_{1} \\
x_{2}
\end{array}\right] \geq 0
\end{gather*}
\]

Of course, we could have combined the two constraint equation sets into one but, as most practical problems naturally include a nonnegativity condition, we will write it separately for emphasis.

\section*{The Dual Problem}

By a standard maximum linear programming problem we mean any problem of the form:
\[
\begin{align*}
\text { to maximize } & P=c^{\prime} \cdot x \\
\text { subject to } & A \cdot x \leq b  \tag{8}\\
\text { and } & x \geq 0
\end{align*}
\]
where \(\mathbf{A}\) is an m-by-n matrix, \(\mathbf{b}\) is a column vector in \(\mathbf{R}^{m}\), \(\mathbf{c}\) is a column vector in \(\mathbf{R}^{n}\), and \(\mathbf{x}\) is a column vector in \(n\) unknowns.

Since Jim and Jack may wish to minimize their expenses, we will also encounter minimization problems. A standard minimum linear programming problem is any problem of the form:
\[
\begin{align*}
\text { to minimize } & C=b^{\prime} \cdot y \\
\text { subject to } & A^{\prime} \cdot y \geq c  \tag{9}\\
\text { and } & y \geq 0
\end{align*}
\]
where \(A, b\), and \(c\) are as in (8) and \(y\) is a column vector in \(m\) unknowns.

The two problems given by (8) and (9) are called dual problems, and their solutions are closely connected. Suppose that \(x\) satisfies (8) and \(y\) satisfies (9). Then \(c^{\prime} x \leq\) ( \(\left.\mathbf{A}^{\prime} y\right)^{\prime} x=y^{\prime} \mathbf{A x} \leq y^{\prime} b=b^{\prime} y\) and we see that \(c^{\prime} x \leq b^{\prime} y\) for any \(x\) and \(y\) satisfying the respective constraint equations. Since we wish to maximize \(c^{\prime} x\) and to minimize b'y, it follows that any pair of solutions, say \(\bar{x}\) and \(\bar{y}\), must satisfy \(\mathbf{c}^{\mathbf{x}} \overline{\mathbf{x}}=\mathbf{b}^{\mathbf{t}} \overline{\mathbf{y}}\) and conversely.

To solve the pair of linear programming problems (8) and (9), we need only solve the following system of equations:
\[
\begin{align*}
& c^{\prime} x=b^{\prime} y \\
& A x \leq b \\
& A^{\prime} y \geq c  \tag{10}\\
& x \geq 0 \text { and } y \geq 0
\end{align*}
\]

The equality \(c^{t} x=b^{t} y\) is equivalent to the two inequalities \(c^{\prime} x-b^{\prime} y \leq 0\) and \(-c^{\prime} x+b^{\prime} y \leq 0\). The nonnegativity conditions \(x \geq 0\) and \(y \geq 0\) are equivalent to \(-\mathrm{I}_{n} \mathrm{x} \leq 0\) and \(-\mathrm{I}_{m} \mathrm{y} \leq 0\) where \(\mathrm{I}_{k}\) denotes the \(k\)-by- \(k\) identity matrix. The condition \(A^{\prime} y \geq c\) is equivalent to \(-A^{\prime} y \leq-c\).
If we let \(\mathbf{z}\) be the column vector in \(n+m\) unknowns formed by adjoining \(y\) to the end of \(x\) (that is, \(z^{d}=\) ( \(\left.x_{1}, \ldots, x_{n}, y_{1}, \ldots, y_{m}\right)\) ), we can rewrite our linear programming problems in one giant system of inequalities:
\[
\left[\begin{array}{l}
\mathbf{A}  \tag{11}\\
-\mathbf{I}_{n} \\
\mathbf{o}_{(m, n)} \\
0_{(m, n)} \\
\mathbf{c}^{c} \\
-\mathbf{c}^{2}
\end{array}\right]\left[\begin{array}{c}
\mathbf{0}_{(m, m)} \\
\mathbf{O}_{(n, m)} \\
-\mathbf{A}^{2} \\
-\mathbf{I}_{m} \\
-\mathbf{b}^{2} \\
\mathbf{b}^{\prime}
\end{array}\right] z \leq\left[\begin{array}{c}
\mathbf{b} \\
\mathbf{O}_{(n, 1)} \\
-\mathbf{c} \\
\mathbf{o}_{(m, 1} \\
0 \\
0
\end{array}\right]
\]
where \(\mathbf{O}_{(j, k)}\) denotes a \(j\)-by- \(k\) matrix of zeros. If this system of inequalities is consistent, then the point that satisfies all the inequalities at once gives the solutions to both the maximum and the minimum problems.

For our problem (7) with Jim and Jack, we see that the system (11) becomes:
\[
\left[\begin{array}{rrrr}
2 & 1 & 0 & 0 \\
1 & 2 & 0 & 0 \\
-1 & 0 & 0 & 0 \\
0 & -1 & 0 & 0 \\
0 & 0 & -2 & -1 \\
0 & 0 & -1 & -2 \\
0 & 0 & -1 & 0 \\
0 & 0 & 0 & -1 \\
2 & 3 & -4 & -3 \\
-2 & -3 & 4 & 3
\end{array}\right] z \leq\left[\begin{array}{r}
4 \\
3 \\
0 \\
0 \\
-2 \\
-3 \\
0 \\
0 \\
0 \\
0
\end{array}\right]
\]

The solution to this problem, as we will see later, is:
\[
z=(12 / 3,3 / 3,1 / 3,11 / 3)
\]
a solution that can be derived from the above matrix by use of Khachiyan's algorithm.

\section*{Some General Implementation Problems}

As we mentioned in our discussion of Khachiyan's paper his achievement of obtaining a polynomial-time algorithm is attained only by paying the price of requir-

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ing an incredible level of precision in all the calculations. Moreover, his initial circle of radius \(2^{I}\) can be replaced by a far smaller circle, as will be explained shortly. This does not matter to Khachiyan, since, at the initial stage of the algorithm, the precision problems are more important.

The main problem we have created for ourselves is in our transformation of dual linear programming problems into a system of linear inequalities. Our statement that c'x \(=b^{\prime} y\) is equivalent to the inequalities \(c^{\prime} x-b^{\prime} y \leq 0\) and \(-c^{\prime} x+b^{\prime} y \leq 0\), while true mathematically, is generally false from a computational viewpoint.
If we think of \(c^{\prime} x-b^{\prime} y \leq 0\) and - \(c^{\prime} x+b^{\prime} y \leq 0\) as "half-planes" in some \(n\)-dimensional Euclidean space (shown in figure 4 for \(n=2\) ), then it is true that they will intersect along a "line," where c'x \({ }^{\text {' }} \mathrm{b}^{\text {' }} \mathrm{y}=0\). Unfortunately, our computer calculations of the common points will be rounded off to a finite number of decimal places, and we should not be surprised if we cannot correctly calculate a point that has zero difference between our calculated values of \(c^{\prime} x\) and \(b^{t} y\).
Our solution to this difficulty is to choose a tolerance within which we will agree that our values for c'x and b'y are essentially the same. Let \(\epsilon>0\) be this tolerance. If we require that \(c^{\prime} x-b^{\prime} y \leq \epsilon\) and \(-c^{\prime} x+b^{\prime} y \leq \epsilon\) then we have formed a "tube" around the line c'x - b'y \(=0\) (shown for \(n=2\) in figure 5) with width \(\epsilon\) in the direction perpendicular to x . The actual tolerance thus created will


Figure 4: Dissection of a plane into two half-planes by a line of the form \(c^{\text {t }} \mathbf{x}-\mathbf{b}^{\prime} \mathbf{y}=\mathbf{0}\).


Figure 5: Dissection of a plane into two half-planes dictated by the limited accuracy of a computer. Because any computer has a limited accuracy, it is unlikely for it to compute the exact location of a point on the line \(c^{\prime} x-b ' y=0\). Instead, the line separating the two half-planes (as shown in figure 4) is replaced by a thin "tube" with a diameter less than or equal to \(2 \epsilon\). The variable \(\epsilon\) is chosen so that a given computer can compute the location of a point that is no more than \(\epsilon\) away from a point on the center line.
depend on the slope of the relation \(c^{\prime} x-b^{\prime} y=0\) relative to the x subspace.

Thus our system of inequalities is no longer (11) but rather:
\[
\left[\begin{array}{ll}
\mathbf{A} & 0_{(m, m)}  \tag{12}\\
-\mathbf{I}_{n} & 0_{(n, m)} \\
\mathbf{O}_{(m, n)} & -\mathbf{A}^{\prime} \\
\mathbf{O}_{(m, n)} & -\mathbf{I}_{m} \\
\mathbf{c}^{c^{2},} & -\mathbf{b}^{2} \\
-\mathbf{c}^{i} & \mathbf{b}^{2}
\end{array}\right] \quad \mathrm{z} \leq\left[\begin{array}{c}
\mathbf{b} \\
\mathbf{O}_{(m, 1)} \\
-\mathbf{c} \\
\mathbf{O}_{(m, n)} \\
\epsilon \\
\epsilon
\end{array}\right]
\]

Let us now turn to the problem of estimating an initial region that will contain all solutions of the system of linear inequalities (2), from Part 1. The solutions of the systems, if any exist, form a polyhedron determined by the vertices at which the linear inequalities intersect. We can take for our initial region any sphere about the origin containing all these vertices, since such a sphere must then include some solution points of the system.

The problem is then to estimate the distance to the vertex furthest from the origin. The system may be written as \(\mathbf{A x} \leq \mathbf{b}\) where \(\mathbf{A}\) is an \(m\)-by-n matrix of integers and \(\mathbf{b}\) is a column vector with \(m\) integer entries. We may suppose that \(m \geq n\) since we can otherwise add on \(n-m\) trivial inequalities that will not change the solutions of the original system and will add only 0 s and 1s to the matrix \(\mathbf{A}\).

We can now compute all possible vertices of the region \(\mathbf{A x} \leq \mathbf{b}\) by examining \(n\) rows of the equation \(\mathbf{A x}=\mathbf{b}\) at \(\mathbf{a}\) time and applying Cramer's rule. For each subset of \(n\) equations, we will find \(x_{i}=\frac{D_{i}}{D}\), for \(i=1, \ldots, n\), where \(D\) is the determinant of the \(n\)-by- \(n\) matrix of equation coefficients and \(D_{i}\) is the determinant of the same matrix, but with corresponding \(n\) entries of \(\mathbf{b}\) replacing the \(i\) th column of the matrix.

Since we are dealing with integer coefficients, if \(D \neq 0\), then \(\left|x_{i}\right| \leq\left|D_{i}\right|\); and, by Hadamard's inequality, \(\left|D_{i}\right|\) is no more than the product of the norms of the columns of the matrix in question. This now explains \(\mathbf{Q}_{0}=2^{I} \cdot I_{n}\), since \(2^{2}\) is greater than the product of the absolute values of all the coefficients in the system (2). We now see that an estimate better than \(2^{L}\) will result if we determine the greatest possible norm for the \(n\)-subsets of each column of \(\mathbf{A}\) and then combine the \(n-1\) greatest such norms with the greatest \(n\)-subset norm from \(b\). For example, Khachiyan's estimate for the region of (7) is \(2^{44}\) while the above estimate based on Hadamard's inequality is \(2^{9}\).

The problems caused by the precision needed in computing the values required at each step of the algorithm appear to be nearly insurmountable. We shall not pursue this subject further than to observe its central position in the list of difficulties that prevent Khachiyan's algorithm from immediately replacing the Simplex method as the preferred method for solving linear programming problems.

\section*{Khachiyan's Algorithm on the TRS-80}

The program given in listing 1 represents a translation of the preceding discussion into a computer program. In writing this program, we have attempted to make the translation as literal as possible for two reasons. First, we wished to study how Khachiyan's algorithm actually pro-

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ceeds for real examples. Second, once the program is running, it remains a challenge for each user to discover improvements and modifications. We invite you to experiment.

The program will accept two different kinds of problems. If you wish to study the consistency of a system of linear inequalities such as equation set 2 (given in Part 1 of this article, last month), the program will accept the equations in the form \(\mathbf{A x} \leq \mathbf{b}\). If you wish to study a linear programming problem such as (8) or (9), the program will ask for \(\mathbf{A}, \mathbf{b}, \mathbf{c}\), and \(\epsilon\). The program will then create the system given by (12). In either situation, you will have three choices for \(L\) : you may have Khachiyan's or Hadamard's values computed or you may specify your own choice.

Because of the limited precision available on the TRS-80 (far less than the \(2^{-377 L}\) required for the algorithm), our program cannot be used to decide the consistency or inconsistency of even the smallest systems of inequalities. Thus it becomes meaningless to terminate the algorithm after \(\mathrm{N}=16 \mathrm{Ln} n^{2}\) steps, so our program does
not include a termination statement based on the number of steps executed.

If you enter the system of inequalities (1.1), you can watch the algorithm construct a solution point. It will take about thirty-eight steps to begin to find a reasonable estimate for \(x\). When you try equation set 1.2 (an inconsistent system given in Part 1), you will be able to watch the algorithm attempt to find a solution (a reasonable compromise between the inequalities is (1.5, 1.5)) and then decide that it had better try again.

The actual solution of the linear programming problem given in (7) and its dual is \(\left(x_{1}, x_{2}\right)=(12 / 3,2 / 3)\) and \(\left(y_{1}, y_{2}\right)\) \(=\left(1 / 3,1^{1 / 3}\right)\). You should try various values for \(\epsilon\) and contrast the number of steps required for the algorithm to terminate at a solution.

\section*{Klee-Minty Example}

As we noted earlier, the importance of Khachiyan's algorithm is that the number of steps required increases as a polynomial based on the size of the system of in-

Text continued on page 255

Listing 1: A program using Khachiyan's algorithm, written for the Radio Shack TRS-80 Model I running Level II BASIC.
```

'********************************************************
'* K'HACHIYAN'S ALGORITHM *
'*COFYFIGH' 1979 JC STEUENSON, AM ROCKETT, GC EERFESFORD*
' wwwww***************************************************
CLEAR100

```
0 CL.S
20 DATA \(1,119,1,119,1,119,3,69,118,120,4,69,116,117,121,4,69,115,122,123,8,68,69\)
\(, 70,95,114,123,124,125,8,67,71,95,112,113,125,126,127,11,64,65,66,72,73,74,94,96\)
, 110, 111, 123,14,60,61,62,69,73,74,75,76,77,78,93,96,107,124,11,58,60,61,70,79,89
.91
30 DATA 98,99,105,124,10,57,71,72,80,81,87,95,100,101,102,10,56,73,77,81,82,85,9
\(6,103,104,125,11,56,73,77,78,81,82,85,97,100,103,104,10,56,72,77,81,82,86,97,102\)
, \(103,123,11,57,58,80,81,87,88,96,100,101,102,104,13,59,79,80,89,98,99,100,101,10\)
5,106,107,121,127
40 DATA \(8,60,78,79,89,100,106,107,127,1,107\)
50 FOF \(I=0\) TO 448 STEF 64 : FRINTEI,STRING \(\$(64,191)\); :NEXT I

70 FFINTO576,STRING \(4(4,128)\);TAE (51)STRING \((13,128)\);
80 FRINTES12, STRING \((64,128)\);
90 FRINT@651,"THE K H A C H I Y A N ALGOK I THM*
100 FRINTE843,"COFYRIGHT 1979";:FRINT巴907,"J.C. STEUENSON, A.M. ROCKETT \& G.C. E:
ERFESFORD"
110 FOR I \(=3\) TO 20
120 FiEAD ل

140 NEXT I
150 FOR \(I=1\) TO 1000 : NEXT I
160 FFINTe834, "DO YOU WISH TO READ THE INTRODUCTION?";
170 FF'INTE898,"TYFE 'Y' IF YOU DO, ELSE HIT 'ENTEF' TO FRINT THE MENU.";
\(180 \mathrm{C} \$=\) INKEY \(\$\) : IF \(\mathrm{C} \$={ }^{\circ}\) " THEN 180
190 IF C \(\$=" Y "\) THEN 200 EL SE 230
200 FFINTE768, "THIS FROGRAM HAS TWO OFTIONS, YOU MAY USE IT TO SOLUE A LINEAR F'
ROGRAMING FROELEM OR YOU MAY VERIFY THAT A SYSTEM OF INERUALI- TIES IS CONSISTEN
T. IF YOU CHOOSE TO SOLVE A FROELEM, THERE ARE THREE OFTIONS FOR CHOOSING THE
    FARAMETER; L.';
210 FRINT: SEE KHACHIYAN'S FAFER FOR NOTATION, FRESS 'ENTEF' TO CONTINUE,";
\(220 \mathrm{Z} \$=\) INKEY \(\$\) : IF \(\mathrm{Z} \$=\) " \({ }^{2}\) THEN 220

NTE394, "2) CHECK CONSISTENCY OF A SYSTEM. "FFINT:FRINT:INFUT"ENTER THE NUMEER OF
THE OFTIUN YOU WISH" \(\ddagger C \%\)

Listing 1 continued on page 248


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260 CLS
270 ' \(\quad\) wwwwww HOW TO USE THE PROGRAM \(w * * * * * * * ~\)
280 INFUT"DO YOU WISH TO REUIEN THE FORMAT FOR ENTERING A FROELEM (Y/N -- 'ENTER
') ": Z\$
290 IF \(\mathrm{Z} \$={ }^{\circ} \mathrm{N}\) " THEN 400
295 IF C\%=1 THEN 300 ELSE CLS:FRINT"TO DECIDE THE CONSISTANCY OF A SYSTEM OF INE
QUALITIES, WFITE THE SYSTEM IN THE FORM:":FRINTTAE(23)" AXX < = E":PRINT"WHEFE
A IS A \(N\) E:Y \(N\) MATRIX AND E: AN N-UECTOR. FRESS 'ENTEF' TO EEGIN."
296 Z\$=INKEY \(\$\) :IFZ \(\$=\) " \(=\) THEN 296 ELSE 400
300 CLS
310 PFINT"TO SQLUE A STANDAFD LINEAR FROGRAMMING PROELEM OF CHECK
TANCY: ":FRINT:FRINT"1) WRITE THE FROBLEM IN THE FQRM: MAXIMIZE (C, \(X\) )
                                    CONSIS
                    SUEJECT TO THE CONSTRAINTS AXX \(<=E^{\circ}\)
320 FRINT" AND \(X=>0^{*}\)
330 FRINT \(X\) AND C ARE COLUMN UECTORS OF DIMENSION N WHILE E
AN M-UECTOR. A IS AN M EY N MATRIX. THE NOTA-
330 FRINT \(\quad X\) AND C ARE COLUMN UECTORS OF DIMENSION N WHILE E
AN M-UECTOK. A IS AN M EY N MATRIX. THE NOTA- TION, ..........
AN M-UECTOR, A IS AN M EY N MA
IS A STANDARD INNER FRODUCT."
    340 FRINT:FRINT:FRINT:FRINT"HIT 'ENTER' TO CONTINUE THE DIFECTIONS"
350 Z \(\$=\) INKEY \(\$\) : IF \(\mathrm{Z} \$={ }^{-1}\) " THEN 350
370 CLS:FRINT:FRINT" 2 ) THE COMFUTER SEEKS A SOLUTION OF THE ERUATION
                    \((C, X)=(E, Y)\) WHEFE Y IS A SOLUTION OF THE DUAL. IN GENEFA
L THE MACHINE CANNOT ACHEIUE THIS, SO A TOLERANCE EFSILON, MUST
    EE GIUEN"
380 F'FINT:FFINT"3) FRESS 'ENTER' TO EEGIN THE ALGORITHM. THE COMFUTER WILL ASK
    YOU FOR EACH ITEM AEQUE."
\(390 \mathrm{Z} \$=I N K E Y \$\) :IFZ \(\$=\) " \({ }^{2}\) THEN390
400 CLS: INFUT "HOW MANY ROWS HAS THE MATRIX A"; M : INFUT "HOW MANY COLUMNS IN TH
E MATRIX \(A^{\prime \prime}\) : \(N\)

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410 IF \(C \%=2\) THEN N9 \(=N: M 9=M\) : GOTO 430
\(420 \quad N 9=M+N: M 9=2 *(M+N+1)\)
430 DIM \(A(N 9, M 9), E(M 9), X 1(N 9), X 0(N 9), F(M 9), Q 1(N 9, N 9), Q 0(N 9, N 9), V(N 9, N 9), W(N 9, N 9)\) , E1 (M9)
440 CLS: FFFINT"FLEASE TYFE IN THE FOWS OF THE MATFIX A. FRESS 'ENTER' AFTEF K EYING EACH NUMEEF."
450 FOR \(J=1\) TO M
460 FOR \(I=1\) TO \(N\) : INFUT \(A(I, J): ~ N E X T ~ I\)
470 NEXT ل
480 CLS:FFINT*HEFE IS THE MATFIX A. IF IT IS NOT COFFECT, NOTE THE INDICES OF T HE MISKEYED ELEMENTS. FRESS 'C' TO MAKE COFFECTIONS. ELSE HIT 'ENTEF'."
490 FOR \(J=1\) TOM: FOR \(I=1\) TO N: FRINT A(I;J);" "; :NEXTI:FFINT:NEXTJ
\(500 \mathrm{Z}==\) INKEY生: IF \(\mathrm{Z} \$={ }^{\circ}\) " THEN 500
510 IF \(Z \$=\) "C" GOSUE: 700 ELSE 530
520 GOTO 480
530 CLS: FRINT"FLEASE TYFE IN THE ENTFIES OF THE VECTOF E; YOU NEED ";M;"NUMEERS -"
540 FOF I=1TOM : INFUT E(I) : NEXT I
550 CLS:FFINT"HEFE IS THE VECTOF E:" \(\ddagger\) FOF I=1 TO M : FFINT E(I) :NEXT I: INFUT"I

560 IF \(\mathrm{C} \%=2\) THEN 730
570 CLS:FFINT WHAT ARE THE COEFFICIENTS OF THE OEIJECTIUE FUNCTION? YOU MUST S UPFLY";N;"NUMEEFS."
580 FOR \(I=M+1\) TO N9: INFUT \(E(I): E(I)=-E(I): N E X T\) I
590 CL.S: FRRINT"THE COEFFICIENTS OF THE OEJECTIUE FUNCTION AFE: *
\(600 \mathrm{FOF} \mathrm{I}=\mathrm{M}+1 \mathrm{TO} \mathrm{N} 9\) : FRINT -E(I) : NEXT I
610 IF \(\mathrm{C} \%=2\) THEN 730
620 INFUT"IS THE OESECTIUE FUNCTION CORFECT (Y/N)"まZ\$: IF Z\$=*N" THEN 590
630 CLS: INFUT"WHAT FOSITIVE NUMEEF DO YOU WANT FOR THE 'TOLEFANCE', EF'SILON
- ; E(M9-1) : E(M9)=E(M9-1)

640 FOF \(I=1\) TO \(N: F O F J=1\) TO M: \(A(N+J, M+I)=-A(I, J): N E X T J: N E X T I\)
650 FOR \(I=M+N+1\) TO M9-2 : \(A(I-M-N, I)=-1\) : NEXT I
660 FOF \(J=1\) TO \(N: A(J, M 9-1)=-E(J+M): A(J, M 9)=E_{i}(J+M): N E X T\) J
670 FOR \(J=N+1\) TO N9: \(A(J, M 9-1)=-B(J-N): A(J, M 9)=E(J-N): N E X T\) J
680 GOTO 730
690 STOF
700 CLS: INFUTPTO COFFECT ENTRIES IN A. ENTEF THE ROW AND COLUMN INDICES OF THE ELEMENT TO EE COFRECTED" \(\ddagger\) I, J; INFUT"NOW ENTEF THE CORFECT UALUE";A (J, I)
710 INFUT"COFFECTIONS COMFLETE (Y/N)"; Z\$: IF Z\$:"N" THEN 700
720 RETUFN
730 CLS
740 FFINT"INDICATE YOUR CHOICE FOR THE DETERMINATION OF L FROM THE LIST EELOW:
":FFINT:FFINT:FRINT" 1) KHACHIYAN'S FORMULA":FRINT:FPFINT" 2) H
ADAMAFD'S INEGUALITY":FFINT:FFINT" 3) YOUR OWN CHOICE. ":INFUTIC\%
750 ON IC\% GOTO 770,2040,760
760 INFUT "WHAT IS YOUF UALUE FOF L";LL:GOTO780
\(770 \mathrm{LL}=0\) : \(\mathrm{FOR} \mathrm{I}=1 \mathrm{TO} \mathrm{NO}: \mathrm{FOF} \mathrm{J}=1 \mathrm{TO} \mathrm{M9}: \mathrm{LL=LL+LOG}(A E S(A(I, J))+1): N E X T \mathrm{~J}: \mathrm{NEX}\) \(T I: F O F I=1\) TO M9: LL=LL+ LOG(AES(E(I)) +1):NEXT I : LL= LL + LOG(N9*MY): LL= INT (LL/LOG(2)) +1
780 FFINT THE VALUE OF L FOR THIS FUN IS: "iLL

800 FOF \(I=1\) TO M9 : E1 (I) \(=-E(I)\) : NEXT I
\(810 \mathrm{FOF} \mathrm{I}=1 \mathrm{TO}\) NG
\(820 \mathrm{QO}(\mathrm{I}, \mathrm{I})=2 \mathrm{LL}\)
830 NEXT I
840 GOSUE 1960: T0=MX
850 : \(8 * * *\) END OF INITIALIZATION \(* * * * * *\)
860 '
\(870 \mathrm{K7}=0\)
\(880^{\prime}\)
890 ' \(\quad\) *** EEGINNING OF MAIN ITEFATION ****
900 .
910 K7=K7+1 : CLS : FFINT CCOMFUTING STEF ** :" \(\ddagger \mathrm{MX}\) : FOF \(I=1\) TO N9 : FFIINT "X(";I;")="; XO(I) ; NEXT I

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Listing 1 continued from page 250:
```

920 FOK I = 1 TO N9
9 3 0 ~ F ( I ) = 0
940 FOF J=1 TO N9
950 F(I)=F(I)-R0(J,I)*A(J,I0)
960 NEXT J
9 7 0 ~ N E X T ~ I ~
980 GOSUE 1010
985 GOTO 1090
990; **** FIND THE NORM OF F *****
1000.
1010 NF=0
1020 FOF I=1 TO N9
1030 NF=NF + F(I)wF(I)
1040 NEXT I
1050 NF = SQR(NF)
105S IF NF=0 FRINT"WARNING!!!!! THE NOFM OF F IS ZERO. IF YOU WISH TO CONTINUE,
TYFE 'CONT' FOLLOWED EY 'ENTEF''
1060 RETUFN
1070 ' **** STEF TO NEW X-ITERATE ****
1080
1090 FOF I=1 TO N9
1100 X1(I)=0
1110 FOF J= 1 TO NG
1120 X1(I)=X1(I)+Q0(I,J)*F(J)
1130 NEXT J
1135 IF NF=0 CLS:FRINT"THE NORM OF F IS TOD SMALL, FRODUCING A MACHINE ZERO. ":FR
INT"HERE IS THE UECTOR F:";FDR I = 1 T0 NG : F'RINT"F(";I;')=";F(I): NEXT I : FRI
NT'F'ROGFAM HAS EEEN STOF'FED":STOF'
1140 X1(I)=X0(I) + X1(I)/NF/(N9+1)
1150 NEXT I
1160 GOSUE 1590
1170
1180 ' **** STEF TO THE NEXT Q-ITERATE ****
1190
1200 FOF I = 1 TO N9
1210 FOF J=1 TO N9
1220 Q1(I,J)=0
1230 FOR K= 1 TO N9
Q1(I,J)=Q1(I,J)+ Q0(I,K)*U(K,J)
NEXT K
1250
1260 IF J=1 LET Q1(I,J)=Q1(I;J)*N9/(N9+1)
ELSE LET Q1(I,J)=Q1(I,J)*NG/SQR(NO*NG-1 )
1280 NEXT J
1290 X0(I)=X1(I)
1300 NEXT I
1310 FOR I= 1 TO N9
1320 FOR J=1 TO N9
1330 QO(I,J)=Q1(I,J)
1340 NEXT J
1350 NEXT I
1360
1370 , **** COMFUTE THE NEW DEFECT ****
1380 '
1390 FOF I= 1 TO M9
1400 E| (I) =0
1410 FOR J=1 TO NQ
1420 EI(I)=E1(I) + A(J,I)*X0(J)
1430 NEXT J
1440 E1(I)= Ei(I)-Ei(I)
1450 NEXT I
1460 GOSUE:1960
1470 IF TOSMX THEN T0=MX
1490 IF MX>O THEN }91

```

REM MERGE SORT USING LINK () FOR INDEX
FUNCTION • MERGE ( \(1, \mathrm{~J}=\) = NTEGER ) \(=\) INTEGER
VAR T,KM,M=INTEGER
If ARRAY (I) <ARRAY (I) THEN
BEGIN

\(\operatorname{LINK}(X M)=1\)
KM=I
\(\mathrm{I}=\mathrm{LINK}(\mathrm{I})\)
END
LINKiKM) \(=1\)
END \(=T\)
FUNCTION SORT(IS.IS = INTEGER) = INTEGER
VAR RS.II IJ = INTEGER
IF IS = IS THEEN
BEGIN
\(\operatorname{LINK}(I S)=0\)
RETURNED VALUE = IS
GOTO OEND

\section*{END}
\(K S=I S+(\) IS \(-1 S)\) 2)
II SORTIS.KS)
IJ = SORT(KS + I, IS)
RETURNED VALUE MERGE(II.II)
OEND
END - RETURNED VALUE


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Listing 1 continued from page 252 :

1500
1510 FRINT"THE FRROCESS TERMINATED AFTER";K7;"STEFS"
1520 FFKINT
1530 FFFINT"THE SOLUTION IS"
1540 FOF I=1 TO N9
\(1550 \quad\) FKINT"X("; I; ") ="; XO 0 ( I )
1560 NEXT I
1570 END
1580
1590 ' \(\boldsymbol{1 5 * * * ~ S U E F R U U T I N E ~ O R T ~ ( F ) ~ * * * * * ~}\)
1600
\(1610 \quad K=1\)
1620 IF \(F(K)<0\) THEN \(1630 E L S E\) K=K+1 : GOTO1620
1630 FOR \(I=2\) TO N9
1640 FOF \(\mathrm{J}=1\) TO N9
1650 W(J.I) \(=0\)
1660 IF JぐK THEN I \(1=1\) ELSE \(I 1=0\)
1670 IF \(J=I-I 1\) AND \(\downarrow\) LVK LET \(W(J, I)=1\)
1680 NEXT J : NEXT I
\(1690 \mathrm{WN}=0\)
1700 FOF \(J=1\) TO NG: \(N N=F(J) \times F(J)+W N: N E X T\)
1710 WN=SQR (WN)
1720 FOR \(I=1\) TO N9 :V(I,1)=F(I)/WN: \(W(I, 1)=V(I, 1): N E X T\)
1730 FOK I=2 TD N9
1740 FOR II \(=1\) TO N9
1750 U(I1,I) \(=W(I 1, I)\)
1760 NEXT II
1770 FOF \(J=1\) TO I-1
\(1780 \mathrm{~L}=0\)
1790 FOF J1=1 TO N9
\(1800 \mathrm{~L}=\mathrm{L}+\mathrm{U}(\mathrm{J1}, \mathrm{~J}) \times W(\mathrm{J1}, \mathrm{I})\)
1810 NEXT ل1
1820 FOF I1=1 TO N9
\(1830 \mathrm{~V}(I 1, I)=U(I 1, I)-L \times U(I 1, J)\)
1840 NEXT II
1850 NEXT J
\(1860 \mathrm{WN}=0\)
1870 FOK I2=1 TD N9
\(1880 \mathrm{WN}=\mathrm{WN}+\mathrm{V}(I\) I, I) *U(I2,I)
1890 NEXT I2
\(1900 \mathrm{WN}=\mathrm{SQR}(\mathrm{WN})\)
1910 FOF I2=1 TO N9: U(I2,I)=U(I2,I)/WN
1920 NEXTI2
1930 NEXT I
1940 FETUKN
1950
1960
1970 1980
1990
2000
2010 IF E1(I) > MX
2020 NEXT I
2030 FETUFN


2090 : GOSUE 1010 : Ei \(1(K Z)=N F: N E X T K Z\)
2060 FOR \(J=1\) TO M9: \(F(J)=B(J): N E X T J:\) GOSUE \(2090:\) GOSUE \(1010: F O R\) J=1TO N9
-1 : \(F(J)=E 1(J):\) NEXT J :MT \(=\) N9 : GOSUE: 2090
2070 FOF \(J=1\) TO N9-1 : FPN=FN*F(J): NEXT \(J: F N=F \cdot N * N F\)
\(2080 \mathrm{LL}=\) INT(LOG(FNxSQF(N9))/LOG(2))+1: GOTD 780
\(2090 \mathrm{I}=1: \mathrm{T}=\mathrm{F}(\mathrm{I}): K \mathrm{~T}=0: K=\mathrm{I}\)

T FETURN ELSE 2100

NEXT \(J: F(I)=T: K T=K T+1\) : GOTO 2100

Text continued from page 246:
equalities and not exponentially, as in the Simplex method. An example showing this exponential growth of the number of steps in the Simplex algorithm was constructed in 1972 by Klee and Minty. It is interesting to see how our program reacts to this problem. We are indebted to Dr Philip Wolfe of IBM for showing us the following version of the Klee-Minty problem.

Let \(n\) be given. Let \(\mathbf{c}^{t}=\left(10^{n-1}, 10^{n-2}, \ldots, 10^{1}, 1\right), b^{r}=(1\), \(\left.10^{2}, 10^{4}, \ldots, 10^{2(n-1)}\right)\) and:
\(\mathbf{A}=\left[\begin{array}{ccccc}1 & 0 & 0 & \cdots & 0 \\ 2 \times 10^{1} & 1 & 0 & \cdots & 0 \\ 2 \times 10^{2} & 2 \times 10^{1} & 1 & \cdots & 0 \\ \cdot & \cdot & & \cdots & \cdot \\ \cdot & \cdot & \cdots & \cdot \\ 2 \times 10^{(n-1)} & 2 \times 10^{(n-2)} & & \cdots & 1\end{array}\right]\)

The Simplex method takes \(2^{n}-1\) steps to find the solution of the linear programming problem (8). Running our program for Khachiyan's algorithm gave the results shown in table 1.
\begin{tabular}{cll}
\(\mathbf{n}\) & \begin{tabular}{l} 
Number of steps for \\
Simplex method
\end{tabular} & \begin{tabular}{l} 
Number of steps for \\
Khachiyan's method
\end{tabular} \\
\hline 1 & 1 & 35 (with \(\epsilon=.01)\) \\
\hline 2 & 3 & 525 (with \(\epsilon=.01)\) \\
\hline 3 & 7 & 2849 (with \(\epsilon=.01)\) \\
\hline
\end{tabular}

Table 1: \(A\) short comparison of the Simplex and Khachiyan algorithms. Although this comparison strongly favors the Simplex method, Khachiyan's algorithm would be consistently better, given problems of a sufficiently large size.

Although this data seems to reflect unfavorably on Khachiyan's method, it must be noted that this is only for small problems. Khachiyan's method would certainly require less steps than the Simplex method in some realworld situations, where a typical industrial problem may involve 10,000 inequalities and 50,000 variables. Far more experience with Khachiyan's method will be required to decide whether its theoretical advantage is of practical value.

We wish to thank the C W Post Research Committee for providing financial support for the preparation of this article.

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Regency, Chicago IL. This is the first national conference and exhibit on packaged software held in the US. For more information, or to reserve space, call
(312) 263-3131 or write Software INFO, Suite 545, 222 W Adams St, Chicago IL 60606.

September 23-25
Compcon '80 Fall, Capital Hilton Hotel, Washington DC. Sponsored by the IEEE (Institute of Electrical and Electronics Engineers), this show is concerned with distributed computing and related topics. Discussions will cover interfaces, standards, and protocols; data communications and networking; computer systems;
data bases; security; office systems; and more. Details from Compcon '80 Fall,
POB 639, Silver Spring MD 20901, (617) 879-2960.

September 24-27
The Tenth Annual Conference of the Society for Computer Medicine, San Diego Hilton, San Diego CA. This conference has been planned for physicians, attorneys, administrators, computer professionals, comptrollers, engineers, nurses, and anyone interested in the use of computers for patient care. Sessions on medical subjects, technical subjects and contributed papers on new research in computer medicine will be offered. For information, contact Society for Computer Medicine, 1901 N Ft Myer Dr, Suite 602, Arlington VA 22209, (703) 525-0098.

September 25-28
Mid-Atlantic Personal and Business Computer Show, Philadelphia Civic Center,
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\section*{October 1980}

October 1-2
Choosing and Using Microprocessor Development Systems, London Press

Centre, London, England. This seminar will present information and practical experience on which to base the selection and use of microprocessor-development systems. It will provide guidelines to answer questions on the definition of microprocessor-development systems, what features should be looked for, how to analyze particular requirements, and what systems are commercially available. The program is intended for senior engineers and engineering managers
who have some knowledge of microprocessors. Contact the Conference and Courses Unit, Sira Institute Ltd, South Hill, Chislehurst, Kent BR7 5EH, England.

\section*{October 1-3}

The Tenth International Symposium on FaultTolerant Computing, Kyoto, Japan. This meeting is devoted to the theory and practice of reliable computing and will cover design of fault-tolerant circuits and systems, analysis of system performance and reliability;


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applications of coding techniques, software reliability and testing, and more. For information and traveling arrangements, contact Secretary of FTCS-10, Dept of Applied Mathematics and Physics, Faculty of Engineering, Kyoto University, Kyoto 606 Japan.

\section*{October 6-8}

APL Users Meeting, Toronto, Canada. This conference is aimed at APL users as well as those considering the use of APL in their systems. Speakers will present papers which discuss the practical use of APL. Managing APL resources, teaching APL, and APL programming techniques will also be covered. The registration fee is \(\$ 180\) (in Canadian funds), which includes a copy of the proceedings. For a brochure and registration material, contact Rosanne Wild, I P Sharp Associates Ltd, 145 King St W, Toronto, Ontario, M5H 1J8, Canada.

\section*{October 8-10}

Circulation Computer Systems Symposium, Chicago Marriott Hotel Chicago IL. More than 425 newspaper publishers, general managers, circulation directors, controllers, and data-processing managers are expected to attend this symposium. Workshop sessions will be held for participants who already have or who are considering automated circulation systems. For more information, contact
American Newspaper
Publishers Association, The Newspaper Center, POB 17407, Dulles Airport, Washington DC 20041, (703) 620-9500.

\section*{October 14-16}

\section*{Minicomputer and} Microcomputer Conference and Exposition, Brooks Hall/Civic Auditorium, San Francisco CA. Contact Managing Director, Mini/Micro Conference and Exposition, 32302 Camino Capistrano, Suite 202, San Juan Capistrano CA 92675 , (714) 661-3301.

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October 16-19 Midwest Personal and Business Computer Show. For more information on this exposition, contact National Computer Shows, POB 678, Brookline Village MA 02147, (617) 739-2000.

\section*{October 26-28}

The Eleventh ACM North American Computer Chess Championship, Opryland Hotel, Nashville TN. This is a four-round Swiss-style tournament with participants restricted to computers. All of the best chess programs in North America are expected to participate. A maximum of twelve teams will participate. The deadline for entries is September 8, 1980. Contact Monty Newborn, School of Computer Science, McGill University, 805 Sherbrooke St W, Montreal, PQ, H3A 2K6, Canada,(514) 392-8274.

\section*{October 26-29}

International DataProcessing Conference and Business Exposition, Philadelphia Sheraton Hotel, Philadelphia PA. This conference is being sponsored by the Data Processing Management Association. Contact the Conference Coordinator, DPMA International Headquarters, 505 Busse Hwy, Park Ridge IL 60068, (312) 825-8124.

October 27-29
ACM Annual Con-ference-Previewing the Computer Age, Opryland Hotel, Nashville TN. This conference will focus on the computer technology, products, and services that will come into general use during the 1980 s. The technical program will be organized around the Association for Computing Machinery's (ACM) Special Interest Groups, with additional sessions for papers of general interest. Contact Dr Gordon Sherman, Technical Program Chairman, ACM '80, University of Tennessee Computer Center, Knoxville TN 37916, (615) 974-6758.

October 27-30
The Fifth International Con-
ference on Computer Communications, Peachtree Plaza Hotel, Atlanta GA. The theme for ICCC/ 80 is "Computer Communications: Increasing Benefits for Society." More than one hundred speakers will present papers on applications and technical developments of computer communication and assess their worldwide implications for the 1980s. Fees are \$175 for preregistration and \(\$ 200\) at the conference. Contact ICCC \(/ 80\), POB 280, Basking Ridge NJ 07920, (201) 221-8800.

October 28-30
The Fourth Annual Interface West, Los Angeles Convention Center, Los Angeles CA. More than one hundred fifty computer-related companies will exhibit their wares. The conference will offer programs on office automation and smallsystems procedures for businessmen, plus data communications, distributeddata processing, and networking for technically oriented managers. Many speakers will be featured. For further information, contact The Interface Group, 160 Speen St, Framingham MA 01701, (617) 879-4502 or call toll free, (800) 225-4620.

October 30-November I National Small-Computer Show, New York Coliseum, New York NY. Hourly lectures on data-processing and word-processing applications for small computers, exhibitions of hardware and software, and seminars on various aspects of computerrelated news will be featured. A lecture schedule and basic information are available from the National Small Computer Show, 110 Charlotte Pl, Englewood Cliffs NJ 07632, (201) 569-8542.

NOVEMBER 1980
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Center, Seattle WA. The theme of this year's fair is "Hands On." The booths and exhibits will reflect this idea, and the public will have access to as many computers and terminals as possible. Contact the Northwest Computer Society, POB 4193, Seattle WA 98119, (206) 284-6109.

November 10-14 The Fourth Annual DataEntry Management Conference, Orlando FL. This conference will cover data entry, distributed processing, and word processing with emphasis on data entry, including humanmachine interface. Contact Data Entry Management Association, POB 3231, Stamford CT 06905, (203) 322-1166.

\section*{November 18-20}

The Third Industrial Revolution, McCormick Place, Chicago IL. This exposition and conference is devoted to development by manufacturing companies of systems for information management. Information may be obtained from Banner \& Grief Ltd, 110 E 42nd St, New York NY 10017, (212) 687-7730.

November 19-21
Comdex '80, Las Vegas Convention Center, Las Vegas NV. Comdex is a conference and exposition for independent sellers of smallcomputer and wordprocessing systems, peripherals, media, and supplies. Address inquiries to the Interface Group, 160 Speen St, Framingham MA 01701, (800) 225-4620, in Massachusetts call (617) 879-4502.

November20-21
Western Educational Computing Conference, San Diego CA. This seminar will feature papers and seminars on the use of computing in higher education for instruction, administration, and research. Contact Ron Langley, Director, Computer Center, California State

University, Long Beach, 1250 Bellflower Blvd, Long Beach CA 90840, (213) 498-5459.

\section*{November 20-23}

Northeast Personal and Business Computer Show, Hynes Auditorium, Boston MA. This is an annual exposition open to the general public. The admission will be \(\$ 5\). Contact National Computer Shows, POB 678, Brookline Village MA 02147, (617) 739-2000.

\section*{November 21-23}

National Home Entertainment Show, New York Coliseum, New York NY. Exhibits will cover video, photography, audio, games, and home computers. Seminars and demonstrations will be featured in this show. Contact United Business Publications Inc, 475 Park Ave South, NewYork NY 10016, (212) 725-2300.

\section*{BVIEs Bugs}

\section*{An Error in Fifteen}

I enjoyed seeing my article "Fifteen: A Game of Strategy" appear in the June 1980 BYTE (page 230). Unfortunately a bug crept into the program (listing 1), and it will not run as listed. The problem is in line 720 , which should read:

\section*{"IF T2 > 0 THEN 750"}
rather than "IF T2 \(>0\) THEN \(270^{\prime \prime}\). With this change it runs as it should.

If the EXIT statements are dropped and the PRINT statements changed, then the program runs very nicely on a TRS-80 under Level II BASIC.

\footnotetext{
John Rheinstein
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}

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\title{
Exploring Ballistics with Your Computer
}

\author{
Robert W Jenks \\ POB 962 \\ Islamorada FL 33036
}

Many sports are intricately involved with the properties of objects lofted into the air and thereby committed to the inevitable effects of gravity. Both players and fans relish golf's hole-in-one, the long bomb to the wide receiver in football, and the home run in baseball. In the case of target shooting, the path of the projectile is of particular interest. How the bullet gets to the target is the province of physics, but where it lands resides solely in the skill of the shooter. BALISTIC is a program to calculate just where a bullet will go.

\section*{Ballistics}

Ballistics is the study of the behavior of projectiles at various ranges. Of interest to shooters are the velocity, time of flight, drop, and drift at evenly incremented ranges of 50 or 100 yards. Also of interest is the maximum height attained by the bullet above a horizontal line from the bore to a bull's-eye, the trajectory above and below a line of sight at various ranges, and the energy of the bullet.
A variety of factors influence the path of a bullet as it leaves the muzzle; most important are muzzle velocity, gravity, and air resistance. Muzzle velocity is determined by internal ballistics and factors such as bullet weight and bore diameter, barrel length, powder weight and burning rates, and maximum pressures.

The calculation of these factors is beyond the scope of this article. Muzzle velocity depends upon the direction of the bore relative to the horizontal, since a velocity is formally a vector quantity. As it leaves the muzzle, though, the speed of the bullet can be most easily measured with an instrument called a chronograph. Bore elevations at reasonable ranges are typically less than a quarter of a degree, and therefore are of negligible influence. The acceleration of gravity is dependent on latitude and altitude (and thus on the distance to the center of the Earth), and upon local rock density and underlying mass. This, too, tends to minor deviations: only \(0.5 \%\) from the equator to the poles, only \(0.15 \%\) from sea level to 15,000 feet. The acceleration of gravity can be regarded as a constant 32.1725 feet per second per second in English system units.
Air resistance is the most complicated factor, and its effect is dependent on the density of the air, temperature (and thus the speed of sound), wind velocity, and the properties of the bullet-specifically, speed, sectional density (proportional to the ratio of mass to frontal area), and shape. Whereas gravity pulls the bullet toward the center of the Earth, air resistance acts as a drag opposite to its direction of motion at any instant. This effect of air resistance, in-
dependent of gravity (under usual conditions), determines the time of flight to any range and the remaining velocity. The effect of gravity combined with the influence of air resistance determines bullet drop at any range. Therefore, the calculations of the effects of the air naturally come first.

\section*{Air Resistance}

The effect of the atmosphere is to push against the moving bullet. Because a force acting on a mass results in an acceleration or deceleration, depending upon the force's direction, a bullet is decelerated at a rate proportional to the ratio of the drag force to the mass. For a standard projectile, this retardation \(R\) is related to a constant A times a power \(m\) of the velocity at any instant: thus \(R=A V^{m}\). It has been deduced that the retardation or drag (call it \(r\) ) for any other projectile differing from the standard only in scale of size is directly proportional to a ratio of the standard projectile's deceleration to a factor known as the ballistic coefficient: thus \(r=R / C\). The ballistic coefficient \(C\) for a bullet differing in varying degrees of shape from the standard is, in turn, proportional to the ratio of the sectional density to a quality called the form factor (commonly known as \(i\) ): thus \(C=\left(w / d^{2}\right) / i\).

The form factor is usually disagreeably hard to calculate from


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geometric properties alone, and is therefore inferred from the results of ballistic experimentation. But for ogival pointed bullets (ie: a bullet with a point shape defined by a circular arc meeting parallel straight sides at a tangent, or spitzer) \(i=\sqrt{(16 n-4) / 7 n^{2}}, \quad n=L^{2}+0.25, n\) equals the ratio of arc radius to bullet diameter, \(L\) equals the ratio of bullethead length to diameter (see reference 1). Most bullets are ogival in shape, but serious changes in the form factor are caused by even small flats on the nose (such as hollow points or dents in soft-nose jacketed bullets), and no
further use of this mathematical relation will be made.
Since the velocity of a bullet at any time is dependent upon the deceleration, which in turn is dependent on the instantaneous velocity, a differential equation is involved. Since a change in velocity is dependent on the integral of acceleration, the use of the calculus is formidably indicated. Whereas given an initial muzzle velocity one might attempt to tabulate range and velocity for suitably small increments of time, it is easier to tabulate changes in range and time for suitably small decrements in ve-

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locity, and avoid the calculus entirely. Summations of these increments of time and range give the total time of flight to a given distance. To do this the values of the constants \(A\) and m in the equation \(R=\mathrm{A} V^{\mathrm{m}}\) must be determined.
Values for the constants A and m were determined by Russian Colonel Mayevski based on data compiled by the German firm of Krupp Armorers in 1881. These figures were converted into English units by Colonel James M Ingalls of the United States Army in the form of a famous tabulation known as the Ingalls Ballistics Tables.
The standard projectile used in the Krupp firings was a spitzer-pointed projectile of 2 -caliber radius, flat base, and an overall length of 3 caliber. The shape of small-arms bullets today is similar enough to this standard projectile to allow the Ingalls tables to closely predict their performance. It was found that the factors \(A\) and \(m\) varied with velocity, but could be taken as constants within suitable limits of velocity and still give accurate results. Thus eight ranges of velocity from 5000 feet per second (fps) to 0 fps , each with its own constants \(A\) and \(m\), cover the range of small-arms bullets. The factors \(A\) and \(M\) in listing 1 are these constants. Also available in the program are the constants to reconstruct the British Ballistic Tables of 1909: these seem to more closely agree with hand-loading data such as is in the Sierra Bullets Reloading Manual (for the reloading of cartridges by the shooter).

To reconstruct the Ingalls or British tables, a standard projectile is assumed, with a Krupp-shaped nose, weighing 1 pound, 1 inch in diameter, and with an assigned standard ballistic coefficient of 1 and a form factor of 1 (since \(w / d^{2}=1 / 1^{2}=1\) ). For a small change in velocity \(v=U-W\) ( \(U=\) initial velocity, \(W=\) final velocity over a small change in velocities), and average velocity \(V=(U+W) / 2\), the time for the projectile to decelerate from \(U\) to \(W\) is \(t=v / A V^{m}\), and the distance over which it travels \(s=v / A V^{(m-1)}\). The total time to slow from a given muzzle velocity to any velocity \(W\) equals the sum of all these increments of time ( \(T=\Sigma t\) ) and the total distance \(S=\Sigma s\).

The computer solves these summations for any bullet, given either its ballistic coefficient or form factor

Listing 1：BALISTIC，a North Star BASIC ballistic program．The workings of this pro－ gram and the peculiarities of North Star BASIC are described in the text．
```

KEM 且 "FALISTIC" EY K W JEitIS 1979 MOI 9/10/%! **
GOSUR 1540\REM (OUTFUT TO TEFMINAL)
LINE }7
HIM C$(50),T(10,2)
    C1=1\ A4==1\ U=5\ R3:500
! CHF$(27),CHF$(42)
REM ** INPUT FAFAMETERS **
INFUT "CALCULATE EALLISTIC COEFFICIENT (YES/NO)? *,I$
IF I ="YES" THEN F=1 ELSE F=O
INFUT INGALLS OR ERITISH 190% TABLES? ", I\$
IF I$(1;4)=*INGA* THEN F1=1 ELSE F1:0
        T3=59+1*F1
        F1=29.53+.47*F1
INPUT1 "WINL SFEEL;",WI\ ! " Miles Fer Hour"*
INFUT1 "CROSS WIND ANGLE:",A1\ ! " Desrees From Eraadside"
    W2=W1*COS(2*3.1415927*A1/360)*88/60
    IF I$="W" THEN 460
INFUT "CARTRIIGE;",C\$
INPUT1 WEIGHT:",W\ ! "Grains"
W=W/7000

```

```

        IF F THEN 290
    INPUT *BALLISTIC COEFFICIENT:",C
IF C<< THEN 260
INPUT "FORM FACTOR:",I
IF C=0 THEN C=(W/DN2})/
IF I=0 THEN I=(W/DN゙2)/C
C1=C
INFUT "NON STANDARII CONDITIONS (YES/NO) ?*,I\$
IF I$<`"YES* THEN 460
    REM *宗 NON STANIARIL ATMOSPHERIC CONMITIONS **
INPUT 1 "TEMFERATURE:",T3\ ! " Hesrees Fahrenheit"
INPUT1 "PRESSURE:"P1\! " Iriches Mercury"
INPUT1 "ALTITUNE:",A2\ ! E Feet*
        T4=59-(3.566E-3)*A2+1軒F1
        P'2=29.53-(B.581E-4)*A2+(8.602E-9)*A2の2+.47*F1
        A3=1+(3.073E-5)*A2+(6.371E-10)岝A2`2?
        A4=A3事(2-F'1/F'2)年(T3+459.4)/(T4+459.4)
        C=C1*A4
        IF F THEN 430
!\! "MODIFIED C:", X5F3,C\!
        GOTO 440
        T3=59+1*F 1\ P1=29.53+.47*F1\ A2=0
        I=(W/D-2)/C
    REM -- END OF ROUTINE --
IF NOT F THEN INFUT "TO 500 OR 1000 YARDS? *,R3
        R3=R3/500
INPUT1 "MUZZLE UELOCITY:",VI\! " Feet Fer Secord"
        U2=U1+U\R2=0
        IF NOT F THEN 560
INPUT1 "RANGE:",R1\ ! " Yards"
        R1=R1专3
INPUT1 "FINAL UELOCITY:",U4\ ! " Feet Fer Secorid"
        I=1\ C=1\ GOTO 700
    REM ** FRINT LIATA **
    |,C$
TAB(50),INT(W*7000+.5)," Grains ",%*5F3,D," Caliber*
! TAB(25),"BALLISTIC COEFFICIENT: ",C," FORM FACTOR: ",I,%*
! TAB(30):"Eased on *,
IF F1 THEN ! *INGALLS"F\ IF NOT F1 THEN ! "ERITISH 1909*,
" Ballistic Tables"
"WINL ",Z曾F1,W1," MPH FROM ",A1," Desrees CROSSWINM ",W2," FF'S"
* TEMFERATURE ",T3; Desrees F PRESSURE ,Z5F2,P1,
- Inches Hs ALTITUDE ",%*,INT(A2),"Feet"
*RANGE VELOCITY ENERGY MAX HEIGHT IROP IR-IFT TIME***
REM * BEGIN TIME ANL UISTANCE CALCULATIONS **
K=2得U审C
U2=U2-2官U
IF F1 THEN GOSUB 1350 ELSE GOSUB 1190
S1=S\ S=S+K/(A\&V2m(M-1))
T1=T\ T=T+K/(A*V2"M)
IF F ANLI U2<U4 THEN }79
IF NOT F AND S`=R2 THEN 870
GOTO }71
REM ** RESULTS OF BC/FF CALCULATION **
S=S1+(S-S1)*(U2+U-U4)/(2*U)
C=(R1/5)/A4
I=(W/Ln
* BALLISTIC COEFFICIENT:",X*5F3.C
-FORM FACTOR:",I,%*
C1=C\F=O\ GOTO 1090
REM ** FFINT A ROW OF BALLISTIC DATA **
U3=(U2+U)-2*U*(R2-S1)/(S-S1)
E=V3~2专W/32.1725/2
T2=T1+(T-T1)*(R2-S1)/(S-S1)
T(R2/(150*R3),0)=R2/3
n1=(110.3+82.7*U3/U1)自T2~2
T(R2/(150*R3),1)=D1
W3=12*W2*(T2-R2/V1)

```

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A Ballistic proportional part constant
A1 Crosswind angle
A2 Altitude above sea level
A3 Altitude factor
A4 Combined atmospheric factor
C Current ballistic coefficient
C1 Standard ballistic coefficient
D Bullet diameter (caliber)
D1 Drop
E Energy
F Flag to indicate calculation of ballistic coefficient
F1 Flag to indicate choice of constants
H Sight height above bore
I Form factor
K Simplified term for calculations
M Ballistic exponent constant
P1 Atmospheric pressure
P2 Pressure factor
R1 Final range
R2 Incremental range for tables
R3 Maximum range (in units of 500 yards)
R4 Range at which sights are on
\(S\) Distance
S1 Previous distance
T Time
T1 Previous time
72 Interpolated time
T3 Temperature
T4 Temperature factor
\(\checkmark\) Incremental velocity
V1 Muzzle velocity
V2 Average interval velocity
V3 Interpolated velocity
V4 Final velocity
W Bullet weight
W1Wind speed in mph
W2Crosswind in fps
W3Wind drift
X Loop variable
T() Trajectory table array
C\$ Cartridge identifier
i\$ Response to input request
Table 1: Table of variables used in the BALISTIC program.
ty are best ignored. And, indeed, few shooters are likely to hazard whirling a sling psychrometer on the range anyway.
Standard conditions at sea level used for the Ingalls Tables are 30 inches of mercury, \(60^{\circ} \mathrm{F}\), and air \(66 \%\) saturated with moisture. This compares with the standard conditions for the tables in the Sierra Bullets Reloading Manual of 29.53 inches of mercury, \(59^{\circ} \mathrm{F}\) and \(78 \%\) relative humidity. The product of these factors with the ballistic coefficient gives an amended ballistic coefficient.

\section*{Bullet Path}

The trajectory of a bullet is conventionally taken to be the path traversed by the bullet in a vertical plane. This path, in turn, can be measured from various datum lines. When it is measured from the line of the bore and the bore is horizontal, the path is referred to as bullet drop.

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range times the ratio of any range to the targeted range ( \(o=d-D r / R\), where \(o=\) modified ordinate relative to the horizontal, \(d=\) drop at any range, \(D=\) drop at targeted range, \(r=\) any range, \(R=\) targeted range). A table of discrepancies between the path of the bullet and the horizontal is modified for the difference between the angle of the line of sight and the horizontal (crossing at the targeted range). Thus \(O=0-h(R-r) / R\), where \(O=\) the ordinate from the line of sight, and \(h=\) the separation of sight and bore; \(h\) usually varies from 0.75 to 2 inches.

For any given target range, the maximum height reached by the bullet above the horizontal while traveling to that range is \(H=48 T^{2}\) inches. Maximum height and midrange trajectory are nearly identical over the limits of practical shooting distances.

\section*{Crosswind}

Though the effect of air resistance on bullet drop is somewhat odd, the effect of a crosswind is downright confusing. One would think that a bullet in a crosswind might do one of three things: it might quickly begin drifting with the wind if it were light relative to its lengthwise sectioned area, or it might resist the wind tenaciously if it were massive relative to this area, or, most likely, it should do a little of both; drifting to the extent that it is light and resisting to the extent that it is massive. In any case its crosspath acceleration should appear to be smooth as its sideways speed approaches that of the wind.

In truth, though, a bullet will drift an amount equal to the product of the component of the wind perpendicular to the axis of the bullet multiplied by the difference between the time the bullet takes to reach any range and the time it would take to reach that range were it fired in a vacuum. This time of travel in a vacuum equals the range divided by the muzzle velocity. It is hard to believe that both a slowmoving bullet and a fast-moving bullet (ie: bullets moving slower or faster than the speed of sound) drift less for the same ranges than bullets moving more nearly at the speed of sound, even though the fast-moving bullet gets to the target sooner and the slow-moving bullet gets there later. A bullet fired at a speed faster than the speed of sound at first accelerates sideways moderately, then accelerates considerably in drift while

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Listing 2: This is a sample run of BALISTIC producing a calculation of bullet parameters. Note that the Sierra Handbook (reference 2) also gives the ballistic coefficient as 0.285 . Compare the velocities for standard conditions.
\[
\begin{array}{rcrrrrr}
\text { FANGE } & 0 & 100 & 200 & 300 & 400 & 500 \\
\text { UELOCITY } & 3800 & 3405 & 3045 & 2713 & 2405 & 2117
\end{array}
\]

REALIY
FUN50
*
CALCULATE BALLISTIC COEFFICIENT (YES/ND)? YES
INGALLS OR BRITISH 1909 TABLES? BRITISH
WINI SFEED: 8 Miles Per Hour
CROSS WIND ANGLE:32 Desrees From Eroadside
CARTFILIGE: . 22/250
WEIGHT:55 Grains
CALIRER:. 224 Inch
NON STANIARD CONDITIONS (YES/NO) PYES
TEMPERATURE: 68 Desrees Fahrenheit
PRESSURE:29:00 Inches Mercury
ALTITUDE:2150 Feet
MUZZLE VELOCITY:3800 Feet Fer Second
RANGE: 400 Yards
FINAL UELDCITY:2460 Feet Per Second
BALLISTIC COEFFICIENT: . 285
FORM FACTOR: . 550
?
TO 500 DR 1000 YARDS? 500

? A
TEMPERATURE; 68 Desirees Fahrenheit
PRESSURE: 29.00 Inches Mercury
ALTITUDE:2150 Feet
MODIFIED C . 300
TO 500 DR 1000 YARDS? 500
MUZZLE VELDCITY:3800 Feet Per Secand
\(\begin{array}{ll}.22 / 250 & \text { BALLISTIC COEFFICIENT: } 55 \text { Grains } 300 \text { FORM FACTOF: } 224 \text { Caliber } \\ .522\end{array}\) Based on ERITISH 1909 Ballistic Takles
WIND 8.0 MFH FROM 32.0 Desrees CROSSWIND 10.0 FPS
TEMPERATURE 68.0 Desrees \(F\) FRESSURE 29.00 Inches Hs ALTITUDE 2150 Feet
RANGE VELOCITY ENERGY MAX HEIGHT DROF DRIFT TIME
YARDS FT/SEC FT-LBS IN. IN. IN. SEC.
\begin{tabular}{rrrrrrr}
0 & 3800 & 1763 & .0 & .0 & .0 & .000 \\
50 & 3611 & 1592 & .1 & .3 & .1 & .040 \\
100 & 3428 & 1435 & .3 & 1.3 & .5 & .083 \\
150 & 3252 & 1291 & .8 & 3.0 & 1.2 & .128 \\
200 & 3082 & 1160 & 1.5 & 5.5 & 2.1 & .175 \\
250 & 2918 & 1039 & 2.4 & 8.8 & 3.4 & .225 \\
300 & 2760 & 930 & 3.7 & 13.2 & 5.0 & .278 \\
350 & 2607 & 830 & 5.4 & 18.7 & 6.9 & .334 \\
400 & 2460 & 739 & 7.4 & 25.4 & 9.3 & .393 \\
450 & 2317 & 656 & 10.0 & 33.5 & 12.1 & .456 \\
500 & 2178 & 579 & 13.1 & 43.2 & 15.3 & .523 \\
SIGHT ON AT:200 & Yard5 & SIGHT HEIGHT:1.5 & Inches \\
FANGE Yards & 0 & 50 & 100 & 150 & 200
\end{tabular}
\begin{tabular}{rrrrrrrrrr} 
\\
TRAJECTDRY In. & -1.5 & -.1 & .7 & 150 & 200 & 250 & 300 & 350 & 400 \\
\hline
\end{tabular}
transiting the speed of sound (slowing down in its motion toward the target), and then settles back to drifting at small incremental velocities from there on.

The logic behind the observations is that the amount of deceleration affecting a bullet traveling close to the speed of sound is large (as a measure) due to turbulence. At both higher and lower speeds, the combined effects of base drag, skin friction, and nose drag are changing less over a given range, and so the bullet travels this distance nearer to the time it would take were it able to maintain its initial velocity. Were the bullet able to arrive at a given range in the time it would take if it could maintain its muzzle velocity, this would imply an absence of air resistance, an absence of wind, and thus no drift. This supports the dependence on the time difference.

Also affecting the horizontal path of a bullet is a gyroscopic effect causing the bullet to point away from its initial line of flight. As the bullet falls, additional air resistance appears on the bottom of the bullet. This leads to asymmetrical torques around the center of mass which cause the bullet to attempt to tilt around a horizontal lateral axis, but because the bullet is spinning, the gyroscopic effect resists the turning moment and redirects it by \(90^{\circ}\), thus causing the bullet to yaw and veer away from the line of the bore. The effect is minor and only amounts to 6.7 inches at 1000 yards for a 150-grain, full-jacket 30-06 bullet.

\section*{The Program}

BALISTIC, listing 1, is written in North Star BASIC for use on a North Star Horizon computer and may need modification for use with other BASICs. An exclamation point (1) is North Star BASIC shorthand for PRINT.' The backslash ( \(\backslash\) ) is the multiple-statement-per-line separator; commas separate print items. Line 60 of the program sends the clear-screen command for a Soroc IQ-120 terminal, an Escape-asterisk (ESC-*) sequence. Lines 1540 and 1550 modify the North Star BASIC disk operating system output routine so as to reconfigure output to either the standard serial port (terminal) or secondary serial port (printer), and thus doing away with the need for device designation parameters in all


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PRINT statements. Lines 1540 and 1550 should be replaced with appropriate routines or just RETURNs on all computers where such execution might cause havoc. BALISTIC runs in 5300 bytes, but can be shortened by deleting spaces and remarks, and by merging statements onto fewer lines. BALISTIC may also be shortened by excising the routine for the constants of one or the other ballistic tables.

\section*{Operation}

The program is self-prompting for the most part, as shown in listing 2. It operates in two major modes: simulating bullet performance based on parametric input or calculating normalized ballistic parameters based on experimental data (after which it returns to the simulation mode). Units are English, and terminology is characteristic of the shooting sports ( 7000 grains per pound). Pertinent information is repetitively printed so that it is not lost in the shuffle. A suitably placed GOTO statement bypassing these lines saves paper when you are compiling records such as handloading information.

When the computer prompts for the caliber, the bore diameter plus the depth of one groove is expected: the diameter of the bullet is a suitable alternate. If the ballistic coefficient, \(C\), is not known, but the form factor, \(i\), is known, entering 0 for \(C\) allows the computer to prompt for \(i\). When the computer prompts for the maximum range to which to calculate, any range may be entered, not just 500 or 1000 yards. But when the program asks for the "SIGHT ON RANGE," a range listed in the table must be used (other than 0). The question mark following the trajectory table prompts for an " A ", " P ", "T", "W", or a carriage return-for new atmospheric data, printer or terminal output, new wind data, or reiterate.

\section*{Conclusions}

I hope all the major factors that affect bullet performance have been included, so that accurate results are possible. The greatest, though unquantized, limitation is that the ballistic coefficient changes with velocity for projectiles differing from 1 in form factor. The farther from stan-

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dard this deviation, the less accurately will the calculated results match the real bullet performance, since the standard projectile will be less of a model for the actual bullet. Even so, the calculations tend to match actual performance within \(1 \%\) for velocity and \(2 \%\) for bullet path out to 500 yards or more, and compare nicely with published cartridge manufacturers' information and reloading guide data. Do not expect especially accurate results for blunt-nosed bullets or slow-moving boattails, though. But the accuracy is probably consistent with random variations in the actual physical conditions such as spatial variations in wind speed and direction, air temperature and humidity, bullet imperfections and variations in weights, etc. Reduction of published data might indicate a mathematical relationship between bullet geometry and the way the ballistic coefficient changes with velocity, and thus the equations in the program might be modified for more universal simulations.
See the references for other sources and additional information. Hatcher's Notebook is extremely interesting reading on a variety of shooting subjects. Other reloading guides are also valuable.
So go ahead, load BALISTIC, and take your computer to the range.

\section*{References}
1. Hatcher, Maj Gen Ret Julian S, Hatcher's Notebook, Third Edition. The Telegraph Press, Harrisburg PA, 1966, Library of Congress number 62-12654.
2. Sierra Bullets Reloading Manual, First Edition, 1971, Sierra Bullets, 10532 S Painter Ave, Santa Fe Springs CA 90670.
3. Walters, Kenneth L, "Crosswind Deflections: a Cast Bullet Anomaly, " Gun Digest, Thirty-third edition, 1979, DBI Books Inc Northfield IL.



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\section*{APPLE II ACCESSORIES}


\title{
An Interrupt-Driven Real-Time Clock for the TMS 9900
}

\author{
Thomas G Morris Jr \\ 861 St Mary Ave \\ San Leandro CA 94577
}

One of the first things many computer enthusiasts feel the need for is a real-time clock for their personal computers.
With many different methods available for the computer to maintain the time of day, I decided that any real-time clock should have a reasonably low software overhead and simple hardware approach.

\section*{Processor Overview}

One of the 16-bit microprocessors now readily available to computer users is the Texas Instruments TMS 9900.

The TMS 9900 is a 16 -bit processor using a memory-to-memory architecture that allows multiple register files (known as workspaces) to reside in memory. A workspace is defined as
sixteen contiguous words of memory, addressable as registers R0 thru R15. This method increases programming flexibility and produces a faster interrupt-response time than other processors have; a context switch may be performed without the use of a stack.

\section*{Registers}

The processor contains three hardware registers. They are:

> program counter (PC)
> status register (ST)
> workspace pointer (WP)

The program counter contains the address of the instruction following the currently executing instruction.

\footnotetext{
1. Register
2. Register Indirect
3. Register Indirect with Auto-Increment
4. Direct (Symbolic)
5. Indexed
6. Immediate
7. Relative
}

Table 1: The 7 main addressing modes of the Texas Instruments TMS 9900 16-bit processor, given with assembler- mnemonic representation. Additional addressing modes can be simulated by subroutines called through extended-operation (XOP) instructions.

The status register contains the current state of the processor (ie: flags and interrupts). The workspace pointer register points to the first word of the current workspace.

\section*{Addressing}

The TMS 9900 has both word and byte addressing capability. The byteaddressing mode is internal to the processor and references the leftmost byte of a workspace register. There are seven main addressing modes. These are given along with the assembler mnemonics in table 1.

\section*{Interrupts}

The TMS 9900 utilizes sixteen vectored interrupts. The interrupt vectors are contained in hexadecimal memory locations 00 thru 3C and consist of the interrupt workspace pointer and a pointer to the interrupt code. When an interrupt has been

\footnotetext{
About the Author
Thomas G Morris Ir works for General Electric as a minicomputer systems software analyst. His personal computer is a Technico Super Starter system with 32 K bytes of programmable memory, 2 K bytes of programmable read-only memory, and 2 K bytes of read-only memory containing a monitor and disk handler. Peripherals include an 8 -inch floppy disk, paper tape reader, a Southwest Technical Products AC-30 cassette unit, and a Texas Instruments 733 KSR terminal.
}

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 several lines of tent each' Atrer a key is programmed. by simply hitting key, all the text is sent to the processon edior This faature is handy for lengthy andiot olten wsed commands and may mechode mitiple staternents REwUMBERIMG ROUTIME With a single keystroke your program is renumbered Starting ine number and increment may be changed
4 BASIC BUFFER PROTECTOR. SYSTEM 2 sends a (CA) when the BASIC BUFFER is full This prevents BASIC lrom crashing
PaimiER ORNER. Sumply hat CTRL \(P\) to difec: output to Centronce printer
GNNVAL ROUTINE II NEW or CLOAD are tyDed, or RESET is hit by mistahe, your program may be recovered This is o salefy derice
OTHEA FLATURES
RUNSTOP stops execution untal amy other key is hit
CLEAR clears screen athen sends a ICRI HIC CLEAR to statt on new page
CTRL characters such as ESC, IF and CLEAR don't return TSN ERROR
RUB doesn't requre the SAIFT key to be depressed This quechens editim
Ceturns a lematiculy to Basic ate TAPE CAC
Suppesses premature CRIF Noumaty it RUB is used en whie CIOAONF
Suppreses to the next lige befort teaching the end of the current te SYSin a BASIC bre. the cursor whil SYSTEM 2 requies 24 of memory and is ayatate in 16.378 .48 K versions 2 prevents lins.

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detected, all lower-priority interrupts are inhibited until the current interrupt has been dismissed. The only exception to this is the reset function (which has a priority level of 0 ).

When an interrupt has been detected, a context switch is performed by fetching the new workspace pointer and program counter values from the appropriate interrupt vector locations. During this same time period, the old workspace pointer, program counter and status registers are saved in the new workspace registers R13, R14, and R15 respectively. When the interrupt has been dismissed by the interrupt subroutine, the processor is returned to its preinterrupt state by issuing a return (RTWP) instruction.

\section*{Input/Output}

The TMS 9900 employs a direct input/output (I/O) interface method which is designated the communication register unit (CRU). The communication register unit provides for a maximum of 4096 bits of I/O capa-
bility. From 1 to 16 bits may be set or reset at a time; additionally, single bits may be tested for their value.

\section*{Clock Hardware}

The heart of the clock assembly is a crystal-controlled, 60 Hz time-base generator sold by many electronic firms. The time-base generator produces an accurate square wave with a \(50 \%\) duty cycle, which is fed through IC3, a 7404 inverter (see figure 1). This buffered signal is then directed to IC1 (7490), which is set up as a divide-by-6 counter. The resulting 10 Hz signal is then divided by IC2, producing the final 1 Hz frequency.
The 10 Hz and 1 Hz frequencies are buffered by IC3 and made available for use as the minimum interrupt rate. One of the three rates is then directed to the clock input of IC4, which produces the necessary latched output. IC4 (7474 dual-D flip-flop) is needed to guarantee that an interrupt will not be missed, regardless of the level chosen. The exception: if a higher-priority interrupt monopolizes
\begin{tabular}{|lccc|}
\hline Number & Type & +5 V & GND \\
IC1 & 7490 & 5 & 10 \\
IC2 & 7490 & 5 & 10 \\
IC3 & 7404 & 14 & 7 \\
IC4 & 7474 & 14 & 7 \\
\hline
\end{tabular}


Figure 2: Flowcharts of routines to operate interval timer.
the processor for longer than the basic interrupt rate, the low-priority interrupt may suffer.

\section*{Hardware Interface}

The clock interface to the computer consists of a simple 2-wire hookup. One wire from the communication register unit port is connected to pin 1 of IC4, clear (CLR), via two sections of the 7404 inverter IC3. This connection provides both the reset and the

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disable signal to IC4. By momentarily bringing this line low, the current interrupt is dismissed, and further interrupts are enabled. However, if this line is held low, all clock interrupts are inhibited until pin 1 of IC4 is once again a logic 1 . The other connection is made between pin 6 of IC4 \((Q)\) and one of the interrupt inputs of the
computer, line 1 in this case. This line signals the processor that an interrupt has been requested by an external device, and is active low.

\section*{Software}

The software necessary to drive the real-time clock is shown in listing 1. To set the time of day and enable the


Figure 3: Flowchart of procedure that sets the clock.

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clock hardware, a call is made to the entry point STIM. This call instruction is followed in memory by the address of the memory location where the time of day may be found. This address pointer is placed into register R0 and the return address set by the first line of STIM code. The value to be used for hours is then compared to the maximum value allowed (eg: 24 for a 24 -hour clock). The same sequence of events occurs for both the minutes and seconds values. If the number to be used is greater than the maximum allowed or is negative, no further testing is done. Instead, the clock is cleared, the hardware is enabled, and a return is made to the calling routine. The calling routine must then set the interrupt mask to allow interrupts at the chosen level.

To obtain the time of day, a call to the GTIM routine is made. The call instruction is followed by the address of the memory location where the time will be stored.

To access the interval timer, the entry points of STCLK and RDCLK are used. STCLK will reset the timer to 0 , and RDCLK will place the current value of the interval timer into


Figure 4: Flowchart of routine that reads the clock.
the caller's R0.
When the clock hardware generates an interrupt, control is transferred to


Figure 5: Flowchart of procedure for dealing with a clock interrupt.

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the entry point CKINT. The interrupt is then reset, and further clock interrupts are inhibited by holding the clock line of the communication register unit low. Next, the interval timer is incremented, as is the rate counter. The rate counter is then compared to the basic clock frequency. If the result of the comparison is
less then 0 , interrupts are reenabled and the interrupted program resumed. If the result is greater than or equal to 0 , the rate counter is reset to 0 and the seconds counter is incremented. The same process that was used for the rate counter is then applied to the seconds, minutes, and hours counters. Lastly, interrupts are
reenabled and the interrupted program resumed.

\section*{Conclusion}

The method presented in this article will allow users a flexible and inexpensive way to maintain the time of day on their personal computer with low software overhead.

Listing 1: Routines that keep time using the real-time clock, written in assembler for the 9900 microprocessor.

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Listing 1 continued on page 292

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Listing 2: A program to demonstrate the use of the real-time clock.


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Listing 2 continued on page 302


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Listing 2 continued:


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Listing 3: Execution of the demonstration program of listing 2.
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\hline THE & HIIMEER & DF & TIEKS & ELAFSEI & IS: & 17 & Fris & THE & LOREECT & TIME & IS: & 23:59:47 \\
\hline THE & PIIMEEF & DF & TILKS & ELAFSEI & IS: & Et & Frid & THE & EOREECT & TIME & IS: & 23:59:50 \\
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\section*{Book Reviews}

\author{
Microcomputers and Physiological Simulation \\ James E Randall \\ Addison-Wesley \\ Reading MA, 1980 \\ 234 pages, hardcover \\ \(\$ 14.50\)
}

The observation of living systems is often a complex and difficult task; for those amateur or professional scientists who spend their time investigating the life signs and physiological responses of man and other animals, the use of laboratory computers in the data-gathering phase of their research has become a necessity. In most cases, the invasion of computers into the laboratory environment started with the advent of
minicomputers such as the LINC (Laboratory Instrument Computer) and later, the Digital Equipment Corporation PDP-12. The relatively low-cost and single-user nature of these systems made them especially attractive to the scientist willing to learn computer science. A typical installation would be optimized for data acquisition and formatting, and sophisticated data analysis, simulation, and modeling would generally be done on large, centralized mainframes such as the IBM \(360-91\). Time on these large machines was not cheap, and the budgets required to support extensive simulation studies were often prohibitive. For these reasons,
the study of biological systems by simulation has tended to be restricted and specialized in nature.

With the arrival of microprocessor hardware and software systems at much lower cost than minicomputers, and with the development of specialpurpose, high-speed arithmetic-processing units, creative and generalized simulation studies may now be performed with a rather modest expenditure of money; of course, inexpensive computing tools do not necessarily reduce the total cost of developing the correct system for a particular application. Here is where Dr Randall's book is invaluable: the background
information on microprocessors, combined with specific examples of biological data simulated with various hardware and software configurations, should allow any life-science experimenter to progress rapidly from the initial idea to a working simulation model.

The first chapters of the book describe the basic realities of the microcomputer world in a clear and comprehensive fashion; the various evolutionary trends in hardware and software design which gave rise to some of the more popular present-day microprocessor systems are explained in a cogent and enlightening manner that should orient

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the neophyte user amidst the growing maze of specifications and performance figures that seem to characterize the technical aspects of microprocessing. Thus, several years of practical experience have been condensed into what will soon be an indispensable reference for anyone considering the mathematical study of physiology.

In addition to people doing research, Microcomputers and Physiological Simulation should help those who would like to use interactive modeling as a teaching or demonstration device. All too often, an actual experiment may not turn out as expected, or the number of people observing the demonstration is so large that no one learns very much. Given these circumstances, a simulation approach for showing the dynamic realities of various physiological functions is both a clever and necessary approach. For example, in the study of cardiac output and central arterial pressure, a student could make a number of "experimental" manipulations of the circulatory system which would, on one hand, help to clarify what really goes on in an intact organism, but which, on the other hand, would be difficult to do within the confines of an experimental preparation. In addition, the time required to load a software model of the heart is much less than that needed to set up a live experiment (and, of course, the overall cost of simulation is likely to be much less than the real thing). So, given the desire to provide better instruction and reduce the time and money needed to give students first-hand experience in physiology, a teacher in the life sciences should consider carefully the interesting and useful techniques developed in this book.

Several of the examples in this book are extensions of topics that have been the subjects of articles in BYTE; the electrocardiogram (ECG) receives considerable atten-
tion, as does the nature of the neuronal axon petential and membrane conductances following various stimulation examples. In addition, the section on digital filtering and waveform distortion is relevant to a wide range of engineering and computer-science applications outside the life sciences. If you already have an Apple II, an S-100-based system, or a TRS-80 system, several BASIC language programs are provided so that you can get up and running
immediately; acquisition of the appropriate arithmeticprocessing option for your microprocessor will allow you to run more sophisticated and more dynamic simulation studies in a reasonable amount of time.

In a larger context, Microcomputers and Physiological Simulation is one more contribution to the field of personal, interactive microprocessor-based teaching tools which in specific circumstances offer
numerous advantages over conventional methods; the creativity and breadth of investigation allowed by flexible and well-conceived software and hardware systems are in many ways much greater, and certainly achieved with less effort, than our present experimental and pedagogical methods support. Of course, for undergraduate or graduate education and research, having a group of students organize and implement one of the simulations described


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in this text will not only provide them with an interesting tool within their specific field of study, but will also allow them to know in some depth the basics of the microprocessor environment which has become an essential substrate of almost all avenues of scientific and laboratory undertaking. Judging by the possibilities offered in Dr Randall's present work, the contribution of the microprocessor to laboratory science and technical education will be
enormous. Comprehensive guides of this sort serve to allow everyone easy access to a much more evolved set of teaching and experimental tools than has been available before.

\section*{Nicholas Bedworth}

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> Microcomputer-Analog Converter Software and Hardware Interfacing

Titus, Titus, Rony, and Larsen Blacksburg Continuing Education Series Howard W Sams, 1978 286 pages, softcover \(\$ 9.50\)

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software illustrations. All software in the book is for the 8080 microprocessor; conversion to other 8-bit microprocessors would range from trivial to moderately difficult.

The topics covered are: analog-to-digital (A/D) and digital-to-analog (D/A) conversion, interfacing digital panel meters, sample-andhold and multiplexer circuits, and miscellaneous conversion techniques. Appendices include data sheets and applications notes for a wide range of D/A and \(A / D\) devices ranging in cost from a few dollars to a few hundred dollars.

The reader of the book is assumed to be familiar with analog circuitry, with digital circuitry, and with 8080 programming. The level of familiarity required for analog devices is about the same as any radio amateur above the Novice class would have. The digital and computer familiarity are at about the same level; anyone who knows what a three-state buffer is and what the difference is between polled and interruptdriven I/O (input/output) should have no trouble with the text. Both polled and interrupt-driven systems are discussed, by the way, along with point graphics and measurement systems.
All in all, this is a good introduction to digital-toanalog interfacing, and a good reference book. The utility as the latter would be increased if there were a good descriptive index of the devices discussed. As with many of the books in this series, there are no blank pages in front or at the back for notes; most readers will probably want several pages of notes, so this is irritating.
John A Lehman
716 Hutchins \#2
Ann Arbor MI 48103

\footnotetext{
Engineer's Notebook: A Handbook of Integrated Circuit Applications
Forrest M Mims III Radio Shack Technical Publications
}

128 pages, softcover \$1.99

Engineer's Notebook is a collection of hundreds of simple circuits using integrated circuits, each one neatly hand-drawn and labeled, with all of the details (resistor and capacitor values, transistor numbers, etc) filled in. The devices used are primarily TTL (transistor-transistor logic), CMOS (complementary metal-oxide semiconductor), and linear function circuits.

As a programmer, I keep a file of useful subroutines for each machine and language with which I work. As the file grows, programming gets easier because more chunks of new programs come straight out of the file. Engineer's Notebook is the start of my circuit file. Since I am a novice to electronics, I simply cannot say whether an experienced circuit designer will find this collection useful. I tend to doubt it; the book is not written for him. For beginners, however, the circuits are a real help. Not necessarily because they will fit right into the next project you build, but because of the help they provide in learning how to use integrated circuits.
After a very brief (fourpage) introduction to basic electronics (where you are told what resistors, capacitors, and semiconductors are for), the book launches into CMOS circuits. In about forty pages it presents various circuits, starting with the use of simple gates and moving through switches and decoders, flipflops and counters, memory devices, and a variety of music- and noise-generating devices including the SN76488N complex sound generator. The TTL section covers simple gate circuits (including a couple of very informative pages on the use of Schmitt triggers), oscillators, selectors and decoders, then counters and dividers. The linear circuits include pages and pages of
op-amp applications, LED (light-emitting diode) bar displays, tone decoders, and uses for voltage-controlled oscillators.

If you do not know much about electronics and if you want to learn how to use integrated circuits, I suggest you buy one of Don Lancaster's "cookbooks" (or some other introductory text), and Engineer's Notebook. Use it as a workbook for the text; think of the circuits as answers to questions the text did not
pose. Go through them using the text and figure out why they work. Answer the question: Why use this value resistor (capacitor, transistor)? Before very long, you will know what you need to know.

I bought the book primarily to learn about TIL. However, because of the variety of circuits presented, I find myself more interested in CMOS and somewhat intrigued by linear circuits. I'm studying all three now. The book is
well worth its two-dollar price no matter what use you make of it.

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> Book Reviews

> Microcomputer Interfacing with the \(\mathbf{8 2 5 5}\) PPI Chip

Paul F Goldsbrough and Peter R Rony Blacksburg Continuing Education Series
Howard W Sams, 1979
224 pages, softcover \(\$ 8.95\)

Those who remember the integrated circuits available a year or two ago may wonder how an entire book could be devoted to a single nonmicroprocessor device. The traditional documentation for such a component is 'U25 on the System Monitor Board is a Motorola or equivalent 6820 PIA that contains two parallel I/O ports....In order to use it however, it must be set up with the proper software" (TDL System Monitor Board Manual). The 217 pages in this book are devoted to showing how the software and hardware for the Intel 8255 PPI (programmable peripheral interface) are set up. The general description (although not the details) is applicable to similar devices such as the above-mentioned 6820 (now 6821) or the Texas Instruments 6011.
The 8255 is a parallel interface device which allows software configuration of up to twenty-four I/O (input/output) lines. It has three basic modes: simple, handshaking I/O, and bidirectional. Up to three different ports may be used (depending on the mode), for input, output, or both. All of this makes the 8255 very flexible; it also makes it complicated.

The book discusses I/O schemes in general, and each of the 8255 modes in particular. Experiments are given for both port- and memory-mapped I/O. All hardware and software illustrated are for an 8080-based system, but the effort required to translate to another microprocessor is minor. Both polled-device
and interrupt-driven I/O are treated, and the book ends with an excellent discussion of the hardware and software requirements for master/slave processors. This section alone is worth the price of the book.

There are, as usual, a few minor faults. On page 63, the diagram of the hex inverter is not labeled; it is a \(74 \times \times 04\). Numbers in the book are sometimes given in octal and sometimes in decimal radix; unfortunately the author often neglects to mention which base he is using. I suppose ideally he ought to give everything in octal, decimal, and hexadecimal, but this convenience is probably not needed by the relatively sophisticated audience at whom this book is aimed.
Personally, I find it hard to read an assembler output such as that in the text which runs the op codes and the operands together. PUSH PSW is much easier to read then PUSHPSW. Finally, I would like a bookwide index of the experiments; it would make the book more useful as a reference.

But all of this is quibbling; the book is more than worth the price if you fall into one of three groups of readers. The first group is made up of people who have an elementary knowledge of digital logic (perhaps gained from some of the other Blacksburg books) and who want to learn how to use programmable interfaces in general and the 8255 in particular. The second group is made up of those who would like a more readable reference to the 8255 than is provided by the data sheet, and who want to see sample hardware and software interfaces. Last, anyone putting together multiple-processor systems would do well to look at the last section of the book for a quick and dirty, but fairly simple, way to do it. Let's see, how many channels should I put on my Z80...?

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\section*{Book fevisws}

\section*{Thrice Upon a Time}

James P Hogan
Ballantine Books, New
York NY, 1980
311 pages, softcover
\$2.25
Technical books and journals are useful for reference facts. Magazine articles and "construction" books supply the latest in the microcomputing art to sharpen our faculties. But these all address the issue of "how," and nourish the intellect with data. Books such as James P Hogan's latest novel, Thrice Upon a Time, answer a far more primal need. When the soul is anguished by a floppy disk's stubborn recalcitrance; when the heart is discouraged by that elusive last bug in the
sorting routine; when the mind is depressed by the . manufacturer's twelfth postponement of his shipping date, the solace from this book's visions is a soothing balm that carries one through to try again tomorrow.

To be sure, Mr Hogan's intricate plot far transcends mere home computing. In his story, which is concerned with some natural disasters and some achievements of mankind, he intertwines causes and effects so that each nourishes the other in an exciting race to enjoy the benefits of achievement without having to bear the extreme price the consequences of the advances seem to engender. Exploring this theme, plus presenting it in a bolero of variations, is a most complex plot concer-
ning a time communications machine. As distinct from the mysteriously operating transporting telephone booths of the H G Wells or Dr WHO variety, Hogan presents a rather welldocumented, even plausible, invention that takes advantage of the Tau wave effect. Now I am sure that Tau waves are not familiar phenomena to many readers. Mr Hogan also is cognizant of this deficiency in the physics background of most of us, and so he presents an explanation of this effect, its discovery and usefulness, with such clarity and vividness that one would no more deny Tau wave existence than one would deny gravity, black holes, or positronic brains. Though I leave the details to Mr Hogan's characters, suffice it for the moment that
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Sir Charles has invented a means to send messages back in time.

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Yes, this paradox has been explored before. But a marvelous craftsman and clear thinker such as James Hogan deserves his platform, and he exploits it with the quintessential detail and plausibility so reminiscent of the John W Campbell era.

So, you may concede, it's a gripping story. But where does my Altair or Apple come in? The answer is on just about every page. It is assumed in the story that at that time, 30 years from now, most people have a working knowledge of highlevel languages. The elderly Sir Charles has a small computer in his home, and it is not a remarkable occurrence. When he needs extra computing power or common data, he doesn't think twice about linking into the national data grid, which offers such services, as any other utility would offer its resources to home users today. What is so all-fired exciting about this story is that Sir Charles, with a setup not too different from what is available right now to us in our computer rooms, has sat down and used that computer to make a time machine. Sure he has access to a Tau wave generator, which most of us still would have trouble acquiring. But if Sir Charles can move such mountains with his setup,
surely we can at least move a few molehills with ours.
The book is top-notch. As a story, it's exciting and involving. As an inspiration, well I don't want to write any longer. My microcomputer awaits.

Jay P Lucas
3409 Saylor Pl
Alexandria VA 22304

Noise Reduction Techniques in Electronic Systems

\section*{Henry W Ott}

John Wiley \& Sons
New York NY, 1976
294 pages, hardcover
\(\$ 24.50\)
Although frequently unrecognized, electrical noise is a serious problem in the microcomputing environment. The home microcomputer is a recognized source of electromagnetic interference (EMI) or radio-frequency interference (RFI). The sound effects of computer games produced on a nearby radio are the mark of clever programming and poor electromagnetic shielding. Further, many prototype or even final versions of digital and analog projects fail completely or suffer occasional untraceable glitches because of improper attention to noise sources. Additionally, the rush to marry the continuous, frequently low-level, analog signals to fast-switching, noisy digital microcomputers promises many tremendous EMI problems. Intolerably, from tens to hundreds of millivolts of digital noise may appear in analog signals that never exceed 10 V and are frequently in the 0.1 V to 1 V range.

The above problems can be solved by the application of information about noise-preferably done systematically in the initial design rather than as a patchwork correction after the fact. Ott's extremely
well-written book contains this information and is one of the finest books on electrical noise, its sources, propagation, reception, and suppression. This book is an outgrowth of lectures at Bell Laboratories, and is directed at a technician-level twoyear college program.
Chapter 1 is a lucid discussion of noise sources, their coupling into your system, and a summary of the elimination methods: shielding, grounding, balancing, filtering, isolation, separation and orientation, circuit impedance control, cable design, and cancellation. The remainder of the book expands on these points.

Chapter 2 discusses the theory of shielding conductors, and why it does not always work. The distinction between capacitive and inductive coupling is carefully made. Grounding schemes for cables are clearly shown along with their relative merits.
Chapter 3 discusses pro-
cedures for minimizing ground loops, low-frequency and high-frequency grounding (they are different), and grounding shields properly. Especially important, and carefully treated, is the elimination of ground loops.

Chapter 4, "Other Noise Reduction Techniques," discusses balancing, powersupply decoupling, the much misunderstood transmission impedance of a power distribution system and its effect on system performance, high-frequency decoupling filters and digital circuits. Chapter 5, 'Passive Components," shows how these poorly appreciated components can dramatically affect system performance.

Chapter 6 is "Shielding Effectiveness of Metallic Shields" and is full of pleasant and unpleasant surprises about shielding properly. Ott discusses in detail how to really prevent EMI generation or reception.

Chapter 7 is on "Contact Protection" in switches and
relays. This unlikely sounding chapter in a book on noise suppression is quite logical. Switches and relays are notorious sources of EMI, and contact protectors yield improved life and performance and also have the beneficial effect of reducing EMI.

Chapters 8 and 9 are about intrinsic noise sources and active-device noise. These two chapters are of greatest value for low-level analog measurements rather than for microcomputer uses.

This book is not easy to read, as it assumes familiarity with DC circuit theory as well as with capacitors, inductors, and the complex impedance treatment of AC circuits. This level of expertise is not required for the book to be exceedingly valuable, however. It is clearly written with a lot of examples and good problems with their solutions.

Like a good novel, it was difficult for me to put this book down. The physical

significance of an equation is discussed clearly and at length; abundant graphs demonstrate concepts and provide valuable later reference. Finally, Ott is exceedingly practical. He has obviously spent long hours up to his elbows in wire and soldering irons tracing down and eliminating noise bugs, and he tells you his secrets.

The book is full of useful and interesting facts. For example, the switching of a single transistor-transistor logic (TTL) gate connected to a power supply through 10 inches of 22 gauge wire causes the ground connection of the integrated circuit to jump by 0.4 V . The synchronous switching of five gates could cause the ground to rise to 2 V I Since 2 V is the logic threshold for transistor-transistor logic, proper operation would be unlikely. This particular problem, a common cause of malfunctions in breadboarded circuits, is partially solved by bypass capacitors.

Do you know how a power-distribution bus strip
works? Why a double-sided printed-circuit board can give far better performance than point-to-point wiring, even with very heavy wire, or even a single-sided printed-circuit board? How much ground area do you need on a printed-circuit board? Do you know what a ferrite bead is, and how it suppresses noise? Do you know what the best type of filter capacitor for filtering an input line is? (The answer is not ceramic disc.) Why is copper a better magnetic shield than steel at high frequencies? How do you seal a cabinet door to EMI? Why, in a cabinet, does a series of ventilating holes with a total area of 1 square inch leak far less EMI than a single crack in the door with an area of 0.1 square inches? Ott explains this plus much more.

The book has a few shortcomings. The author does not always tie separately presented concepts together, and the reader must perform this synthesis. I would also like to have seen more infor-
mation on power-line EMI filters. The book was not written with computers in mind so there are no explicit references to them. The information on digital circuits is very brief. Counterbalancing these problems is the fact that the book does not deal with obsolete technologies, but handles fundamental principles which will always be a proper starting point for attacking a new area.
In summary, this is an excellent book. It should be read by every serious analog/digital designer. A careful reading and application of Ott's principles will save great pain, hours of labor, money, and in some cases even entire projects.

J N Demas
Department of Chemistry
University of Virginia
Charlottesville VA 22901

\section*{THE AQUARIAN CONSPIRACY BY MARILYN FERGUSON}

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\section*{Tracking Down the Modem Filters}

Since my article "An Answer/Originate Modem" was published in the June 1980 BYTE (page 24), I have found that the company which makes the CH1262 and CH1267 filters has moved. The current address and telephone number are:

Cermetek Microelectronics
1308 Borregas Ave
Sunnyvale CA 94086
(408) 734-8150

The filters are available as "miniModem" building blocks from this firm.

Ronald G Parsons
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> The Source and Tymshare Sign Operations Agreement

Source Telecomputing Corporation and Tymshare Inc have entered into a development and pilot operation agreement under which Tymshare, a computer service company, will provide a variety of facilities and services to increase the user capacity of The Source, an information utility. Tymshare's subsidiary, Tymnet Inc, which operates the TYMNET public packetcommunications network serving 200 cities, will be utilized in The Source's expansion program. The number of Source users, now approaching 5000, has increased beyond the system's present capabilities. Utilizing TYMNET's equipment and expertise will better serve existing users and permit The Source to accommodate thousands more.

Through The Source, owners of home computers, computer terminals, and word-processing equipment are able to access a variety
of data bases and programs by telephone connection to computers of The Source network. For details, contact The Source, Source Telecomputing Corporation, 1616 Anderson Rd, McLean VA 22102, (703) 821-6660.

\section*{Heath Offers Source Code to Its Customers}

Heath Company, Dept 350-390, Benton Harbor MI 49022, (616) 982-3210, is offering to its microcomputer customers source code for the company's internally developed system software and hardware. Source code to be released include those for Heath's cassette assembler, debugger, editor, and BASIC, and the source code for HDOS, Heath's disk operating system. Also being offered are the firmware for the \(\mathrm{H}-17\) and \(\mathrm{H}-89\) disk controllers and the firmware for the \(\mathrm{H}-19\) video terminal. The source code listings are \(\$ 25\) each except for HDOS, which is \(\$ 195\). The \(\mathrm{H}-19\) code will also include source on a Heath HDOS floppy disk and the character generator ROM (read-only memory) code. HDOS source code is available on floppy disk and includes the disk Assembler, Editor, BASIC, and DBUG, as well as PIP and other utilities. All products remain copyrighted, and even though source code is available, it is not being placed in the public domain. Heath welcomes licensing discussions for HDOS from other manufacturers.

\section*{Computer Bulletin Board for Radio Amateurs}

A free access program, called HAMNET, was established by Donald Stoner, W6TNS, and The Peripheral People, POB 524, Mercer Island WA 98040, (206) 232-4505. HAMNET utilizes the extensive MicroNet communications network, which allows access through almost two hundred local telephone numbers. Checking into HAMNET permits users to
post and retrieve messages for help wanted, equipment for sale, network news, schedules, and so on. Other features planned are propagation forecasts, Federal Communications Commission (FCC) news, new product announcements, and more. Public-domain programs are also available. HAMGAB is a ham "frequency" for two users to communicate or transfer programs. While the system is primarily oriented towards amateur radio buffs, it is open to all MicroNet customers. A subscription to MicroNet is \(\$ 9\) and \(\$ 5\) per connect hour. Customers are given a 128 K-byte block for storage of files. Information is available from Personal Computing Division, CompuServe Inc, 5000 Arlington Centre Blvd, Columbus OH 43220.
\(\overline{\text { New TRS-80 Keyboards }}\)
Radio Shack has announced an important
change in its TRS-80 Model I microcomputer. The new keyboard that uses a capacitive-contact system to eliminate the well-known keyboard debounce problem does not have removable key caps, which were on the older TRS- 80 models. Any attempt to clean the keyboard by removing the key caps will result in damage to only those TRS-80s that have the new keyboard. TRS-80s with the new keyboard are distinguished by a dull (as opposed to a shiny) finish on the keys and a curved (as opposed to a straight) slope of the keyboard tops when viewed from the side.

\section*{Educational Software for the Apple}

The Department of Natural Science at Eastern Kentucky University, Memorial Science 220, Richmond KY 40475 (606) 622-3735, has completed a search for educational courseware written for
microcomputers. They have compiled a catalog of educational software for the Apple II computer. Schools may obtain a copy of this catalog by writing to Professor John Wernegreen at the above address.


Catching the Khachiyan Bug
In Part 1 of "Khachiyan's Algorithm" by Berresford, Rockett, and Stevenson (August 1980 BYTE), a typographical error occurred in an Editor's Note by Gregg Williams (GW) at the bottom of the first column on page 202. The error at the end of line 7 of the italicized paragraph is in the equation
\[
t=K_{n}{ }^{p}
\]

The correction is \(t=K n^{p} . \square\)


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which builds the message to be transmitted from the single-byte code passed as an argument. The code passed is exactly as described in table 1 of Steve's article.

Admittedly, the software required to drive the transducer is neither processor nor speed independent, but the concept is simple enough to be used on virtually any system.

Listing 1: This software, called from Cromemco FORTRAN, is used to drive an ultrasonic transducer directly from a parallel output port. Output frequencies and timing are based on the 4 MHz clock rate of the author's Z 80 system.



Listing 1 continued on page 316

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Listing I continued:


\section*{Steve Ciarcia's Comments}

My compliments to Alan Trimble on his ingenuity. An ultrasonic transducer tied directly to one line of an output port is a very viable approach. In fact, the first control circuit I designed employed an NE555 timer, used as a tone-burst generator, and an ultrasonic transducer attached as you describe. This additional \(\$ 0.50\) component (the NE555) further reduces the software overhead while maintaining minimum system cost.

When I wrote the article, I made a tough decision. Either I could present a \(\$ 6\) interface designed for use with a computer that has existing output ports, a particular system clock rate; and a particular processor, or I could make the hardware smarter (and more expensive) and yet usable on virtually any computer. With the first alternative, I would have gotten about 200 letters asking how to design a parallel output port;: the second was the better way to proceed under the circumstances.

There are often many approaches to the design of an interface. My philosophy is to try to tender the one that has the greatest potential for being implemented by BYTE readers. I'd rather not be remembered for my great theoretical presentations. I depend on intelligent people like Mr Trimble to read between the lines and customize my interfaces to meet their individual system requirements.

Regarding the expense of buying the equipment, I am familiar with only the MicroMint unit (the Busy: Box). For the purchase price, you get a unit that is assembled and tested; it includes a case, power supply, and instructions; and it comes with the cables required to plug it in and use it.

Anyone wishing to build Mr Trimble's design for a control interface can get the 40 kHz transducer (part, number MM 1002) for \(\$ 6\) postpaid from:

> The MicroMint Inc 917 Midway
> Woodmere NY 11598
> (516) 374-6793

My thanks to Mr Trimble for pointing out this approach to interface design....Steve Ciarcia

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\end{abstract}

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\title{
More on Skip Chains
}

Mark S Williamsen, 3114 Central St, Evanston IL 60201
In regards to Geoffrey Gass's Technical Forum 'Mining the Skip Chain" (February 1980 BYTE, page 148), I would like to add an alternative which has several advantages: a lookup table. A skip chain in its simplest form (testing a single byte to access routines located within a single page [ 256 bytes] of memory) uses a minimum of 4 bytes of 6800 -microprocessor code per test. If the skip chain is to call routines outside of that one page, then 7 bytes are required for each comparison. (See listings 1 and 2.)

On the other hand, a lookup table needs a search routine (as in listing 3) of about 25 bytes and 3 additional bytes for each entry in the table if extended addressing is used. The break-even point is about 6 comparisons. Beyond that, the lookup table scheme uses less memory. It has the additional advantage that the program does not have to be reassembled to add new entries. In fact, if an end-of-table trap is used, as in listing 3, new entries can be written into a programmable read-only memory (PROM) without changing or erasing any previous data. This is ideal for use in a PROM monitor because new commands and routines can be added at any time if blank space is left following the table.


Listing 1 continued: 00028 000F 20 EF 00029

BRA END

START GE
CODE NOT FOUND

TOTAL ERRORS 00000

Listing 2
\begin{tabular}{|c|c|c|c|c|c|c|}
\hline \multicolumn{3}{|l|}{\multirow[t]{2}{*}{\[
\begin{aligned}
& 00001 \\
& 00002
\end{aligned}
\]}} & \multicolumn{4}{|l|}{\multirow[t]{2}{*}{- NAM SKIPEX ADDRESSING}} \\
\hline & & & & & & \\
\hline \multicolumn{3}{|l|}{00003} & \multicolumn{4}{|l|}{*GOES TO ONE OF SEVERAL ROUTINES DEPENDING ON} \\
\hline \multicolumn{3}{|l|}{00004} & \multicolumn{4}{|l|}{*CONTENTS OF ACC. B} \\
\hline \multicolumn{3}{|l|}{00005} & \multicolumn{4}{|l|}{'M, WILLIAMSEN 1/31/'80} \\
\hline \multicolumn{3}{|l|}{00006} & \multicolumn{4}{|l|}{- DEFINITON OF DUMMY LABELS TO
SATISFY ASSEMBLER;} \\
\hline 00007 & & FF00 & INCH & EQU & \$FF00 & \\
\hline 00008 & & 0000 & C1 & EQU & 0 & \\
\hline 00009 & & 0000 & C2 & EQU & 0 & \\
\hline 00010 & & 0000 & C3 & EQU & 0 & \\
\hline 00011 & & 0000 & CN & EQU & 0 & \\
\hline 00012 & & 0000 & R1 & EQU & 0 & \\
\hline 00013 & & 0000 & R2 & EQU & 0 & \\
\hline 00014 & & 0000 & R3 & EQU & 0 & \\
\hline 00015 & & 0000 & RN & EQU & 0 & \\
\hline 00016 & 0000 & BDFF00 & START & JSR & INCH & \begin{tabular}{l}
GET \\
CHARACTER IN ACC. B
\end{tabular} \\
\hline 00017 & 0003 & Cl 00 & FIRST & CMP B & \({ }^{*} \mathrm{Cl}\) & \(\mathrm{B}=\mathrm{CODE} 1\) ? \\
\hline 00018 & 0005 & 2603 & & BNE & SEC & CONTINUE IF NO \\
\hline 00019 & 0007 & 7E0000 & & IMP & R1 & \begin{tabular}{l}
GO TO ROUTINE \\
1 IF YES
\end{tabular} \\
\hline 00020 & 000A & Cl 00 & SEC & CMP B & \#C2 & \(\mathrm{B}=\mathrm{CODE} 2\) ? \\
\hline 00021 & 000C & 2603 & & BNE & THIRD & CONTINUE IF NO \\
\hline 00022 & 000E & 7E 0000 & & JMP & R2 & GO TO ROUTINE 2 IF YES \\
\hline 00023 & 0011 & Cl 00 & THIRD & CMP B & \({ }^{3} \mathrm{C} 2\) & \(\mathrm{B}=\) CODE 3 ? \\
\hline 00024 & 0013 & 2603 & & BNE & NTH & CONTINUE IF NO \\
\hline 00025 & 0015 & 7E0000 & & JMP & R3 & GO TO ROUTINE 3 IF YES \\
\hline 00026 & & & * & & & \\
\hline 00027 & & & * & & & \\
\hline 00028 & & & * & & & \\
\hline 00029 & & & *FURTH & ER COM ARY & ISONS & \\
\hline 00030 & & & * & & & \\
\hline 00031 & & & - & & & \\
\hline 00032 & & & * & & & \\
\hline 00033 & 0018 & Cl 00 & NTH & CMP B & \#CN & \(B=C O D E N ?\) \\
\hline 00034 & 001A & 26 E4 & & BNE & START & GET NEW INPUT IF NO \\
\hline 00035 & 001 C & 7E0000 & & JMP & RN & GO TO ROUTINE N IF YES \\
\hline 00036 & & & & END & & \\
\hline
\end{tabular}

TOTAL ERRORS 00000

Listing 3
\begin{tabular}{|c|c|c|c|c|c|c|c|}
\hline \multirow[t]{2}{*}{\[
\begin{aligned}
& 00001 \\
& 00002
\end{aligned}
\]} & & & & \multicolumn{4}{|l|}{\multirow[t]{2}{*}{\begin{tabular}{l}
NAM LOOKUP \\
*COMMAND DECODER WITH LOOKUP \\
TABLE.
\end{tabular}}} \\
\hline & & & & & & & \\
\hline 00003 & & & & \multicolumn{4}{|l|}{*GOES TO ONE OF SEVERAL ROUTINES DEPENDING ON} \\
\hline 00004 & & & & \multicolumn{4}{|l|}{*CONTENTS OF ACC. B} \\
\hline 00005 & & & & \multicolumn{4}{|l|}{*M, WILLIAMSEN 1/31/80} \\
\hline 00006 & & & & \multicolumn{4}{|l|}{- DEFINITON OF DUMMY LABELS TO SATISFY ASSEMBLER;} \\
\hline 00007 & & FFOO & & INCH & EQU & \$FF00 & \\
\hline 00008 & & 0000 & & Cl & EQU & 0 & \\
\hline 00009 & & 0000 & & C2 & EQU & 0 & \\
\hline 00010 & & 0000 & & C3 & EQU & 0 & \\
\hline 00011 & & 0000 & & CN & EQU & 0 & \\
\hline 00012 & & 0000 & & RI & EQU & 0 & \\
\hline 00013 & & 0000 & & R2 & EQU & 0 & \\
\hline 00014 & & 0000 & & R3 & EQU & 0 & \\
\hline 00015 & & 0000 & & RN & EQU & 0 & \\
\hline 00016 & 0000 & BD & FFOO & START & JSR & INCH & \begin{tabular}{l}
GET \\
CHARACTER IN ACC. B
\end{tabular} \\
\hline 00017 & 0003 & CE & 0018 & & LDX & \#TABLE & INITIALIZE \\
\hline
\end{tabular}

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Listing 3 continued:
\begin{tabular}{|c|c|c|c|c|c|c|}
\hline 00018 & 0006 & A600 & \multirow[t]{2}{*}{GCl} & LDA A & \multirow[t]{3}{*}{\(\chi\)} & GET CODE FROM TABLE. \\
\hline \multirow[t]{2}{*}{00019} & \multirow[t]{2}{*}{0008} & \multirow[t]{2}{*}{08} & & \multirow[t]{2}{*}{INX} & & INCREMENT \\
\hline & & & & & & POINTER. \\
\hline 00020 & 0009 & 81 FF & & CMP A & /\$FF & IF END OF TABLE \\
\hline 00021 & 000B & 27 F 3 & & BEQ & \multirow[t]{2}{*}{START} & GET NEW INPUT. \\
\hline 00022 & OOOD & 11 & & CBA & & \[
\begin{aligned}
& \text { DOES ACC, } \\
& \text { B = CODE? }
\end{aligned}
\] \\
\hline \multirow[t]{2}{*}{00023} & \multirow[t]{2}{*}{OOOE} & \multirow[t]{2}{*}{2704} & & BEQ & FOUND & IF YES, GO TO \\
\hline & & & & & & ROUTINE. \\
\hline \multirow[t]{2}{*}{00024} & \multirow[t]{2}{*}{0010} & \multirow[t]{2}{*}{08} & \multirow[t]{5}{*}{NEXT} & INX & & INCREMENT \\
\hline & & & & & & POINTER TO \\
\hline \multirow[t]{2}{*}{00025} & \multirow[t]{2}{*}{0011} & \multirow[t]{2}{*}{08} & & \multirow[t]{2}{*}{INX} & & NEXT CODE IN \\
\hline & & & & & & TABLE \\
\hline 00026 & 0012 & 20 F 2 & & BRA & GCl & IF NO. \\
\hline \multirow[t]{2}{*}{00027} & \multirow[t]{2}{*}{0014} & \multirow[t]{2}{*}{EE 00} & \multirow[t]{3}{*}{FOUND} & LDX & X & LOAD POINTER \\
\hline & & & & & & FROM TABLE \\
\hline \multirow[t]{2}{*}{00028} & \multirow[t]{2}{*}{0016} & \multirow[t]{2}{*}{6E 00} & & \multirow[t]{2}{*}{JMP} & \multirow[t]{2}{*}{X} & AND GO TO \\
\hline & & & & & & ROUTINE, \\
\hline \multicolumn{3}{|l|}{00029} & \multicolumn{4}{|l|}{*LOOKUP TABLE STARTS HERE:} \\
\hline 00030 & 0018 & 00 & TABLE & FCB & Cl & CODE 1 \\
\hline \multirow[t]{2}{*}{00031} & \multirow[t]{2}{*}{0019} & \multirow[t]{2}{*}{0000} & & \multirow[t]{2}{*}{FDB} & \multirow[t]{2}{*}{R1} & ADDRESS OF \\
\hline & & & & & & ROUTINE 1 \\
\hline 00032 & 001B & 00 & & FCB & C2 & CODE 2 \\
\hline \multirow[t]{2}{*}{00033} & \multirow[t]{2}{*}{001C} & \multirow[t]{2}{*}{0000} & & \multirow[t]{2}{*}{FDB} & \multirow[t]{2}{*}{R2} & ADDRESS OF \\
\hline & & & & & & ROUTINE 2 \\
\hline 00034 & O01E & 00 & & FCB & C3 & CODE 3 \\
\hline \multirow[t]{2}{*}{00035} & \multirow[t]{2}{*}{001F} & \multirow[t]{2}{*}{0000} & & \multirow[t]{2}{*}{FDB} & \multirow[t]{2}{*}{R3} & ADDRESS OF \\
\hline & & & & & & ROUTINE 3 \\
\hline 00036 & & & * & & & \\
\hline 00037 & & & - & & & \\
\hline 00038 & & & * & & & \\
\hline \multirow[t]{2}{*}{00039} & & & \multicolumn{4}{|l|}{\multirow[t]{2}{*}{-FURTHER TABLE ENTRIES AS NECESSARY}} \\
\hline & & & & & & \\
\hline \multicolumn{3}{|l|}{00040} & \multicolumn{3}{|l|}{NECESSARY} & \\
\hline \multicolumn{3}{|l|}{00041} & \multicolumn{4}{|l|}{*} \\
\hline \multicolumn{3}{|l|}{00042} & \multicolumn{3}{|l|}{*} & \\
\hline 00043 & 0021 & 00 & & FCB & CN & CODE N \\
\hline \multirow[t]{2}{*}{00044} & \multirow[t]{3}{*}{0022} & \multirow[t]{3}{*}{0000} & & FDB & RN & ADDRESS OF \\
\hline & & & & & & ROUTINE N \\
\hline 00045 & & & & END & & \\
\hline
\end{tabular}

TOTAL ERRORS 00000

\section*{Beware of Interrupts}

\section*{Dave Feldman, 1856 Viking Way, La Jolla CA 92037}

I have read with interest Michael McQuade's article "A Fast, Multibyte Binary to Binary-Coded-Decimal Conversion Routine" (February 1980 BYTE, page 106).

I wish to make the following comment regarding the program presented in listing 1, on page 110.

If the program is run in an environment in which interrupts exist, the user may experience difficulty in obtaining correct results should an interrupt occur when execution is just before RLOOP or just after LAB17 (in the area of the DCX SP instructions). The data on the stack (which is "recovered" by use of the two DCX SP instructions) will be overwritten by the return address saved when execution is transferred to the interrupt service routine. To prevent this problem, replace each occurrence of DCX SP DCX SP with a PUSH H or keep interrupts off while the subroutine is executing. I recommend the former.

Technical Forum is a feature intended as an interactive dialog on the technology of personal computing. The subject matter is open-ended, and the intent is to foster discussion and communication among readers of BYTE. We ask that all correspondents supply their full names and addresses to be printed with their commentaries. We also ask that correspondents supply their telephone numbers, which will not be printed.

\title{
Bending BASIC in a Recursive Form
}

\author{
Colin Newell, Newcastle, Australia
}

I read Stanley Swizer's "The Towers of Hanoi: Solution Using BASIC Recursion" ("Programming Quickies," March 1980 BYTE, page 240) with interest. He has shown us how to solve this problem in BASIC; however, my BASIC does not incorporate a stack. So here is my way of solving this problem (listing 1).

\section*{Listing 1}
```

10 INPUT "NO OF DISKS ";N
20 LET I = 1
30 LET J = 3
4 0 ~ G O S U B ~ 1 0 0 ~
5 0 ~ G O T O ~ 3 0 0 ~
100 IF N = 0 THEN RETURN
110 LET N = N - 1
120 LET J = 6-I - J
130 GOSUB 100
140 LET J = 6-I - J
150 PRINT "MOVE TOP DISK ON TOWER ";I;" TO TOWER "; J
160 LET I =6-I - J
1 7 0 GOSUB 1 0 0
180 LET I = 6 - I - J
190 LET N = N + l
200 RETURN
300 END

```

\section*{READY}

\section*{RUN}

NO OF DISKS ? 3
MOVE TOP DISK ON TOWER 1 TO TOWER 3
MOVE TOP DISK ON TOWER 1 TO TOWER 2
MOVE TOP DISK ON TOWER 3 TO TOWER 2
MOVE TOP DISK ON TOWER 1 TO TOWER 3
MOVE TOP DISK ON TOWER 2 TO TOWER 1
MOVE TOP DISK ON TOWER 2 TO TOWER 3
MOVE TOP DISK ON TOWER 1 TO TOWER 3

\section*{Programming in the Dark}

\section*{Jeffrey Sainio, 143 N Moreland \#106, Waukesha WI 53186}

Robert Glaser's article on programming 2708-type read-only memories ("Program Those 2708s," April 1980 BYTE, page 198) is a boon to those of us who are interested in programmer boards with three-figure price tags. Having built a similar board, let me offer some pointers I have learned:
- 2708s program faster in the dark. This holds true for the devices manufactured by Intel, Texas Instruments, and Motorola that I have used. The speed difference between total darkness and bright incandescent light is over ten to one. The devices also read Os more easily in the dark (ie: a marginally programmed bit may read correctly in the dark, but not in the light).
- Programming can be done interactively. By pulling the +26 V and CS (chip select) lines low, a byte of information can be read through an input port. If a logical exclusive-OR of the original data and the read data yields all os the byte does not need programming. The result of the exclusive-OR may be inverted and ORed with the desired data, then tested. If the result is anything other than hexadecimal FF, the device should be erased. If a programming pulse is to be applied, remember to set CS at +12 V before applying the +12 V ; and remember that +26 V must be turned off before reading the device.
By using these techniques, I can program a 2708 in three to fifteen seconds. After an entire programming loop has been executed with no false bits indicated, I shine a high-intensity lamp through the device's window to catch any marginal bits. This ensures that all bits are programmed solidly.

Having used this programming technique on devices rated at 450 ns installed in a Z 80 system (running at 4 MHz with no WAIT states), I can say that the method may not seem "kosher," but it is fast and error-free.


\section*{Progremaning Owickies}

\section*{6502 Loop Control}

\author{
Gordon Campbell, 36 Doubletree Rd, Willowdale, Ontario, Canada
}

For clarity, the best way to loop through a field is to start at the beginning and stop at the end. It is important to be able to change the content or length of the field without having to change the code that handles it. Some people use a marker byte such as hexadecimal 00 to stop the loop; however, if you make your assembler work for you, this is unnecessary.
Listing 1 is an example of how to make your assembler perform this task. The X register is used to index through a field. The code is set up so that when the register hits zero, execution is terminated. Thus, begin by loading the register with 256 minus the length of the field. Then work through the field from start to end by loading the accumulator with the byte stored at the end of the message minus 256 , plus the contents of the \(X\) register. The result is that when the \(X\) register hits zero, you are done.
The code shown has been used with two assemblers:


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Carl Moser's ASSM/TED, and Dan Fylstra's 6502 Assembler in BASIC, published by Personal Software. Fylstra's assembler generates an error message on the first pass if MSG and MSGEND follow the code that uses them, but then produces correct object code. Of greater concern is the fact that both assemblers do not notice if MSG is greater than 256 bytes long. This should be an error condition that raises a diagnostic. In both cases the only result is that incorrect code is produced.

PRLOOP \(=7002 \quad\) MSG \(=700 \mathrm{C} \quad\) MSGEND \(=701 \mathrm{~B}\)
/PRINT = FFD2
//0000,701B,701B
\(>\)

\section*{Sorting With a Catch}

Paul T Brady, 91 Marcshire Dr, Middletown NJ 07748
So much has been said concerning various sorting algorithms that it hardly seems possible to be able to contribute to this topic; and yet, in a small business (a nature
center, to be precise), we have developed a sorting routine that handles accounting entries, mailing list entries, etc, at a speed that leaves fancy algorithms in the dust. The special beauty of this technique is that it is very simple, and involves only a slight modification of the usually terribly inefficient brute-force bubble technique.

The routine has another advantage-it will not disturb the order of ties. For example, if one orders by zip code, it will not rearrange entries having the same zip code. This is an advantage if the list were previously alphabetized and you wanted to retain alphabetization within zip codes.

There is a catch. This routine is absolutely terrible for ordering a true random list. The routine is designed to handle a list that already is nearly in order, and you want to add a few extra items. But this is exactly the case in a mailing list, in which you add 20 names to a 1500 -name list, or in accounting, in which you add 15 transactions to a 60 -item list.

\section*{The Algorithm}

The algorithm works as follows: assume that you have an array of L items, \(\mathrm{A}(\mathrm{I}), \mathrm{I}=1\) to L . In the standard bubble sort, you compare A(1) with A(2). Assume that you want the list ordered from smallest to largest entry. Then, if \(A(1)<=A(2)\), leave them alone, but if \(A(1)>A(2)\), reverse them and proceed pairwise down the list. The last comparison made is between \(A(L-1)\) and \(A(L)\), reversing them if \(\mathrm{A}(\mathrm{L}-1)>\mathrm{A}(\mathrm{L})\). You have just made \(\mathrm{L}-1\) pairwise comparisons.

For those unfamiliar with this method, a moment's thought should demonstrate that in this first pass you have guaranteed that the largest entry has sunk to the bottom. That is, \(\mathbf{A}(\mathrm{L})\) now is the largest entry. In subsequent passes, it is no longer necessary to test anything against \(\mathrm{A}(\mathrm{L})\). So, the second pass ends by comparing A(L-2) with \(A(L-1)\). But now, you have guaranteed that the second biggest entry is in the L-1 slot, so each successive pass requires one less comparison.

Even with the shortcut of cutting each pass to be one shorter than the previous pass, this method still takes a long time. But now consider the following. Suppose, during the first pass of \(\mathrm{L}-1\) comparisons, we check to see just how well ordered the list already is. We will set up a window in which \(W\) equals the first pair that was ordered, and \(X\) equals the last pair. Suppose the list contains 85 items, but after the first pass, \(\mathrm{W}=26\) and \(X=\) 34. This means that everything beyond 34 is already ordered. Items earlier than 26 may not be completely in order when considering later items, but the very next pass can compare entry twenty-five with entry twenty-six; ie: at \(W-1\). So, we have a window that will ascend to the top of the list. Further, on each successive pass we will reevaluate \(W\) and \(X\). As soon as \(X<=1\), we can stop. (Note: X can equal zero in the special case that the entire list was already in order before you invoked the routine.)

\section*{The Program}

This idea is so simple that it cannot be new; yet, I have not seen it mentioned, and even if it is published elsewhere, it is worth repeating. The code in listing 1 is for North Star BASIC, in which the semicolon separates statements on the same line. \(W\) and \(X\) have already been defined. T, T1, and T2 are temporary variables. I is an in-
dex variable, and \(A(I)\) is the array. The \(A(I)\) could also be pointers to string variables; the technique is clearly not limited to ordering numbers.

A final comment. This routine is at its very best if the list is already completely ordered before calling it; it makes one pass through the list, discovers that the list is already ordered ( \(X=0\) in statement 135), and quits. This is not at all a ridiculous situation. We have several programs that require ordered data in files, and call the sort routine whenever a "write" is called for, even if nothing was done to disturb the order. In such instances, the sort is only a momentary delay.

Listing 1: A bubble sort with a window. This routine is designed specifically to sort lists with only a few entries out of order. It can even be used to check a list quickly to ensure that all entries are ordered. The main attraction, though, is its simplicity; the actual North Star BASIC code is only eight lines long.
\(100 \mathrm{~W}=2 ; \mathrm{X}=\mathrm{L} ; \mathrm{REM} \mathrm{W}=\) UPPER WINDOW BOUND, \(\mathrm{X}=\) LOWER
105 FOR I=1 TO L
\(110 \mathrm{Tl}=\mathrm{X} ; \mathrm{X}=0 ; \mathrm{FF} \mathrm{W}<2\) THEN \(\mathrm{W}=2 ; \mathrm{T} 2=\mathrm{W}-1 ; \mathrm{W}=0\)
115 FOR J=T2 TO T1-1;REM BEGIN AT T2. STMT 110 ASSURES T2 \(>=1\).
120 IF \(A(I)<=A(I+1)\) THEN 135
\(125 \mathrm{~T}=\mathbf{A}(\mathrm{I}) ; \mathrm{A}(\mathrm{I})=A(\mathrm{I}+1): A(\mathrm{I}+1)=\mathrm{T} ; \mathrm{REM}\). OUT OF ORDER, REVERSE.
\(130 \mathrm{X}=\mathrm{J} ; \mathrm{IF} \mathrm{W}=0\) THEN \(\mathrm{W}=\mathrm{J}\); REM \(\mathrm{W}=0\) IMPLIES FIRST REVERSAL.
135 NEXT;IF X < = 1 THEN EXIT 145;NEXT
140 STOP;REM FOR COMMENT ONLY - WILL NEVER BE REACHED.
141 REM WILL NEVER FINISH SECOND "NEXT" OF 135
145 REM ROUTINE ENDS HERE, LIST IS ORDERED.

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\title{
Notes on Absolute Location Interfaces to Apple Pascal
}

\section*{Daniel D Sokol, 211 Fall Creek Dr, Felton CA 95018}

After seeing the March 1980 BYTE Editorial ("Hunting the Computerized Eclipse," page 6), I realized that many other users of Apple Pascal have encountered the same problem I have: the difficulty in accessing memory locations directly. I have written two programs that help to minimize this problem.

Listing 1: A UCSD Pascal compilation unit called PEEKPOKE which provides the modules PEEK and POKE that allow access to arbitrary memory locations. Care should be exercised in using this routine, because data vital to the operating system may be inadvertently modified.
*SS4,LPRINTER:*)

unit PEEKPOKE; intrinsic code 26;
interface
procedure POKE(var ADDA, DATA:integer); function PFEK (var ADDR: integer): inteqér:
plementation
zype PA = paeked array[0..11 of 0..255) MAGIC = record case boolean of true: (INT: integer) slee: (PTR: "PA)
var Cheatimagic:
procedure TEST(var DATA:inteqer) forward
procedure POKE;
pocedur
TEST (DATA) ;
CHEAT. INT: =ADDR;
CHEAT . PTR- \([0]:=\) DATA
end:
function PEek,
begin
PEETT, INT: \#ADDR;
Pnd;
procedure TEST;
begin \(\begin{aligned} & \text { DATA: }=\text { abs (DATA mod 256) } \\ & \text { end; }\end{aligned}\)
end:

This program has been desiqned to be added to the pascal SYSTEM.LIBRARY. See section 4.2 in the reference menusl for info on the librarian.
(* I used seqment 25 *)
(* Formst is : *) * Poke (addr, data):

Both addr and data must be INTEGRR variables (not constants)

To use in a proaram you must follow the proqram USES PEEKPOKF,
(* this defines a variant *)
(* record which will map *) (* to an absolute hardware a)
(* Main procram *)
begin
(*DUMmy procram *)
end.

The first program, entitled UNIT.PEEK.TEXT (shown in listing 1), is a library intrinsic that performs the same functions as PEEK and POKE in BASIC. It uses the var-iant-record technique to access arbitrary addresses in memory.
The second program is called CALL.ASSY.TEXT (shown in listing 2). It is an assembly-language linkage which allows the user to call, from a Pascal routine, an external (non-Pascal) assembly-language program at an arbitrary address in memory. It is, of course, possible to call an assembly-language module that is linked into a Pascal program, such as this module itself, but the linker has no provision for fixing an absolute address of the called routine. Thus this routine is required as an escape to routines found at locations fixed by hardware, such as the read-only memory regions of the typical Apple input/output (I/O) cards.

Listing 2: CALL, a UCSD Pascal system assembly-language program for a 6502 processor. This routine will call an arbitrary \(a b-\) solute address, such as an address associated with a read-only memory routine in an interface card, which is not normally accessible from Pascal. As in listing 1, care should be exercised in using this routine.
```

procram to create a call function for pascal in the apple;
Use this assembly lanquge proqram to call programs
that are not normally accessable from Pascal.
To use: ASSEMBLE this proaram and save the code file.
Define a PROCEDURE in your proaram as follows -
PROCEDURE CALI(addr): EXTERNAL:
addr must be an inteqer varlable.
Compile your prooram and then run the linker.
When asked for the LIB. name type the name of the save code file.
WARNING : ANY PROGRAM THAT CHANGES MEMORY LOCATIONS MAY INTFRFRRRE WITH THF PASCAL DPERATING SYSTEM.

```
```

.TITLE. * CALL SURR - 15 FEB 89 - DAN SOKOL"
.mACRO POP
PLA
.ENDM l+1
MMACRO PUSH
PROC CALL.I
procedure CALL(ADDR: inteaer): external;

```


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"TelaCom" is a modem-control software packaga designed to allow the user of any CP/M* Ivarsion 1.4 or earliar) or Cromemco CDOS"(version 2.17) equipped microcomputer remote operation
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1. Dial and connect to any Bell 103A compatible computer system;
2. Transfer any ASCII taxt file or data between systems;
3. Remote operation of the TetaCom equipped system through standard telephone lines;
4. Automatic dialing of three user-defined telephone numbers by a single-key control.
TeleCom can also be used as the basis for other user programs which require connection to remote system. The user can run other computer programs on his system while still remaining connected to the remote system. For example this can be a program to send daily cash sales or customer records.
Herdware Requirements:
TeleCom can operate on any B080, 8085, or \(\mathbf{Z 8 0}\) based computer system equippad with CP/M* or any CP/M * derivative (IMDOS, CDOS* , atc.). TeleCom uses 2K of memory spece above CP/M. A D.C. Hayes Micromodem-100 or \(80-103 A^{\circ}\) board is also required.

Why don't you or your computer give us a call, we'll be glad to tell either of you all about the TeleCom package.

ComputerLand of Miles, Ltd. 9511 N. Milwaukee Avenue Nilas, IL 60648 (312) 967.1714


\footnotetext{

}

\title{
A Lowercase-to-Uppercase Converter \\ Roger L Degler \\ Motorola Inc \\ Mail Drop M2 90 \\ 2200 W Broadway \\ Mesa AZ 85202
}

Many ASCII-encoded keyboards are capable of generating both uppercase and lowercase codes. Many of these contain a jumper option that will disable the lowercase characters, and generate their uppercase counterparts. But some keyboards do not offer this option, and trying to use an uppercase/lowercase keyboard on a system that requires only uppercase characters becomes very frustrating. Of course, the uppercase codes may be generated singly by pressing the shift key.

The problem with this is trying to
remember to press the shift key every time you want to enter an uppercase letter and to leave it unpressed when you want to enter a number or lowercase symbol. Mistakes are inevitable. However, there are two possible solutions: convert the lowercase characters to uppercase with additional software in the character input routine; or perform the conversion with a hardware circuit between the keyboard and the computer.
The software approach is the better alternative. The software, shown in listing 1 , is extremely simple and can
be as versatile as the user desires it to be. For example, by setting or clearing a software-flag location, the lowercase characters may be enabled or disabled. This assumes that the user has access to the computer's character-input routine and that the routine can be modified.

The hardware conversion method, on the other hand, is somewhat less versatile and requires more effort to implement. Versatility is lost because alternation between the two modes, that is, allowing and disallowing lowercase, requires the physical act of

Listing 1: Software routine to convert from lowercase to uppercase ASCII (American Standard Code for Information Interchange). This routine is relocatable to any address in memory. It assumes that the character to be converted resides in the accumulator; the result is left in the accumulator. The routine is written for the 6800 microprocessor and requires only 13 bytes.
\begin{tabular}{|c|c|c|c|c|c|}
\hline Hexadecimal Address & Hexadecimal Code & Label & \begin{tabular}{l}
Instruction \\
Mnemonic
\end{tabular} & Operand & Commentary \\
\hline 0100 & 847 F & CNVT & ANDA & \#\$7F & Mask to 7 bits. \\
\hline 0102 & 8161 & & CMPA & *\$61 & Check for lowercase. \\
\hline 0104 & 2D 06 & & BLT & NOCNVT & Do not convert if not. \\
\hline 0106 & 817 A & & CMPA & \#\$7A & Do not convert special characters \\
\hline 0108 & 2E 02 & & BGT & NOCNVT & at end of ASCII code table. \\
\hline 010A & 8A 5F & & ANDA & /\$5F & Convert to uppercase. \\
\hline 010 C & 39 & NOCNVT & RTS & & Return. \\
\hline
\end{tabular}

\title{
UCSD Pascal* for the INTERTEC SUPERBRNIN \({ }^{(10)}\)
}

Certified by Softech Microsystems
Our package includes:
- operating system
- Z80 assembler
- compiler
- user manual
- screen editor
- Jensen \& Wirth Pascal Reference Manual
- filer
- Bowles' Beginners Guide to UCSD Pascal
- library


Figure 1: Schematic diagram for the lowercase/uppercase hardware interface. This circuit assumes that there is a parallel interface between the keyboard and the microcomputer. All integrated circuits are complementary metal-oxide semiconductor (CMOS) types for low power consumption. IC1 and IC2 are 4-bit comparators. Switch SW1 transfers the keyboard between an uppercase-only mode and a mixed uppercase-and-lowercase mode. These two modes are achieved with SW1 closed and opened, respectively.


Figure 2: ASCII code table. When converting from lowercase to uppercase, by either hardware or software, only hexadecimal codes 61 thru 7A should be changed. The change to uppercase is made by setting bit B5 to 0 or, equivalently, by subtracting hexadecimal 20 from the code. All other codes should be left intact.
flipping a switch. Thus, a program calling for large quantities of both uppercase and lowercase input will be inconvenient to run. But this should be no more trouble than shifting on a regular typewriter. In any case, hardware design should be kept as simple as possible.

The circuit in figure 1 meets these desirable requirements. Once constructed, it is connected between the keyboard and the computer. It will convert the lowercase letters " a " through " \(z\) " into their uppercase equivalents if switch SW1 is closed. If SW1 is open, all codes, whether uppercase or lowercase, are passed directly to the interface. Construction is noncritical, and very little power is needed due to the use of CMOS integrated circuits.

\title{
A BASIC Floppy-Disk Accounting System
}
```

10 1IME(19), I(2,19,11),T$(440),I|$(33)
20T$( 1, 55)=0}\textrm{CASH
30T$( 56,110)= 'FLLANT
4OT$(111.,16%)="FAYAELLES
50T$(166,220)="IT LOANS
60T$(221,275)="SERV. FEES
701$(276.330)=% INUENTOFY
30T$(331,385)="RENT
90T$(386,440)= SUFFFLIES
100 !"HAI ANCE SHEET ACCOUN
UNTS =:= INCOME STATEMENT ACCOUNTS"
120 FOKA=07019\F:=A+20\T1 -A\GOSUR1200\T2=T1\T1=R\GOSUR1200
130 !%F1,A," ",T$(T2-10,T2)," *,%5I,H," *,T&(T1-10,T1)\NEXT
140 INFUT* O TO ENIM OR 1 TO ERASE A FILE P?,A\IFA=OTHENENII
150 GOSUB1OOQ\GOSUH1300\!F%," HASS HEEN ERASEI|\\ENL
1000 INFUY FFILE: ,F゙$\OFEN\&O,F$\RETURN
1100 FCIRA=OTCO19,&EAII#O,R(A)\NEXT
1110 FORA:OTO2\FORF:=0TO1S\FORC:OTO11
11:0 FEAII#0,1 (A,H,C) \NEXI \NEXT\NEXT\CLOSE#O\RETURN
I200 T1:&(T1+1)*11\RE: TUFN
1200 T1:(T1+1)*11\FE TUFN
1300 FOURA=OTO1$\WFITE:\#O,R(A) \NEXT
L.310 FOK゙A =OTG\,FORH=OTO19\FORC=OTO11
1320 WFITE\#O,I (A.E,C)\NEXT\NEXT\NEXT\CLOSE%O\RETURN
REAIIY

```

Listing 1: LIST1, a program designed to display the codes used in the author's floppy disk based accounting system. The program also allows the user to erase all data from a given file name. LIST1 is used in the article example to keep track of the business transactions of the //R Company, a fictitious organization.
```

GALANCE SHEET ACCOUNTS :== INCOME STATEMENT ACCOUNTS

```

```

    CASH =:= 20 SERV. FEES
    SECURITIES }\quad=\quad21 FOYALTIES
    RECEIVAELES =}=~22 ASSETS SOLII
    INUENTORY =}==23\mathrm{ SOFTWARE
    OTHER =:= 24 OTHER SALES
    PLANT == 25 INUENTORY
    MLANT 
    EQUIFMENT := = 27 IIEFRECIAT.
    FAW STOCK =:= 28 OTHER
    OTHEF
    FAYABLES
    TAXS FAY.
    LOANS F'AY.
    OTHEF FAY.
    IIEAENTURES
    LT LOANS
    NOTES
    NOTES 
    OTHEF LT 
    STOCK $1FAR == 38 SALARIES
    K. EAKININGS === 39 OTHER
    TO ENII OR 1 TO ERASE A FILE ? RUL
    INFUT ERROR-RETYFE
    O TO ENL OF 1 TO ERASE A FILE ? 1
FILE: BUI
BUW HAS BEEN ERASEI
READY

```

Listing 2: A sample run of LIST1, showing codes used for the balance sheet accounts and income statement accounts.
```

    Joseph J Roehrig
        JJR Data Research
            POB }7
    Middle Village NY 11379

```

The purpose of this article is to present a complete accounting system for a microprocessor equipped with a floppy disk or another storage device. This article gives complete listings for all programs and focuses on the operation rather than on the design of the system. The programs are written in North Star BASIC on an IMSAI 8080 system with 24 K of programmable memory.

As a model we use a fictitious company (JJR) that used the Micro Accounting System in 1976. During this period the journal entry, balance sheet, budget input and general list programs are introduced. Income statement and budget programs are examined later in the article. The magnitude of the figures used and the number of inputs shown are kept to a minimum for the sake of clarity.

In order to design an accounting system, one must decide how many accounts to handle. The system being presented has 20 balance sheet accounts and 20 income statement accounts. The computer automatically clears out all income statement items to retained earnings. For the 20 balance sheet items, only a year-to-date figure is maintained. However, all income statement items are broken down into three possible departments:
\[
\begin{aligned}
& 0 \text { - Administration } \\
& 1 \text { - Local Sales } \\
& 2 \text { - National Sales }
\end{aligned}
\]

Furthermore, monthly activity is tracked for each income statement item. A file contains only one year's worth of data.

The North Star Microfloppy Disk I used has a capacity of 35 tracks. Each track con-
```

10 HIME(19),I(2,19.11),T$(440),II$(33
LE [HM J(100,4)
20T$(1, 55)="C.ASH
30T$( 56,110)="FLANT
4OT$(111,165)= "FAYAKLES
50T$(166,220)="LT LDOANS
SOT$(221,275)= SERU. FEES
70T$(276,330):= " INUENTORY
(30T$(331,385)=0RENT
90T$(386,440)= 'SUFFLLTEG
100 GOSUB100O\GOSUE1100
140 INFUT MONTH: *,M\M=M-1
150 IFM:OORM I.THEN140
160 !"INPUT: $AMDUNT, LEEIT ACC#, CREIIT ACC#, IUEFT#, REF#*
170 :0,0,0,0,0 ENIIS INFUT \A \}=
LGO !"ENTFYY #",Z4I,A+1,\INFUT" ? ",J(A,O),J(A,1),JI(A,2),J(A,3),J(A,4)
182 IFJ(A,1);390KJ(A,1)COTHEN189
184 IFJ(A,2):39ORJ(A,2)COTHEN189
186 IFJ(A, 3,2ORJ(A,3)OTHEN189\GOT0190
189 ! JNVAL IR ENTFY FE,JECTEII"\GOTO180
190 IFJ(A,O)=OTHEN200\A=A+1\IFA<100THEN180
195 A=A-1
200 A A A - 1
2OS INFIUT'SET FRINTER FOR LIST OF ENTRIES ? ",A$\!":
210 1"JOUFNAL ENTRIES MONTH **,%3I,M+1
220 FORE=1T027\!*= * \NEXT\!**\!"*
230 "ENTRY \$ AMDUNT IIEEIT CRELIIT DEFT REFERENCE"
240 FORF=1T060\!":= \NNEXT\!:"\!"
250 FORE=0TOA\T1:=J(B,1)\GOSUB1200\T2=T1\T1=J(E,2)\GOSUR1200
260 ! Z5I,B+1," ",\#$10F2,J(E,O)," ",T$(T2-10,T2),",",
265 :T$(T1-10,T1);%6I;J(E;3)r%12I,J(E,4)
270 NEXT\C=0
280 10 ENDS FROGRAM ANL KILLS ALI.. ENTKIES*
290 :1-100 CORRECTS AN ENTRY"
300 INFUT OUER 100 ENTERG THE ENTRIES INTO THE FILE ? *,B\IFE=OTHENENI
310 IFR. 100 THEN400\C:=1\I=B-1\GOT0330
320 INFUT1 ENTFYY NUMEER ?', L\\D=D-1
330 IFLCONFIN ATHEN32O
340 LNPUT"$, [EEIT,CRE, IEFT,REF ?",J(D,0),J(D,1),J(D,2),J(E;3),J(I,4)
350 IFJ( D,1)<OOFJ(D,1)>39THEN360
352 IF.J(D,2)<OORJ(D,2)\39THEN360
354 IFJ(D,3)<ODRJ(D,3)\2THEN360\GOT0280
360 ! 'CORRECTION REJECTEI"\GOTO340
400 IFCOOTHEN205
410 FORB=0TOA\E=J(B,Z)\FORC=1TO2\D=J(B;C)\IFC=2THENJ(E;O)=0-J(E;O)
420 IFD)19THEN430\F(II)=F(D)+J(E,O)\GOT0440
430 D=I-20\I(E,[1,M)=I(E,D,M)+J(E,0)\B(19)=E(19)+J(B,0)
440 NEXT\NEXT\GOSUB1000\GOSUB1300\!"=\!F%;" UFDATED"\END
1000 INFUT*FILE * *,F$\IJFEN#O,F$\RETURN
1100 FORA=OTO19\REALTO,F(A)\NEXT
1110 FOFA:=0TO2\FORE=OTO19\FORC=0TO11
1120 REAL\#O,I (A,B,C) \NEXT\NEXT\NEXT\CLOSEFO\RETURN
1200 T1=(T1+1)*11\RETURN
1300 FORA=OTO19\WRITE\#O,F(A)\NEXT
:1310 FORA=0TO2\FORB=0TO19\FORC=0T011
1320 WFITTEFO,I (A,B,C) \NEXT\NEXT\NEXT\CLOSE書O\RETURN
REALIY

```

Listing 3: ENTRY1, a program enabling the user to enter business transactions into the computer.
tains ten sectors or blocks, with 256 bytes of data on each sector. Every numerical variable written out to disks using the standard North Star Basic requires five bytes. Therefore, each data file is subdivided as follows:
\begin{tabular}{ll} 
Balance sheet items \(=\) \\
\(20 \times 5\) bytes \\
Income items \(=\) \\
\(20 \times 3\) subdepartments \(\times\) & \(=100\) \\
12 months \(\times 5\) bytes & \(=3600\) \\
& \(=3700\)
\end{tabular},

The size of a data file is 15 blocks ( 3700 divided by 256 ). Listing 1 shows the first program of the system (LIST1). Listing 2 shows the output of LIST1. This program merely shows the codes (numerical between 0 and 39) used for each account and also allows us to erase all data from a given file name. A 15 block data file is created (using the North Star disk operating system commands: CR JJR76 15, TY JJR76 3) to keep track of the JJR Company for the year 1976. The company was formed in December of 1976 and has very limited transactions. These are entered into the accounting system via program ENTRY1 (shown in listing 3). Listing 4 details the entry of these transactions which is as follows:
1. Start business by purchasing 1000 shares of stock for \(\$ 1000\).
2. Buy \(\$ 500\) worth of machinery for cash.
3. Obtain a \(\$ 250\) piece of equipment for cash.
4. Purchase raw stock for \(\$ 50\).

ENTRY1, like the rest of the system's update programs, always asks for a data file at the beginning and a date file at the end of

```

10 IIIME(2,19),T($(440),Y(1),T(2,6),W(1,4),L(1,16),L(7(77)
15 LINE8O
16 L$(1,44)=* C. ASSETS L. ASSETS C.LIAE. L. LIAE.
18 L$(45.77)=" EQLIITY TOT. ASSETS TOT. LIA&EQ*
2OT(% 1. SS)= 'CASH SECURITIES RECEIUAELESINUENTORY OTHEF
30T(56,110)= PLANT
40T$(111,165)== F'AYABLES
50T (166,220)="LT LDANS
GOT$(221, 275)= SERW. FEES
70T$(276,330)=" INUENTORY
@OT年(331,385):=* RENT
90T$(386.440)= 'SUPFLIES
97 FOF(6;440)='SUFFLIES FUSTAGE
92 FDKA=0TU4\REALW(O,A),W(1,A)\NEXT
96 INFUIT*O TO TRANSFER YEAK TO YEAR ?*,A
98 IFA=OTHENGOSUE4000
100 FOR[#=OTOI\GOSUE100O\GOSUF1100\INPUT"WHAT YEAR WAS THAT ? "Y(D)
110 NEXT
1*O INFUT "DATE ? *, II$\INFUT"GET FRINTER REALIY ? *,A\&
130 !"BALANCE SHEET AS OF ",II\$
132 1%21I;Y(0),%9I,Y(1)," IIFF= = '
134 ! %21I,Y(0),%9I,Y(1)," IIIFF*
136 FOKA:= 1TOB\!"========:===** \NEXT\!."
140 FORA =0TO19\E(2,A):=B(O,A)-B(1,A)\IFA%16THEN145\REAIL (O,A),L(1,A)
145 NEXT
150 FORA=0TO4\C=W(O,A) \I=W(1,A) \FORE=0TO2
160 FORE:=CTO[\T(E;A)=T(E;A)+E(E;E)
170 NEXTE\NEXTE\NEXTA
180 FORA=0TO2\T (A,5)=T(A,0)+T(A,1)
190 T(A,G):=T(A,2)+T(A,3)+T(A,4)\NEXT
200 FORA=0T016\FORE=0TO1
210 IF200%L(E;A)THEN230
220 1• *, \GOTO400
230 IF100%L(E;A) THEN260
240 T3:zL(E,A)-100\T1=T3\GOSUR1200
250 lL(T1-10,T1); % %9F2,T(0,T3),T(1,T3),%日F2,T(2,T3),\G0T0400
260 T3=L(B,A)\T1=T3\GOSUB1200
270 !T$(T1-10,T1)," %%9F2,E(0,T3),G(1,T3),%BF2,F(2,T3),
400 IFB=1THEN410\!" = ",\GOTO420
410 !:
420 NEXT\NEXT\!"*\ENL
1000 INFUT"FILE : ",F%\OFEN*O,F$\RETURN
1100 FORA=OT019\REAII*O,B(IT,A)\NEXT\CLOSE*O\FETURN
1200 T1=(T1+1)*11\RETURN
2000 LATAO,10,1,11,2,12,3,13,4,102,100,200,200,14,200,15,200,16
2002 LATA5,17,6,103,7,200,8,18,9,19,101,104,200,200,105,106
4000 INFUT"GIUE FILE TO EE TRANSFEREN ? ,F\$
4010 OFEN\#O,F\$\FORA=OTO19\REAISO, B (O,A) \NEXT\CLOSE\#O
4020 INFUT"GIVE FILE TO RECEIUE DATA ? "FF

```

```

40:35 CLOSE:O\RETURN
REAIIY

```

Listing 5: BALI, a program that calculates a year end balance sheet. The program is capable of transferring the previous year's records to the current year.

0 TO TRANSFER YEAR TO YEAR ?O GIUE FILE TO RE TRANSFEREI ? JJR 76 GIUE FILE TO RECEIVE DATA? JJRT7 FILE : JJR76 WHAT YEAR WAS THAT ? 1976 FILE: JJR76
WHAT YEAR WAS THAT ? 1976
IATE ? 12/31/76
GET FRINTER READY?
the program. This makes it possbile to save the original file and to produce a new file, which is the original plus any updates. In the example, only one file (JJR76) is used.

Since the transactions shown were the only transactions for the year, it is now possible to run a year end balance sheet. Program BAL1 (listing 5) is executed. Listing 6 shows a sample run of the program. BAL1 first asks if any of the balance sheet items are to be transferred to a new file. This is important because all of 1976's year-end assets, liabilities and equity balances must be transferred to the new year, 1977. Therefore, the user should instruct the program to transfer 1976 balance sheet items (file JJR76) to 1977 (file JJR77).

The balance sheet program also allows for comparisons to be made and asks for two files to be compared. Since this is JJR's first year of operation, we are forced to compare 1976 to 1976. The balance sheet is now produced.

Note that the balance sheet is printed by lines 200 to 420 of the program. A programming trick has been used to shorten the length of the actual program. As the example shows, the balance sheet is composed of 17 lines with two entries per line, or 34 total entries. There are 20 individual items, seven totals and seven blank items. Array \(L(1,16)\) determines which items appear on each line. An \(L(1,16)\) value of 0 to 19 refers to a particular account, 100 to 106 is linked to a total, and 200 is used to generate blanks. Lines 2000 and 2002 show the values of \(L(1,16)\). I point this out because most of the financial statements were produced using this method.

During 1977 our small business has expanded by hiring a local salesperson. However, sales do not take place until November, and our proprietor wants to segregate the revenue generated by himself from the sales brought in by the sales-
\begin{tabular}{|c|c|c|c|c|c|c|c|}
\hline \multicolumn{8}{|l|}{GALANCE SHEET AS OF 12/31/76} \\
\hline & 1976 & 1976 & HIFF = & \(=\) & 1976 & 1976 & DIFF \\
\hline CASH & 250.00 & 250.00 & \[
.00
\] & \[
=\text { PAYABLES }
\] & \[
.00
\] & . 00 & . 00 \\
\hline SECURITIES & .00 & . 00 & \(.00=\) & \(=\) TAXS FAY. & . 00 & . 00 & . 00 \\
\hline RECEIVABLES & . 00 & .00 & . \(00=\) & \(=\) LOANS PAY. & . 00 & . 00 & .00 \\
\hline INUENTORY & . 00 & . 00 & . \(00=\) & \(=\) OTHER F'AY. & . 00 & . 00 & . 00 \\
\hline OTHER' & . 00 & . 00 & . \(00=\) & \(=C . L I A B\). & .00 & . 00 & . 00 \\
\hline \multirow[t]{4}{*}{C. ASSETS} & \multirow[t]{4}{*}{250.00} & \multirow[t]{4}{*}{250.00} & \multirow[t]{4}{*}{. 00} & \(=\) & & & \\
\hline & & & & \(=\) DERENTURES & . 00 & . 00 & . 00 \\
\hline & & & & \(=\) LT LOANS & . 00 & . 00 & . 00 \\
\hline & & & & = NOTES & . 00 & . 00 & . 00 \\
\hline PLANT & . 00 & . 00 & \(.00=\) & = OTHER LT & . 00 & . 00 & . 00 \\
\hline MACHI NERY & 500.00 & 500.00 & . 00 & \(=L . L I A B\). & . 00 & .00 & .00 \\
\hline EQUIPMENT & 200.00 & 200.00 & . 00 & \(=\) & & & \\
\hline RAW STOCK & 50.00 & 50.00 & . 00 & = STOCK 1FAR & -1000.00 & -1000.00 & . 00 \\
\hline OTHER & . 00 & . 00 & \(.00=\) & = R. EARNINGS & . 00 & . 00 & . 00 \\
\hline L. ASSETS & 750.00 & 750.00 & . 00 & \(=\) ERUITY & -1000.00 & \(-1000.00\) & +00 \\
\hline TOT. ASSETS & 1000.00 & 1000.00 & \(.00=\) & \(=\) TOT. LIAgE & -1000.00 & \(-1000.00\) & .00 \\
\hline
\end{tabular}

READY
```

10 nIME(2.19,11)
100 "USE FURGET FILES ONLY ? *,\GOSUH1000\GOSUF1100
L10 : O TO AIIE TO EXISTING BU|GETS*
120 :1 TO OUER RILE EXISTING FUIGETS
130 INFIJT"2 TO ENI ? *,A\IFA`THEN140\GOSUB1000\GOSUF1300\ENI
140 INFUT" IEFT,FIRST MONTH, ENII MONTH ? ",B,C,I
142 IFEOOOFFB2THEN3OO
l42 IFHOOOFB-2TH
14S 1FF[10RD$12THEN300
144 IFC 10FCC 12THEN3OO
L5O 1NF.JT"ACCOUNT, AMOUNT ($.01 FETURNS TO START) ? *,E,F
155 EmE-20
160 IFF=.01THEN110\IFECOORE\19THEN30O
170 FIIRG:CCTON\IFA=OTHEN190
.180 E(E,E,G-1)=F\GOTO200
190 E(E,EE,G-1)={+B(E,E,G-1)
2OO NEXT\GOTOL50
$00 1"LAST ENTRY INCORRECT"\GOTO1 10
1000 INNFUT"FILE: - FF$\OFEN\&OPF\$\RETURN
I100 F OFA=OTO1%\READ\#O.F\NEXT
1110 FORA=0TO2\FORB=OTO19\FORC=OTO11
1120 FEALI*O,H(A,E,Cl)\NEXT\NEXT\NEXT\CLOSE\#O\RETURN
1300 FOKA=OTO19\WRITE\#O.Z9\NEXT
1300 FORA=OTO19\WRITE%O,Z9\NEXT
1320 WRITE\#O,H(A,B,C)\NEXT\NEXT\NEXT\CLOSE%O\RETUKN
REAEIY

```

Listing 7: BUD-IN1, a program that generates budgets and enables the user to keep separate records of, for instance, the sales generated by each salesperson in the organization.
```

```
FIL.E : J.JR77
```

```
FIL.E : J.JR77
MONTH: 11
MONTH: 11
INFUT: $AMOUNT, IIEBIT ACC*, CREIIT ACC*, DEFT殔, REFF
INFUT: $AMOUNT, IIEBIT ACC*, CREIIT ACC*, DEFT殔, REFF
0,0,0,0:0 ENLIS INFUT
0,0,0,0:0 ENLIS INFUT
ENTKY & 
ENTKY & 
ENTKY 2 ? 0,0,0,0,0
ENTKY 2 ? 0,0,0,0,0
SET FRINTER FOR LIST OF ENTRIES ?
SET FRINTER FOR LIST OF ENTRIES ?
,JOURNAL ENTRIES MONTH * 11
,JOURNAL ENTRIES MONTH * 11
*)
```

```
*)
```

```


```

NTKYY 2 ? 0,0,0,0,0,5

```
```

NTKYY 2 ? 0,0,0,0,0,5

```


```

JOURNAL ENTRIES MONTH * }1

```
JOURNAL ENTRIES MONTH * }1
```

JOURNAL ENTRIES MONTH * }1
ENTFIY * AMOUNT IIEBIT CRENITT DEPT REFERENCE
ENTFIY * AMOUNT IIEBIT CRENITT DEPT REFERENCE
ENTFIY * AMOUNT IIEBIT CRENITT DEPT REFERENCE
100.00 DEPRECIAT. MACHINERY
100.00 DEPRECIAT. MACHINERY
100.00 DEPRECIAT. MACHINERY
\$150.00 RENT PAYABLES
\$150.00 RENT PAYABLES
\$150.00 RENT PAYABLES
\$200.00 SECURITIES SOFTWARE
\$200.00 SECURITIES SOFTWARE
\$200.00 SECURITIES SOFTWARE
\$50.00 INUENTORY RAW STOCK
\$50.00 INUENTORY RAW STOCK
\$50.00 INUENTORY RAW STOCK
\$100.00 SALARIES SECURITIES
\$100.00 SALARIES SECURITIES
\$100.00 SALARIES SECURITIES
ENDS FFKOGRAM ANO KILLLS ALL ENTRIES
ENDS FFKOGRAM ANO KILLLS ALL ENTRIES
ENDS FFKOGRAM ANO KILLLS ALL ENTRIES
1-100 CORRECTS AN ENTRY
1-100 CORRECTS AN ENTRY
1-100 CORRECTS AN ENTRY
GUER 100 ENTERS THE ENTRIES INTO THE FILE ? }11

```
GUER 100 ENTERS THE ENTRIES INTO THE FILE ? }11
```

GUER 100 ENTERS THE ENTRIES INTO THE FILE ? }11

```

Listing 9: Updated accountling sheet of the company's activities for November and December 1977, generated by ENTR Y1.

LOAD BUD-INI
READY
RUN
USE RUDGET FILES ONLY ? FILE : BUI
- TO ADD TO EXISTING BULIGETS

1 TO OUER RIDE EXISTING RUNGETS
2 TO END ? 1
UEFT,FIRST MONTH, ENII MONTH ? \(1,11,12\)
ACCOUNT, AMOUNT ( \(\$ .01\) RETURNS TO START) ? 23,-90
ACCOUNT, AMOUNT ( \(\$ .01\) RETURNS TO START) ? 25,15
ACCOUNT, AMOUNT ( \(\$ .01\) RETURNS TO START) ? 38,40
ACCOUNT, AMOUNT ( \(\$ .01\) RETURNS TO START) ? 0,. 01
0 TO ADD TO EXISTING RUDGETS
1 TO OVER RIDE EXISTING BUNGETS
2 TO END? 2
FILE: BUI
REAIY
Listing 8: A sample run of BUD-IN1.
person. Therefore, the salesperson's activities are placed in department 1: local sales. Listing 7 shows the budget input program BUD-IN1 (see also listing 8).

The budgets are coded like the journal entries and the file containing budget information is identical to the other actual data files, JJR76 and JJR77. For ease of entry, there are two options for entering budget data. One option allows us to add incremental amounts to existing budgets; the other allows for the entry of brand new absolute budget amounts. The amounts entered can be for one or more months. In our sample, the local sales department will be assigned specific budgets for:
1. \(\$ 90\) of software sales in November and December.
2. Inventory usage of \(\$ 15\) for both months.
3. November and December salary costs of \(\$ 40\).

These figures are entered into file BUD. The system, by asking for both read and write files, allows you to save as many versions of a budget as you desire. That ends the 1976 transaction.

No activity took place in our small business between January 1977 and October 1977. However, in November the following item is entered via the ENTRY1 program:
1. The proprietor receives \(\$ 500\) in cash for royalties.

This, as well as December's activity, is shown in listing 9. During December, the following journal entries are made for administration, department 0:
1. Depreciation of \(\$ 100\) is booked.
2. A rent liability of \(\$ 150\) is incurred.

The salesman's department 1 has the following activity:
3. \(\$ 200\) in software is sold for securities.
4. The software was written on \(\$ 50\) worth of raw stock.
```

O TO TRANSFER YEAR TO YEAR ?1
FILE : JJR77
WHAT YEAR WAS THAT ? }197
FILE : JJR76
WHAT YEAR WAS THAT ? }197
DATE ? 12/31/77
GET FRINTER READY ?

```
\begin{tabular}{|c|c|c|c|c|c|c|c|}
\hline BALANCE SHE & \[
\text { AS OF } 1: 12
\] & \[
\begin{aligned}
& 31 / 77 \\
& 1976
\end{aligned}
\] & DIFF = & & 1977 & 1976 & DIFF \\
\hline CASH & 250.00 & 250.00 & . \(00=\) & FAYABLES & \(-150.00\) & . 00 & -150.00 \\
\hline GECURITIES & 100.00 & . 00 & \(100.00=\) & TAXS PAY. & . 00 & .00 & . 00 \\
\hline RECEI VABLES & 500.00 & . 00 & \(500.00=\) & LOANS PAY. & . 00 & .00 & . 00 \\
\hline INUENTORY & . 00 & . 00 & \(.00=\) & OTHER PAY. & .00 & .00 & .00 \\
\hline OTHER & . 00 & . 00 & \(.00=\) & C. LIAB. & -150.00 & .00 & \(-150.00\) \\
\hline C. ASSETS & 850.00 & 250.00 & 600.00 \(=\) & & & & \\
\hline & & & \(=\) & IUEBENTURES & .00 & .00 & .00 \\
\hline & & & \(=\) & LT LOANS & .00 & .00 & . 00 \\
\hline & & & \(=\) & NOTES & . 00 & . 00 & . 00 \\
\hline PLANT & . 00 & . 00 & . \(00=\) & OTHEF LT & . 00 & . 00 & . 00 \\
\hline MACHINERY & 400.00 & 500.00 & \(-100.00=\) & L. LIAE. & . 00 & . 00 & . 00 \\
\hline EQUIPMENT & 200.00 & 200.00 & . \(00=\) & & & & \\
\hline RAW STOCK & . 00 & 50.00 & \(-50.00=\) & STOCK \$1FAR & \(-1000.00\) & -1000.00 & . 00 \\
\hline OTHER & . 00 & . 00 & . \(00=\) & F. EARNINGS & -300.00 & . 00 & -300.00 \\
\hline L. ASSETS & 600.00 & 750.00 & \(-150.00=\) & EQUITY & -1300.00 & -1000.00 & \(-300.00\) \\
\hline TOT. ASSETS & 1450.00 & 1000.00 & \(450.00=\) & TOT. LIARE & \(-1450.00\) & -1000.00 & \(-450.00\) \\
\hline
\end{tabular}
REALY

\section*{Listing 10: Year end balance sheet for the \(/ / R\) company and a comparison with the previous year.}

\(12[1 \%(1,33)=\) "ADMINIST. LOCAL SALESNAT. SALES •
15 LINE 132
2OT\$( 1,55\()=\) 'CASH SECURITIES RECEIVABLESINUENTORY OTHER

30T \(\$(56,110)={ }^{\circ}\) PLANT
40T*(111,165) = *FAYAFLES
50T ( \((166\) :220) \(=\) "LT LUANS
\(60 T \$(221,275)={ }^{\bullet}\) SERU. FEES
70T \(\$(276,330)=\) - INUENTORY
日0T \(\$(331,385)=\) - RENT
90T \(\$(386,440)=\) SUFFLIES
100 GOSUE 1000 \GOSUR 1100
120 FORA: OTO2\REALIW \((0, A), W(1, A) \backslash N E X T\)
124 ПАТАО,4,5,9,10,19
\(1260 \$(1,44)=\) TOTAL SALESCOST DF GS OTHER EXP. -PROF./LOSS*
130 FORA: OTO3
132 !"INCOME STATEMENT " IFA=3THEN138

138 ! "TOTAL OF ALL LIEFARTMENTS"

150 !" TOTAL" \GOSUH1500
170 FORB \(=0\) TO2 \(\backslash C=W(0, F) \backslash D=W(1, F) \backslash F O R E=C T O D\)
180 T1=E+20 \GOSUR1200\!T* (T1-10,T1);" ",
190 FORF゙:=0TO11\! \(18 \mathrm{BF} 2, I(A, E, F), \backslash I(A, E, 12)=I(A, E, 12)+I(A, E, F)\)
195 IFA \(=3\) THEN2OS
\(200 \mathrm{I}(3, E, F)=I(3, E, F)+I(A, E, F)\)
\(205 \mathrm{~T}(A, B, F)=T(A, B, F)+I(A, E, F)\)
210 NEXTF \! \%9F2, I (APE, 12) \NEXTE\T1=B\GOSUB1200\GOSUB 1500
215 ! 0\$(T1-10;T1)." *
220 FORF: \(=0\) TO11 \izBF2,T(A,B,F), \(T(A, B, 12)=T(A, B, 12)+T(A, B, F) \backslash N E X T F\)
230 ! \% 9F2,T(A, \(\mathrm{F}, 12\) ) \(!\) !
235 FORF=OTO12 \(\mathrm{T}(A, 3, F)=T(A, 3, F)+T(A, B, F) \backslash N E X T\)
238 NEXTB\! :
240 ! \(0 \leqslant(34,44)\), " \(\quad, ~ \ F O R F=0 T 011\)
245 ! ZBF2,T(A;3,F), \NEXT\!Z9F2,T(A,3,12)
247 FORF=1T033\!" *NEXT
250 NEXTA\ENII
1000 INFUT \({ }^{\circ}\) FILE : , F \(\$ \backslash O F E N * O, F \$ \backslash R E T U R N\)
1100 FORA \(=0\) TO19 \READ 10 , \(\mathrm{B}(\mathrm{A}) \backslash \mathrm{NEXT}\)
1110 FORA \(=0\) TO2 1 FORB=0TO19 1 FORC=0T011

\(1200 \mathrm{~T} 1=(\mathrm{T} 1+1) * 11\) \RETURN

READY
Listing 11: INCOME1, a program designed to show assets and liabilities for any or all company departments over a 1 year period.
5. An invalid account number 200 is disallowed by the program.
6. \(\$ 100\) of securities is paid to the salesperson as salary.

Listing 9 shows an update of the company's activities for 1977. In listing 10 the year end 1977 balance sheet is run and compared to year end 1976. Program INCOME 1 (listing 11) is loaded and run. Listing 12a shows the administration account, listing \(12 b\) the local sales department, listing \(12 c\) is the consolidation of the three accounts (national sales, unused account in these examples, was not shown). This program requires as input only the data file's name.

Listings 14 a and 14 b show the budget program (BUD1) in action (see also listing 13). Since the file structure remains the same throughout, you can compare any quantities you like, and since all 12 months are stored on disk, any month can be printed. Like the 12 month income statement, all three departments and a summary can be produced.

The inputs for this program are:
1. MONTH: the particular month of the report.
2. ACT File: file name for the current data.
3. BUD File: file name for a budget or prior year's results that you want to compare to the current year's.
4. L.Y. File: last year's file name or any other file.
5. 0,0 for department 0 .

1,1 for department 1 .
2,2 for department 2.
0,1 for departments 0 and 1 .
1,2 for departments 1 and 2.
0,2 for departments 0,1 and 2 .
0,3 for all departments and a summary.

\section*{About the Author}

Joseph / Roehrig is currently manager of budgets, operations and engineering for the NBC Television Network. He was previously in charge of television network systems at NBC, during which time he worked with hardware configurations. Mr Roehrig is also president of JJR Data Research, a computer software service.

FILE : JJF77
INCOME STATEMENT
ADMINIST. IEFARTMEN'T
\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|c|c|c|}
\hline [TEM & MON- 1 & MON- 2 & MON - 3 & MON- 4 & MON- 5 & MON- 6 & MON- 7 & MON- 8 & MON- 9 & MON-10 & MON-11 & MON-12 & TOTAL \\
\hline SERU. FEES & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline ROYALTIES & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & -500,00 & . 00 & -500.00 \\
\hline ASSETS SOLII & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline SOFTWARE & .00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline OTHER SALES & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline toral. Sales & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & -500.00 & . 00 & -500.00 \\
\hline INUENTOFY & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline ASSETS SOLI & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline DEFRECIAT. & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & 100.00 & 100.00 \\
\hline OTHEF & . 00 & . 00 & +00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline OTHEF: & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline cost of es & & & & & & & & & & & & & \\
\hline COST OF GS & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & 100.00 & 100.00 \\
\hline RENT & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & 150.00 & 150.00 \\
\hline ELECTRIC & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline GAS & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline TELEFHONE & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline FUELICATION & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline GUFFLIES & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline Postage & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline TRANSFPET. & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline Galafies & . 00. & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline (JTHEF) & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline @THER EXF', & +00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & 150.00 & 150.00 \\
\hline --FROF./LOSS & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & \(-500.00\) & 250.00 & -250.00 \\
\hline
\end{tabular}

Listing 12a: An example of a typicai INCOME run, showing the yearly record for the administrative department of the \(/ / R\) company for 1977.


Listing 12b: An INCOME run for the //R company's local sales department for 1977.
\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|c|c|c|}
\hline \multicolumn{14}{|l|}{INCOME GTATEMENT} \\
\hline \multicolumn{14}{|l|}{TOTAL OF ALL LEFARTMENTS} \\
\hline ITEM & MON- 1 & MON- 2 & MON - 3 & MON- 4 & MDN- 5 & MON- 6 & MON- 7 & MON- 8 & MON- 9 & MON-10 & MON-11 & MON-12 & TDTAL \\
\hline \multicolumn{14}{|l|}{} \\
\hline GERU. FEES & .00 & . 00 & .00 & .00 & . 00 & .00 & .00 & .00 & .00 & . 00 & .00 & . 00 & . 00 \\
\hline ROYALTIES & .00 & .00 & .00 & . 00 & . 00 & . 00 & . 00 & .00 & . 00 & . 00 & \(-500.00\) & . 00 & \(-500.00\) \\
\hline ASSETS SOLI & . 00 & .00 & .00 & .00 & .00 & . 00 & . 00 & .00 & . 00 & . 00 & 400 & . 00 & . 00 \\
\hline SOF TWARE & .00 & .00 & . 00 & .00 & .00 & .00 & .00 & .00 & . 00 & .00 & +00 & \(-200.00\) & \(-200.00\) \\
\hline OTHEF SALES & . 00 & .00 & .00 & . 00 & . 00 & .00 & . 00 & . 00 & .00 & . 00 & . 00 & . 00 & . 00 \\
\hline \multicolumn{14}{|l|}{} \\
\hline TOTAL SALES & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & -500.00 & -200,00 & 700.00 \\
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\] \\
\hline OTHER & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & .00 & .00 & . 00 & . 00 & . 00 & .00 & . 00 \\
\hline COST OF GG & . 00 & . 00 & .00 & . 00 & .00 & . 00 & . 00 & . 00 & . 00 & .00 & . 00 & 150.00 & 150.00 \\
\hline RENT & .00 & . 00 & .00 & .00 & .00 & .00 & . 00 & .00 & . 00 & . 00 & . 00 & 150.00 & 150.00 \\
\hline ELECTRIC & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & +00 & . 00 & .00 \\
\hline GAS & .00 & . 00 & . 00 & . 00 & .00 & .00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline TELEFHONE & . 00 & .00 & .00 & . 00 & .00 & . 00 & . 00 & . 00 & . 00 & . 00 & .00 & . 00 & . 00 \\
\hline PUBLICATION & . 00 & . 00 & . 00 & .00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & +00 & . 00 & . 00 \\
\hline SUFFLIES & . 00 & . 00 & . 00 & .00 & .00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 \\
\hline POSTAGE & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & +00 & . 00 & . 00 \\
\hline TRANSFORT. & . 00 & .00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & .00 & .00 \\
\hline SALARIES & . 00 & . 00 & . 00 & .00 & . 00 & . 00 & . 00 & .00 & . 00 & . 00 & .00 & 100.00 & 100.00 \\
\hline OTHER & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & . 00 & .00 & . 00 & .00 & . 00 & . 00 & . 00 \\
\hline OTHEF EXF. & . 00 & . 00 & .00 & . 00 & . 00 & . 00 & .00 & . 00 & . 00 & . 00 & . 00 & 250.00 & 250.00 \\
\hline -FROF ./LOSS & .00 & . 00 & .00 & .00 & .00 & .00 & . 00 & .00 & .00 & .00 & \(-500.00\) & 200.00 & \(-300.00\) \\
\hline
\end{tabular}

Listing 12c: An INCOME run for all departments for the //R company in 1977.

12 [IB (1,33) = 'ALMINIST. LOC.AL SAL.ESNAT. SALES:
15 LINEEO
17 I: = "ACTHUH - Y
SOT* ( \(1, S 5\) ) = SERU. FEES ROYALTIES ASSETS SOLISDFTHARE DTHER SALES* 70TB(56.110) = 'INUENTOFY ASSEIS SOLILIEFRECIAT. OTHER OTHER ** GOT* 111.165 ) = "RENT ELECTRIC GAS TELEFHONE FUELICATION: وOTE (166,220)= SUFFLIES FOSTAGE TKANSFOKT. SALAFIES OTHER 92 INFUT-MONTH \(\rightarrow\)., M\M=M-1

95 GOSJH 1000 \GOSUH 1 100 \A-A1

\(9 B I(E, C, E)=1(B, C, E)+F(E+C, I)\)
100 IF \(A=D T H E N I(B, C, A)=K(E, C, D)\)
100 NEX=DTHENI(B,C,A)=F(E,C,D
102 NEXTITNEXTCTNEXTE\NEXIAI
108 FURA OTOASTEF4 \FOFR=0TO2\FORC \(=0\) T019
\(10 \mathrm{~B} I(E, C ; A)=I(E, C, A+2)-I(E, C, A+1)\)
110 NEXTCTNEXIETNEXTA

120 FORA \(=0102 \backslash R E A D H(O, A), H(1, A) \backslash N E X T\)
124 INATAO:A.5.9,10,19
126 O\& \((1,44)={ }^{\circ}\) TOTAL SALESCOST OF GS OTHER EXP. -PROF.ALOSS"
130 FORA=A1TOA2


138 'TTOTAL OF ALL DEPARTMENTS"

\begin{tabular}{|c|c|c|}
\hline 141 & 'TAB(55)."YEAR TO DATE" & \\
\hline 142 &  & ' \({ }^{\prime}\) \\
\hline 143 &  & \\
\hline 144 & 1'ITEMS UAR, ACT. BUD L.Y. & UAR. \({ }^{\text {, }}\) \\
\hline 146 & ! ACT. BUD. L.Y. \({ }^{\text {c }}\) (GOSUB1500 & \\
\hline 170 & \(F O R E=0 T O 2 \backslash C=H(0, B) \backslash D=W(1, B) \backslash F O R E=C T O D\) & \\
\hline 180 &  & \\
\hline 190 &  & \\
\hline 195 & IFA=3 THEN205 & \\
\hline 200 & \(\mathrm{I}(3, E, F)=1(3, E, F)+I(A, E, F)\) & \\
\hline 205 & \(T(A, B, F)=T(A, B \geqslant F)+1(A, E, F)\) & \\
\hline 210 &  & \\
\hline 215 & 108(T1-10,T1), ", & \\
\hline 220 &  & \\
\hline 230 &  & \\
\hline 238 & NEXTH\1'* & \\
\hline 240 &  & \\
\hline 245 &  & \\
\hline 247 & FORF=1TO33\1* *NEXT & \\
\hline 250 & NEXTA\END & \\
\hline 1000 & INFUT'FILE : *FS\OPEN*O,FS\RETURN & \\
\hline 1100 &  & \\
\hline 1110 &  & \\
\hline 1120 & KEAD*O,R(A,B,C) \NEXT \NEXT TNEXT\CLOSE@O\RETURN & \\
\hline 1200 & T \(1 \times(T 1+1)\) + 11 \RE TURN & \\
\hline 1500 & FORZ=1TOB0 \! " \(=\) " \NEXT\! - \RETURN & \\
\hline & & \\
\hline
\end{tabular}

Listing 13: BUD1, a program designed to give a more detailed picture of individual departments' performance than is found in the INCOME program (see listing 11).

\section*{COLOR SOFTWARE}

\section*{Unless otherwise noted all programs are \(\$ 15\) each, for Apple II, Atari 16K, TI 99/4}

UNITS: Practice converting yards feetinches, pounds-ounces, metric units, etc.
FRACTIONS: Practice adding, subtracting, multiplying and comparing fractions.

NUCLEAR REACTOR: Realistic dynamic model of nuclear power plant in operation.

3-D STARTREK: Discover new planets, fight Klingons in 3 -dimensional galaxy.
MAJOR LEAGUE BASEBALL: Manage Major League teams and make all lineup, batting, pitching and running decisions. \$25. Apple II with 48 K , Applesoft ROM and one disk.

ROADRACE: Race around 2.25 mile course. 1 or 2 players. Not for TI 99/4.
BLACKJACK: Popular card game for 1 to 3 players. Not for Apple II.


Listing 14a: A sample run of BUD1, showing a breakdown of activities for November 1977. ACT stands for actual, BUD for budgeted amounts, L.Y. for last year, and VAR for variance. VAR indicates the difference between the budgeted amount and the actual amount taken in or paid out. L.Y. indicates the amounts for the previous November and is included for reference only.


Table 1: Table of contents for the floppy disk showing the locations of all programs used in this accounting system.

Listing 14a shows the November results for local sales, and listing 14b shows the December results. A listing of the table of contents for the disk containing all of the accounting information is shown in listing 15. The data shown consists of file name, starting block, size in blocks and type ( \(2=\) program and 3 = data).

The file structure described earlier is fairly simple. Therefore, it is easy to add more programs to the system. The programs can calculate salaries, depreciation and accounts receivable, and enter this information directly into the data files. The account titles used in the programs are generally found in lines 20 to 90 and can be modified for other usages. The number of accounts can be easily expanded within the current 24 K programmable memory space by limiting the income statement subdivisions or by eliminating the monthly history. Quarterly type reports can also be added.

If you plan to enter these programs into your system, start with program LIST1. Most of the other programs can be formed by editing this particular program.


Listing 14b: A similar breakdown for December 1977.

\title{
Whestis Now?
}

\section*{Disk Controller from Shugart}

A microprocessor-based disk controller with on-board data separator logic capable of controlling up to four Winchester hard- or floppy-disk drives is available from Shugart, 435 Oakmead Pky, Sunnyvale CA 94086, (408) 733-0100. The SA1400 features automatic copying of disks, sector interleaving, error correction code autonomous to the microprocessor, and optional microdiagnostics. Data transfer between the controller and the host microprocessor is improved by sector buffering. The SA1400 is based on a bitslice microprocessor and works with Shugart SA1000 8-inch and SA4000 14 -inch Winchester drives and SA800/850 8-inch floppy-disk drives. Other functions include overlapped seek operations, integral data separators, automatic switching of head and cylinder, and optional track formats. Write precompensation is also included on the board. The Shugart standard floppy-disk protocol and either of the SA1000 or SA4000 fixed-disk protocols are used for the interface to the drive. A general-purpose interface is used to transfer commands and data between the host processor and the controller. In original equipment manufacturer's quantities, the SA1400 is \(\$ 1125\).
Circle 539 on inquiry card.


\section*{Ectype Floppy Disks from Syncom}

The Ectype 8- and 5-inch floppy disks have a wear life exceeding 10 million passes for both hard- and soft-sector operations. The disks are \(100 \%\) certified, and are made for IBM and nonIBM equipment with other formats available. Syncom also manufactures Ectype MC/ST magnetic cards and Ectype 3348-70 Data Modules. For more information, contact Bozell \& Jacobs Public Relations, Butler Sq, 100 N 6th St, Minneapolis MN 55403, (612) 371-5500.
Circle 541 on inquiry card.


\section*{Drum-Type Graphics Plotter}

Strobe Inc has introduced a drum-type graphics plotter with a 0.004 -inch step size, and a 21.6 by 28 cm ( 8.5 by 11 in ) paper capacity. The interactve digitizing mode allows the user to enter directly into the host computer \(X, Y\) coordinate data corresponding to pen location. The Model 100 plotter is controlled by the computer through two parallel output ports and one parallel input port. Hardware interfaces and software drivers are
available for the Apple II, TRS-80, PET, and S-100 machines. An optional plot software package, providing vector generation and alphanumerics, that runs with most versions of BASIC and FORTRAN is also available. The price of the Model 100 plotter is \(\$ 680\). For details, contact Strobe Inc, 897-5A Independence Ave, Mountain View CA 94043, (415) 969-5130.
Circle 540 on inquiry cara.

\section*{DC 100A Tape Cartridge Drive}

The Moya Corporation, located at 6311 DeSoto Ave, Unit H, Woodland Hills CA 91367, (213) 533-5993, has introduced the MicroDrive/OEM series of tape drives which offer up to 1.344 megabytes of storage in a package that measures 467 cubic cm ( 28.5 cubic inches). The transport is available with the mechanism-only board or the minimum-electronics board. Both models include a maximum data capacity of 1.344
megabytes, a transfer rate of 48 K bytes per second, read/write speed of 30 ips (inches per second), and search/rewind speed of 90 ips . The mechanism-only board contains the circuitry required to interface the transport mechanism. The minimum-electronics board provides a switching power amplifier to drive the motor, a digital interface on control and status lines, a write amplifier, and a read preamplifier. The units are \(\$ 99\) in original equipment manufacturer's (OEM) quantities.
Circle 542 on inquiry card.

\section*{Where Do New Products Items Come From?}

The information printed in the new products pages of BYTE is obtained from "new product" or "press release" copy sent by the promoters of new products. If in our judgement the information might be of interest to the personal computing experimenters and homebrewers who read BYTE, we print it in some form. We openly solicit releases and photos from manufacturers and suppliers to this marketplace. The information is printed more or less as a first in first out queue, subject to occasional priority modifications. While we would not knowingly print untrue or inaccurate data, or data from unreliable companies, our capacity to evaluate the products and companies appearing in the "What's New?" feature is necessarily limited. We therefore cannot be responsible for product quality or company performance.

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\section*{Vector Graphic's MP Printer}

The Vector Graphic MP is a 5 -by-7 dot-matrix, software-driven printer that can print at a speed of 150 cps (characters per second). The price of the

MP is under \(\$ 1000\) from Vector Graphic Inc, 31364 Via Colinas, Westlake Village CA 91361, (213) 991-2302.
Circle 543 on inquiry card.


\section*{Direct-Connect Modem for the TRS-80}

Emtrol Systems Inc, 1262 Loop Rd, Lancaster PA 17604, (717) 392-2105, has introduced Lynx, a direct-connect telephone modem for the TRS-80. Lynx connects with the TRS-80 keyboard and the telephone line-no acoustic coupler is used. It includes originate and answer
capability, and is programmable for word length, parity, number of stop bits and full- or half-duplex. The minimum requirements are a TRS-80 Level I or II with at least 4 K bytes of programmable memory. The Lynx is priced at \(\$ 239.95\). Circle 544 on inquiry card.

\section*{Coosol's Printer Kits}

Coosol has announced the availability of its 40 -column friction-feed and 80 -column tractor-feed dot-matrix impact printers in kit or assembled-andtested forms. The units are microprocessor-controlled and programmable with thirty-two system-level software commands. They feature graphics dot-plotting mode, ninety-six ASCII (American Standard Code for Information Interchange) characters with uppercase and lowercase, nine softwareselectable sizes, reverse-font printing capability, parallel and serial interfaces, data rates from 110 to 9600 bps (bits per second), and adjustable tractor width for paper size selection. Prices for kits are \(\$ 295\) for the 40 -column and \(\$ 455\) for the 80 -column printer. Assembled and tested impact printers are \(\$ 325\) for the 40 -column and \(\$ 485\) for the 80 -column, both without enclosures. For further information, contact Coosol Inc, 1585-200 Adams Ave, Costa Mesa CA 92626 , (714) 545-2216.

Circle 545 on inquiry card.

\section*{Music Synthesizer for the H-8 from Heath}

The Heath Company has introduced a music synthesizer system for the H-8 computer. The HA-8-2 music synthesizer system includes a circuit board and software. The software allows the user to enter any song into the system from conventional sheet music. The synthesizer board, which connects to any stereo system with two shielded cables, produces a 27.5 to 6600 Hz frequency response with up to nine harmonics. An \(\mathrm{H}-8\) with at least 24 K bytes of memory. a floppy-disk drive, and video terminal are required. The HA-8-2 is priced at \(\$ 159\) from Heath Company, Benton Harbor MI 49022, (616) 982-3210. Circle 546 on inquiry card.

\section*{Storage Control Unit for the TI990 Bus}

The ISC 4000 supports up to four 14- or 29-megabyte Shugart Winchester disk drives. The unit will also support floppy-disk or high-density tape backup devices. Compatibility with Texas Instruments' T1990 software is maintained by emulating existing TILINE bus devices. A complete 29 -megabyte system, including a floppy disk, sells for \$7000 from Data Management Labs, 2148 Bering Dr, San Jose CA 95131, (408) 946-9424.

Circle 547 on inquiry card.

\title{
Whats New?
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\section*{Speech Recognition Unit}

The Heuristics 7000 speech recognition unit, which sells for approximately \(\$ 3000\), will interface with all RS-232 terminals. The 7000 enables users to enter information into their computers directly and with few errors. By eliminating the need for hand entry, busy businesspeople and the handicapped will benefit. The unit can recognize up to sixty-four words or phrases, each up to

3 seconds in length, and it is compatible with all common programming languages. It enables computers to take keyboard or voice input, or both simultaneously. The 7000 comes with a noise-cancelling headset microphone. Contact Heuristics, 1285 Hammerwood Ave, Sunnyvale CA 94086, (408) 734-8532.

Circle 548 on ínquiry card.

\section*{Interactive Video}


The Cavri III computer/video player integrator enables users to index and later access videotape frames or segments or to interact with videotaped materials. In addition to integrating computer-aided instruction with
videotape, the system is useful for
storage and retrieval of text and audiovisual information. The system also allows a user to control all remote functions of the video machine from the computer keyboard or from within a program. Access time to a desired point on a video cassette is less than 5 seconds. The average time required to find randomly distributed segments of tape on a 30 -minute cassette is about 45 seconds. Search accuracy is \(\pm 7\) frames.
The Cavri III consists of an Apple I/O (input/output) board, cables and connectors, systems software in Applesoft BASIC on disk, and a user's manual. It is available for video cassette recorders that carry a control pulse or that interface with manufacturers' search units. Users can convert already made videotapes, produce new tapes, or arrange to have Cavri produce materials. For information, contact Cavri Systems Inc, 26 Trumbull St, New Haven CT 06511, (203) 562-9873.

Circle 549 on inquiry card.

\section*{Floating-Point Board for the Apple}

Increased speed is now available for the Apple II. The Computer Station Am9511 fast floating-point processor board plugs into the Apple II and relieves it of the task of doing transcendental functions in software. Instead, it uses a version of the standard floating-point BASIC, called Applefast, that allows the user to run existing programs without modifications: Taking 5000 square roots normally takes 250 seconds running Applesoft, but with Applefast it takes 15 seconds. Details can be obtained from Computer Station 12 Crossroads, Granite City IL 62040, (618) 452-1860.

Circle 550 on inquiry card.

\section*{Reduce the Cost of Memory for the PET}

The PH-001 2114 programmable memory adapter for the 2001-8 PET allows the use of lower-cost 2114 programmable memory integrated circuits to replace one to eight of the 6550's 1 K by 4 circuits used in the 8 K -byte PET. The board alone is \(\$ 8.95\), and the entire unassembled kit is \(\$ 13.95\), or \(\$ 24.95\) assembled. Contact Optimized Data Systems, POB 595, Placentia CA 92670 , (714) 996-3201.

Circle 551 on inquiry card.

\section*{MSC-8100 Features Hardand Floppy-Disk Storage}

The MSC-8100 system incorporates an intelligent controller/formatter with a universal IEEE-488 bus protocol, a Winchester technology hard-disk drive with a 19.1-megabyte capacity, and a backup floppy-disk drive with a capacity of 1.6 megabytes per disk. The MSC-8100 is useful for word-processing and smallbusiness applications. The average access time of the hard-disk drive is below 30 ms . The controller features a fullsector data buffer, error detection and correction, error recovery including automatic retry, automatic position verification, automatic seek to alternate track, parallel or serial interrupt, relative sector addressing, programmable sector interleaving, implied seeks, and more. Self-testing diagnostics are also provided. The MSC-8100 is priced at \$9250. For information, contact Microcomputer Systems Corporation, 432 Lakeside Dr, Sunnyvale CA 94086, (408) 733-4200.

Circle 552 on inquiry card.

\title{
What's New? \\ MISCELLANEOUS
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\section*{Pensée Pascal Computer}

Computer Interface Technology's Pensée system is a stack-oriented, 16-bit computer with a dual floppy-disk subsystem capable of storing up to 2 megabytes. It features 64 K bytes of programmable memory; floating-point hardware; floppy-disk controller; 8-inch single- or double-sided, single- or double-density floppy-disk drives; two serial RS-232 asynchronous/synchronous ports; two unidirectional 8-bit parallel ports; and self-test diagnostics. Pensée utilizes the UCSD Pascal operating system version III. 0 , which includes the Pascal compiler, BASIC compiler, file manager, screen-oriented editor, and debugger. Some UCSD language extensions are also included. Prices range from \(\$ 3500\) to \(\$ 9000\), depending on peripheral subsystems. Obtain information from Computer Interface Technology, 201 W Dyer Rd, Santa Ana CA 92707, (714) 979-9920.

Circle 553 on inquiry card.

\section*{Peelings}

Peelings is devoted exclusively to reviews of software for the Apple II and Apple II Plus microcomputers. Each bimonthly issue contains reviews of twelve to fifteen programs or software packages. Subscriptions are \(\$ 15\) from Peelings, Ed Burlbaw, 945 Brook Cr, Las Cruces NM 88001, (505) 523-5088.
Circle 554 on inquiry card.

\section*{The Flex-File System}

The Flex-File is a nonglare vinyl page having pockets on each side to house two 8 -inch floppy disks plus a center pocket to store 22 by 28 cm ( 8.5 by 11 inch) paper, computer printouts, or other documentation. The pages are three-hole punched for storage in standard three-ring binders. Flex-File pages are priced at \(\$ 8.95\) for a package of ten pages and are available from BIS Inc, POB 969, Brentwood TN 37027
Circle 555 on inquiry card.

\section*{Elementary Math Edu-Disk}

The Elementary Math Edu-Disk contains an arithmetic-readiness test and four interactive lessons designed to teach elementary addition, subtraction, multiplication, and division, in nine skill levels. These lessons use color graphics and a computer-simulated voice to maintain student interest and reinforce basic concepts. The student's scores are maintained on disk and are accessible only through a special teacher's program. The system is self-demonstrating and is recommended for the student with no prior arithmetic experience, and as a supplement in higher-level remedial situations. The requirements for the program are an Apple II computer with 48 K bytes of programmable memory with Integer BASIC. The price for the program is \(\$ 39.95\), from Muse Software, 330 N Charles St, Baltimore MD 21201, (301) 659-7212.

Circle 556 on inquiry card.

\title{
What's New? \\ MISCELLANEOUS
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\section*{Dust Covers for Computer Terminals}


These dust covers are designed to protect video terminals, printers, and keyboards from dust and dirt. They are made of heavy-gauge clear plastic that will protect against water damage. The covers are custom made to fit any specific model of computer terminal, keyboard, or printer for all computer systems. When ordering, specify the
system being used. The price for a cover for a video terminal including keyboard is \(\$ 9.95\). For a keyboard only, it is \(\$ 8.95\), and for a printer it is \(\$ 9.95\). For details, contact The Computer Accessories Company, 20 Boat Ln, Port Washington NY 11050, (516) 767-0366.

Circle 557 on inquiry card.

\section*{Burst-Error Processor from AMD}

Advanced Micro Devices (AMD) has announced a general-purpose burst-error processor (BEP). This LSI (large-scale integration) device, the AmZ8065, can detect and allows correction of up to 12-bit burst errors in serial data streams moving at up to 20 million bps (bits per second). The codes implemented in the BEP include 48 - and 56 -bit polynomials used by IBM and 32 - and 35 -bit polynomials favored by minicomputer manufacturers. The BEP provides two read modes, normal and high-speed, that determine the correction methodology if an error is found. The AmZ8065 user can select the correction method based on the Chinese Remainder Theorem. This method computes the error location and the correction needed. The BEP employs a reciprocal polynomial that approaches the data stream from the check-bits side. This reduces worst-case correction time to the length of the data stream. The device accepts data as serial bytes which allows a single-phase clock requirement of 2.5 MHz . It operates from a single +5 V supply and comes in a 40 -pin integrated circuit. Prices start at \(\$ 69\) each in one hundred-unit lots. Contact Advanced Micro Devices Inc, 901 Thompson Pl, Sunnyvale CA 94086, (408) 732-2400.

Circle 560 on inquiry card.

\section*{Accounts Receivable Program for the TRS-80}

Radio Shack has an accounts receivable system for use on the TRS-80 Model I. Accounts receivable provides end-of-month billing, statements ready for mailing, automatic customer-record updating, totals for general ledger posting, optional message lines on billing statements, and full accounts receivable analysis including activity status, and more. Reports printed by this system are complete transaction file report, general ledger recap report, complete accounts listing, account listing by activity status, accounts receivable analysis by activity status, and posting report. A Model I Level II system with 16 K bytes of programmable memory, plus an expansion interface with at least 16 K bytes of programmable memory, an 80 -column printer, and a minimum of two disk drives are required. The accounts receivable system is priced at \(\$ 149.95\) from Radio Shack dealers and stores.

Circle 558 on inquiry card.

\section*{Computer/Typewriter Interface}

The I/O Pak from Rochester Data consists of an array of coils positioned in the same pattern as a typewriter's keyboard, in a unit that fits directly over the keyboard. These coils are wired into an electrical decoding matrix. The I/O Pak is designed to generate hard copy directly from a computer through any electric typewriter with a powered carriage return. No modification to the typewriter is required, and all adjustments to compensate for different key heights are incorporated in the I/O Pak. Available options include interfaces and software for the TRS-80 Level I and II, the Apple II, and a 6-bit parallel interface for general operation with other computers. Centronics-compatible and PET interfaces are also available. The I/O Pak retails for \(\$ 469\); the interface board and power supply required for packaged operation are priced at \(\$ 145\). Contact Rochester Data Inc, 3100 Monroe Ave, Rochester NY 14618, (716) 385-4338.
Circle 559 on inquiry card.

\section*{OKI 4 K Static \\ Programmable-Memory Integrated Circuits}

OKl Semiconductor, 1333 Lawrence Expy, Suite 401, Santa Clara CA 95051, (408) 984-4840, has introduced the MSM 2114L series of 4 K static programmable memory integrated circuits. The MSM 2114L, MSM 2114L-2, and MSM 2114L-3 are n-channel silicon-gate MOS (metal-oxide semiconductor) circuits that use fully static circuitry which does not require clocks or refreshing. The circuits are interchangeable with all standard 2114L parts and feature TTLcompatible (TTL is transistor-transistor logic) I/O (input/output), and a single +5 V power supply. They feature maximum access times of 200 ns for the \(2114 \mathrm{~L}-2,300 \mathrm{~ns}\) for the \(2114 \mathrm{~L}-3\), and 450 ns for the 2114L, and maximum power dissipation of 370 mW . Prices are \(\$ 5.45\) for the 2114L, \(\$ 5.65\) for the \(2114 \mathrm{~L}-3\), and \(\$ 6.75\) for the \(2114 \mathrm{~L}-2\). These prices are for 100 -unit quantities.

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Whats New?
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\section*{MISCELLANEOUS}

\section*{Model 460 Paper Tiger Printer from IDS}

The Model 460 addition to the IDS Paper Tiger family of printers produces letter-quality printing at a speed of 160 cps (characters per second). It also provides high-resolution graphics capability and includes proportional character spacing and automatic text justification. The Model 460 is a dot-matrix printer that utilizes a horizontal and vertical dot overlay to achieve letter-quality printing. It can print in 80-, 96- and 132 -column formats. Foreign and custom character sets are optional and up to four 96 -character sets can reside in the 460 at the same time. Paper-handling
features include pin-feed tractor drives. A microprocessor provides an automatic test of the printer's memory and electronics each time the power is turned on, and a full character-set print capability test. A 2 K -byte buffer allows the Model 460 to accept the contents of a 1920-character video screen. The 460 has a standard RS-232C serial interface as well as a Centronicscompatible parallel interface. Serial transmission rates from 110 to 9600 bps (bits per second) are switch selectable. The Model 460 costs \(\$ 1295\) from Integral Data Systems, 14 Tech Cir, Natick MA 01760, (617) 237-7610.

Circle 562 on inquiry card.

\section*{Aspen Ribbons}

Aspen Ribbons has announced the addition of four cartridge ribbons to its line of ribbon products. Aspen now manufactures Hytype I and II ribbons in nylon and carbon. Aspen molds its own cartridges by injection. Colors and
private labels are available. The company also has a Wang multistrike cartridge ribbon and Qume 2 and 3 multistrike ribbons. For additional information, contact Aspen Ribbons, 1700 N 55th St, Boulder CO 80301, (303)
444-4054.
Circle 563 on inquiry card.

\section*{Music Synthesizer for the Apple}

The Juke Box is a music synthesizer designed for any 48 K -byte Apple using Applesoft BASIC. It can produce three simultaneous voices and one channel of white noise. Pitch, rhythm, tempo, attenuation, and envelope can be selected and controlled for each voice independently from the other channels. The synthesizer has a five-octave range. Each card has an on-board amplifier capable of directly driving an 8 -ohm speaker. As many as six cards can be installed to generate a total of eighteen notes. Multiple boards can create stereophonic, quadraphonic, and polyphonic operation. The devices can be daisy-chained to create more voices per speaker. A graphics music editor is also provided só the music can be seen and heard as it is input and edited. The price for the Juke Box is \(\$ 129.95\). Contact American Micro Products Inc, 705 N Bowser, MS 107, Richardson TX 75080, (214) 238-1815.
Circle 564 on inquiry card.

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\section*{OSI C1P Superboard II Modification Kit}

The Super-Mod Kit provides a 48 -character by 26 -line video display and software selection of 300 or 1200 bps (bits per second) for cassette and RS- 232 operation. The kit also provides an RS-232 port, start and stop control of the cassette, and doubling of system clock speed. Voice cuing and a listening function can be added. The kit contains all parts and documentation. Among the kit's contents are a regulated multiple-voltage power supply, a programmed monitor PROM (programmable read-only memory) compatible with all existing Ohio Scientific Instruments' functions and capable of for matting the video display with screen clear function callable under BASIC or assembly language, and sample programs. The price is \(\$ 95\) from A H Systems Inc, 9710 Cozycroft Ave, Chatsworth CA 91311, (213) 998-0223.
Circle 565 on inquiry card.

AIM-65 Enclosure


This enclosure is designed for the AIM-65 microcomputer. It is made out of high-strength ABS plastic and comes with mounting hardware, wire, and switches. All parts are pre-cut and drilled, and there is room for two additional boards. The color is white with a blue base. The enclosures are \(\$ 49.95\) plus \(\$ 2.50\) for shipping and handling. Contact Don-El Enterprises, 3261 Michigan Ave, Costa Mesa CA 92626, (714) 546-7481.

Circle 566 on inquiry card.

\section*{Modem Microphone from Novation}


Super Mike was engineered specifically to eliminate data-distorting second harmonics. This Federal Communications Commission (FCC) registered microphone slips into your telephone handset, replacing the existing carbon microphone. The device eliminates the carbon granule packing problems that can cause a difference in reproduction level from telephone to telephone.
Priced at \(\$ 9.95\), Super Mike is available from hobby stores, retail electronic outlets, and industrial distributors. For complete information contact Novation 18664 Oxnard St, Tarzana CA 91356, (213) 996-5060.

Circle 567 on inquiry card.

\section*{The Nobus-Z}

The Nobus-Z contains a 4 MHz Z80A microprocessor, the CP/M operating system, 64 K bytes of dynamic programmable memory, dual-density 8 -inch floppy-disk drives with 600 K bytes per side, and a 6 K -byte color text and graphics feature. Console configurations range from a keyboard and television set to separate word-processing display terminals. A typical 70 K -byte system with 600 K bytes of disk storage costs under \(\$ 3000\). For more information, contact Exo Electronics Company, POB 3571, Culver City CA 90230, (213) 390-6527. Circle 568 on inquiry card.

\section*{AIM-65 Expansion}

The Memory-Mate, a 16 to 48 K-byte programmable-memory expansion board offers AIM-65 expansion for development system and process-control applications. The memory is assignable in 4 K blocks, with each of the blocks positionable anywhere in the system. The board also features full parity check circuitry and includes protection for AIM's 4 K on-board programmable memory. Another feature is programmable write protection in 4 K blocks. Four 8 -bit bidirectional, 6522-type I/O (input/output) ports are included on the board. In addition, the board includes a programmable tone generator for audible warnings and sockets for up to 4 K PROM (programmable read-only memory). Price of the Memory-Mate with 16 K bytes of storage, connector to AIM, and manual is \(\$ 475\). Write AIM-Mate Series, Forethought Products, 87070 Dukhobar Rd, Eugene OR 97402, (503) 485-8575.

Circle 569 on inquiry card.

\section*{Floppy Disk Insurance?}

Micro Lab has instituted a new plan for microcomputer users: Micro Lab Disk Insurance. The policy is being offered with the purchase of its Data Factory product line. The package is sold to the user with two locked versions of the master disk. If a master disk becomes damaged during the policy period, the policyholder may return the inoperative copy to Micro Lab for immediate free replacement. Users can switch to the backup master disk without any break in service. In addition, if an update in the program should occur, users will be notified, and the older versions will be revised at no cost. The policy sells for \(\$ 17.50\) per year. The Data Factory, a data-base management system, is offered in Applesoft and other forms. The program can run with one or two disk drives, but needs 48 K bytes with Applesoft in read-only memory. Information can be obtained by writing or calling Micro Lab, 811 Stonegate Dr, Highland Park IL 60035, (312) 433-7877. Circle 570 on inquiry card.

\section*{The PMC-80-Compatible with the TRS-80}

Personal Micro Computers Inc, 475 Ellis St, Mountain View CA 94043, (415) 968-1604, is offering a software- and hardware-compatible equivalent of the Radio Shack Model I, Level II TRS-80. The PMC-80 has a cassette tape recorder, 16 K bytes of programmable
memory, Level II Microsoft BASIC interpreter in ROM (read-only memory), a power supply, computer, and keyboard. The system will display on either a television monitor or on a television set using a built-in VHF channel 3 modulator. All software available for the TRS-80 will operate in the PMC-80. Level II BASIC or SYSTEM cassettes will load in the PMC-80 without volume
adjustments. All peripherals designed for the TRS-80 parallel port interface to the PMC-80 through an interface adapter available from the company. The price for the PMC-80, according to the manufacturer, is about \(\$ 200\) less than a comparably equipped TRS-80.

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What's New?
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\section*{MISCELLANEOUS}

\section*{Multibus-Compatible Multimemory Board}

A Multibus-compatible memory module that can accommodate industrystandard ROMs (read-only memory), EPROMs (erasable programmable readonly memory), and static programmable-memory integrated circuits in any combination is available from Artec Electronics Inc, 605 Old County Rd, San Carlos CA 94070, (415) 592-2740. The board contains sockets and memory interface logic for up to sixteen twenty-four-pin memory devices. It can contain a maximum of 64 K bytes of EPROMs or 32 K bytes of static programmable-memory circuits. The board can operate with only one socket filled. Memory addresses are independently assigned for each socket with wire-wrap jumpers. Any multiple of 1 K bytes can be addressed within a 64 K-byte address space. Memory access time is wire-wrap selectable. The lowpower interface circuitry contains inhibit logic for each of two banks of eight memories. The multimodule board can interface with any 8 -bit Multibus-

\section*{Printer from Matchless}

The MS-204 printer is compatible with the TRS-80, Apple, PET, or any Centronics-type system. This 132-column, bidirectional, 9-by-7 dotmatrix printer has a printhead life of 100 million characters. Among the features are a print speed of 125 cps (characters per second) and throughput print speed of 63 lines per minute. The adjustable sprocket feed mechanism allows the use of forms from 6.4 to 24 cm wide ( 2.5 to 9.5 inches), with loading from either the bottom or rear. Uppercase and lowercase characters are provided. The printer provides preprogrammed and programmable tab postions, and top of form and bottom of form functions. The retail price is \(\$ 795\) from Matchless Systems, 18444 Broadway, Gardena CA 90248 , (213) 327-1010.

Circle 575 on inquiry card.
compatible microcomputer. The price of the board is \(\$ 175\), not including memory circuits.
Circle 572 on inquiry card.

\section*{PDP-11 FORTH}

This FORTH system runs on any PDP-11 or LSI-11 microprocessor and requires less than 24 K bytes of memory. The floppy disk contains an RT-11 directory with FORTH in Macro-11 source, with extensive comments; this source can be assembled and run under RT-11, or under RSX-11M, or stand-alone, with or without EIS. The disk is single-density, but will run on a dual-density drive under RT-11. PDP-11 FORTH implements the FORTH Interest Group (FIG) language model, with fulllength names to 31 characters, and extensive compile-time checks. In addition, an editor, a FORTH assembler, and a string package in FORTH source, are included. The system on disk, the PDP-11 FORTH User's Guide, A FORTH Primer, FORTH Introduction Reprints, an installation manual, and an assembly listing comprise the entire system. The cost is \(\$ 140\) from John S James, POB 348, Berkeley CA 94701, (415) 526-8815.

Circle 576 on inquiry card.

\section*{Desk-Top Calculator with a Voice}

The Model SP1260-D, a talking calculator from Canon, is expected to be used in general business offices, banks, brokerage houses, schools, hospitals and factories. The unit's speech synthesizer is used when the operator wants to check entries on the roll paper. The voice feature eliminates the need for two employees to check lists of numbers. The calculator can store up to 128 items of data, including the final result of the input. The SP1260-D incorporates the voice feature, a 12 -digit capacity, memory for accumulating results, item counting, decimal point selection, and more, for \(\$ 399\). Contact Canon Calculator Division, Canon USA Inc, 10 Nevada Dr, Lake Success NY 11042.
Circle 573 on inquiry card.

\section*{All-CMOS Single-Board Microcomputer}

Pacific Cyber/Metrix Inc, 6800 Sierra Ct, Dublin CA 94566, (415) 829-8700, has announced availability of an allCMOS (complementary metal-oxide semiconductor) single-board microcomputer capable of plugging directly into the Intel-originated Multibus card cage. The PPS-1201 features a CMOS 6100 microprocessor, 4 K bytes of memory that can be configured as any combination of CMOS programmable memory and CMOS EPROM (erasable programmable read-only memory), a programmable real-time clock, memory expansion controller, three 12 -bit-wide parallel ports, and a single serial port. Also included is a transparent 1 K -byte monitor and debugger plus a binary bootstrap for loading on-board programmable memory through the serial port. The 6100 microprocessor employs a binary instruction set identical to that of the Digital Equipment Corporation PDP-8 and VT-78 DECstation minicomputers, so software development can be carried out on any of these machines. The price for the 1201 is \(\$ 995\). Circle 574 on inquiry card.


\title{
Whats New?
}

\section*{Report on Personal Computers Covers Trends, Systems, Software, and Vendors}

Datapro Research Corporation's All About Personal Computers, traces the development of personal computers, discusses the future of the devices, and outlines how to buy a system. Also featured are reports on fifteen of the top personal computers, plus directories listing vendors of computers, software, peripherals, and publications. All About Personal Computers is available for \(\$ 25\) from Datapro Research Corporation, 1805 Underwood Blvd, Delran NJ 08075, (609) 764-0100.
Circle 577 on inquiry card.

\section*{Report on Voice Processing}

The technologies of speech recognition and speech synthesis have been implemented into computer systems and have been employed in transportation quality control, auto assembly, bank deposit transfer, and consumer products. In the April 1980 issue of Data Entry Awareness Reports, MIC (Management Information Corporation) discusses the voice-processing state of the art, its applications, and how to use it. This report is available to subscribers of Data Entry Awareness Reports or can be purchased separately by check for \(\$ 15\). Contact Voice Processing Report, Management Information Corporation, 140 Barclay Center, Cherry Hill NJ 08034, (609) 428-1020. Circle 578 on inquiry card.

\section*{A Catalog from Wintek}


A catalog containing information and specifications on Wintek's Sprint 68 development system/control computer with Wizrd multitasking DOS (disk operating system), macro editor, assembler, C compiler, 12 K BASIC, and 4 K industrial BASIC, is now available. The catalog also discusses alternatives for software development, Wintek's design and educational services, and cross software products. Contact Wintek Corporation, 1801 South St, Lafayette IN 47904, (317) 742-8428.
Clircle 579 on inquiry card.

\section*{Computer Selection Handbook}

Written specifically for small businesses and consultants, the Computer Selection Handbook presents a nontechnical method for selecting computer systems. This book concentrates on the practical and business aspects of choosing the right computer for your small business. The Computer Selection Handbook explains how to document small-business computer needs, solicit and evaluate vendor proposals, make the selection decision, and manage the installation and operation of the new system. The handbook is available directly from Decision Resources Corporation, 28203 Ridgefern Ct , Rancho Palos Verdes CA 90274, (213) 377-3533, for \(\$ 35\).
Circle 580 on inquiry card.

\section*{BASIC Training for Compucolor Computers}

BASIC Training for Compucolor Computers, by Joseph J Charles, is intended for beginning users of the Compucolor II computer and is designed to serve as an introduction to Compucolor II BASIC. There are over 100 example programs and dozens of exercises in the book. The topics covered include the first steps of entering and listing programs, BASIC statements, functions, graphics, random-access files, flow-charting, subroutines, and more. The price of the book is \(\$ 14.95\), and it is available from Joseph J Charles, Dept B, POB 750, Hilton NY 14468.
Circle 581 on inquiry card.

\section*{Back Issues of Dr Dobb's Journal}

Dr Dobb's Journal of Computer Calisthenics and Orthodontia: Running Light Without Overbyte, volumes 1, 2. and 3, are available from Hayden News. Almost everything from all issues of Dr Dobb's Journal for a particular year have been gathered into these volumes. They are priced at \(\$ 18.95\) each from Hayden Book Company, 50 Essex St, Rochelle Park NJ 07662, (201) 843-0550. Circle 582 on inquiry card.

\section*{Archer Engineer's Notebook}


Radio Shack has published a handbook of 415 electronic circuits for electronics hobbyists, experimenters, technicians, and engineers. Applications are included for most of the integrated circuits sold by Radio Shack. Dozens of problem-solving circuits are described. Tips and techniques for beginners are included. The book is divided into two major sections: digital and linear. It was compiled and hand-executed by Forrest M Mims III. The Archer Engineer's Notebook is available from participating Radio Shack stores and dealers for \$1.99.
Circle 583 on inquiry card.

\section*{AIM-65 Newsletter from Rockwell}

A newsletter for owners of AIM-65 microcomputers is available on a subscription basis from the Newsletter Editor, Rockwell International, POB 3669, RC55, Anaheim CA 92803, (714) 632-2321. Interactive responds to readers' questions, publishes articles by users, reports on the activities of AIM-65 users groups, and supplies articles on novel applications. The cost is \(\$ 5\) for six issues.
Circle 584 on inquiry card.

\section*{BITS Catalog}

The fall issue of the BITS catalog is available. BITS is a distributor of computer publications located at 25 Rt 101 W, POB 428, Peterborough NH 03458, (603) 924-3356. This catalog features publications from BYTE, Osborne/McGraw-Hill, Scelbi, and others. The catalog is priced at \(\$ 0.50\). Circle 585 on inquiry card.

\title{
Whats New? PUBLICATIONS
}

\section*{Health Planning Publication}

Hapenney Associates has announced a publication entitled Data Bits. It is written for health planners, and is designed to coordinate the data and automation efforts of health planners within the 205 health-systems agencies and 51 state health planning and development agencies in the US. It examines technological advances in automated data processing that may affect health planners. ltems of interest regarding happenings at the federal level are provided, as well as information regarding current activities of different agencies. Data Bits is published monthly. Subscriptions are available at \(\$ 60\) per year. Single issues are \(\$ 5\) per copy. Contact the Assistant to the Editor, POB 1076, Columbia MD 21044, (301) 596-0874.
Circle 586 on inquiry card.

\section*{User Ratings of Computer Systems}

User Ratings of Computer Systems, from Datapro Research Corporation, 1805 Underwood Blvd, Delran NJ 08075, (609) 764-0100, details the results of a survey of 14,900 computer users that produced 4614 usable responses that provided ratings of 7871 installed systems from sixty-four vendors, along with information on applications, software, languages, problems, and future user plans. The survey covers personal computers, mainframes, minicomputers, and small-business computers. The report also includes summaries of ratings for various software applications, which languages are most commonly used on different systems and configurations, and how users felt about documentation for systems. Copies are available for \(\$ 25\).
Circle 587 on inquily carc.

\section*{Bulletin on DC-to-DC Power Supplies}

A data sheet introducing a selection of thirty new 5 and 6 watt, DC-to-DC power supplies is available from Sola Electric, 1717 Busse Rd, Elk Grove Village IL 60007, (312) 439-2800. The low-profile switching converters are designed for printed-circuit board mounting. Specification charts provide basic technical data, operational and physical descriptions.
Circle 588 on inquiry card.


\section*{The MicroShopper Guide to Microcomputers}

MicroShopper 80: The New Computers is a 192 -page business and personal guide to microcomputer hardware and software, published by P G I Publishing, a division of The Phoenix Group, 1425 W 12th Pl, Tempe AZ 85281, (602) 967-1421. This fifth edition features photographs of microcomputer systems, peripherals and accessories, plus industry literature from more than 100 manufacturers representing over 500 products. It is designed for first-time computer users, consultants, dealers, and data-processing professionals. Definitions, explanations, and reviews of equipment are provided. MicroShopper is priced at \(\$ 9.95\) retail or \(\$ 11\) including postage and handling, direct from P G I.
Circle 589 on inquiry card.

\section*{TRS-80 Supply Catalog}

The TRS-80 DOSHS (Directory of Software, Hardware, and Services) is designed to help users locate software, hardware, and support services for the TRS-80 microcomputer. The catalog contains hundreds of listings for S-100 adapters for the TRS-80, books, colorgraphic units, TRS -80 units, consulting services, floppy disks, expansion interfaces, RS-232 interfaces, light pens, lowercase modification kits, magazines, newsletters, plotters, printers, rentals, repair services, speech synthesizers, and more. It is available for \(\$ 6\) from Pen-Ter Research, 9633 Rosehill Rd, Lenexa KS 66215.

Circle 590 on inquiry card.

\section*{International Directory of Software}

The International Directory of Software is a one-volume directory featuring over 3200 independently marketed software products available from American and European suppliers. Each product is indexed within as many as five categories. Systems and applications software are listed in the directory under a total of 107 categories, including communications, compilers, data management, development aids, systems software for mainframes, systems software for microprocessors, utilities, accounting, administration, production and distribution, modeling, and other categories for various specialized applications software. Data on each product describes its date of origin, installed base, function, terms for purchase or leasing, operational mode, configuration requirements, and the names and addresses of suppliers worldwide. The International Directory of Software is priced at \(\$ 140\). Contact CUYB Publications Inc, First Federal Bldg, Suite 401, Pottstown PA 19404, (215) 326-5188. Circle 591 on inquiry card.

\section*{The B00K: Accessing the TRS-80 ROM, Volume I}

The BOOK is the first of three volumes on machine- and assembly-language access to the Level II BASIC ROM (readonly memory) in the TRS-80 Model I microcomputer. This volume details the mathematic subroutines and data formats. A fully commented listing of these routines is provided. Included in the book is a memory map of the entire machine that provides descriptions of over 500 memory locations. The BOOK is available at computer stores or from Insiders Software Consultants, POB 2441, Springfield VA 22152, (703) 960-2998, for \(\$ 14.95\) plus postage and handling. Circle 592 on inquiry card.

\section*{Catalog from OK Machine and Tool Corporation}

This catalog from OK Machine and Tool Corporation, 3455 Conner St, Bronx NY 10475, (212) 994-6600, features numerous wire-wrap tools and supplies, controllers, tape readers, circuit boards, and other items for homebrewers. A price list is also available.
Circle 593 on inquiry card.

\section*{High-Resolution Package for the AIM-65}


The MTU K-1009-1C Text/Graphics Printout program permits the AIM-65 to print text and high-resolution graphics without modifications to the computer or the printer. The contents of the AIM-65 text buffer are reproduced as ten lines of up to 127 characters per line. The display is created as a 320 -by- 200 dot matrix. The program provides the

Quick Print mode that generates the image on one paper strip, and the Quality Print mode that generates the image as two 320-by-100 strips to be taped together. The program is priced at \(\$ 25\) from Micro Technology Unlimited, 2806 Hillsborough St, POB 12106, Raleigh NC 27605, (919) 833-1458.
Circle 594 on inquity card.

\section*{Genealogy Program}

AppleRoots is a genealogy software package that can be used for human or animal genealogy. It has seventeen userdefinable fields. Functions include system initialization; record entry, change, delete; print index or records; print list of children, family records, or four-generation pedigree chart. All printer functions can be displayed on the screen or sent to the printer. All functions are menu-oriented and no programming is required to customize the system for personal use. The package is written in Applesoft and requires one disk drive and an Apple II with 24 K bytes of programmable memory. The system sells for \(\$ 39.95\) from Computer Data Systems Corporation, 695 E 10th N, Logan UT 84321, (801) 753-6990. Circle 595 on inquiry card.

\section*{Educational Software}

Educational Software, 801 E 6th Ave, Helena MT 59601, developers of educational software for the preschool thru eighth grade student, has announced a line of programs for the home-computer user. The programs provide positive feedback and cover a wide group of subjects for the young home-computer user. The programs measure the user's performance during each session and are designed for easy modification by the consumer.
Circle 596 on inquiry card.

\section*{XYBASIC Interpreter for 8080, 8085, and Z80 Systems}

XYBASIC is a language designed specifically for measurement and process control. It offers the standard features of BASIC plus machine-language linking, software interrupts, and bit manipulation commands. Versions are available for SBC/80, CP/M, ISIS-II, Intellec 8 Mod 80, and MDS-800 systems. The nonstandard XYBASIC versions, with a patchable 1/O (input/output), make the language adaptable for 8080,8085 , and \(Z 80\) sytems.

By allowing XYBASIC and the user's program to be placed in ROM (readonly memory), a program can be developed on the target system, put in ROM, and run. This eliminates the problems of floppy-disk program storage in hostile environments. XYBASIC options include a 9511 version utilizing the floating-point circuit, an EDIT version providing edit commands, an extended disk version for use with CP/M systems, and a real-time clock version for SBC/80s. XYBASIC is available in integer or extended forms. Versions start at \(\$ 350\). Custom versions can be made. For information, contact Mark Williams Company, 1430 W Wrightwood,
Chicago IL 60614, (312) 472-6659.

\section*{Apple Users Gain Access to Dow Jones News and Stock Quotes}

Apple Computer Inc, 10260 Bandley Dr, Cupertino CA 95014, (408) 996-1010, has introduced the Dow Jones News and Quotes Reporter, a software package that puts Apple users in touch with financial news. The program retrieves, displays, and optionally prints selected news stories from the Dow Jones News Service, the Wall Street Journal, and Barron's magazine, plus it can list price quotations for more than 6000 securities.

The user gains access through a telephone and modem, and, to access news stories, the user selects News Retrieval Service from the menu. Once a password has been verified, the user can select a news category or company, scan a list of headlines about it, and view the story. Stock quotes can be gained in the same way. The system will run on an Apple II or Apple II Plus with a minimum of 48 K bytes of programmable memory. Also required are a 16-sector format Apple Disk II with a controller, a modem, a video monitor, and a telephone. A printer is optional. Owners will receive \(\$ 25\) of connection time when they purchase the package, which retails for \(\$ 95\).
Circle 598 on inquiry card.

\section*{CP/M Advanced BASIC Compiler}

This compiler, called the Topaz Compiler, produces a relocatable object file that is auto-linked with several libraries to produce a CP/M-compatible .COM file. Two types of floating points are available as well as integer and a fixedpoint format. The compiler supports REPEAT...UNTIL, WHILE...DO, IF...THEN...ELSE, BEGIN...END, and CASE...OF techniques. All structured statements may be nested. The compiler supports double- and single-precision floating point, fixed-point packed binary-coded decimal, integer, string and character data types. Disk files may use a packed binary format or an ASCII (American Standard Code for Information lnterchange) storage format. Any .COM file can be loaded and executed from control of a BASIC program. Commands can be executed under program control after the .COM file is finished. The price is \(\$ 249.95\) from Midwest Digital, 863 Wood Ave, Wichita KS 67212, (316) 721-1671.
Circle 599 on inquiry card.

\title{
Whats New? SOFTWARE
}

\section*{Symbolic Dissassembler for 6809 Computers}

The 6809 symbolic disassembler is written for users of the 6809 microprocessor. DISASM6809 is reentrant, able to be put in ROM (readonly memory), and positionindependent. It is called as a subroutine once for each instruction to be disassembled. All necessary parameters, including the address of the user's output routine, are passed in registers. The disassembler can produce alphanumeric symbols in both the label and operand fields. Invalid op codes are detected. The program requires under 2 K bytes of space and uses approximately 32 bytes of memory on the calling stack. Output format is syntactically identical to Motorola's assemblylanguage definition. DISASM6809 is available as a commented assembly listing with instructions for \(\$ 25\). Contact C R Bilbe, 6933 Cedarwood Cir, Colorado Springs CO 80918.
Circle 600 on inquiry card.

\section*{Order-Entry Software Package for Small Businesses}

Order Entry will handle the documentation and control of purchasing and sales. The information from Order Entry can be processed through the accounts payable, accounts receivable, inventory control, and general ledger programs from Compumax, updating these modules to reflect purchase and sales activity. Order Entry includes generation and printing of purchase and sales orders, computation of tax and registration of deliveries against outstanding purchase orders and of shipments against outstanding sales orders, along with complete purchase and sales order history reports. The program is available in Micropolis 1053/II ( 48 K ), Apple II, PET (DOS 2.0), and Microsoft under \(\mathrm{CP} / \mathrm{M}\) versions. For further information, contact Compumax, POB 1139, Palo Alto CA 94301, (415) 321-2881. Circle 601 on inquiry card.

\section*{Microsoft BASIC \\ Interpreter for the Z8000}

BASIC-Z8000 is an interpreter for the 16-bit Z8000 microprocessor. This interpreter uses an expanded internal notation that takes advantage of the Z8000's 32 -bit instructions. The accuracy of internal calculations is in excess of eight digits for single precision and eighteen digits for double precision. Variables are stored using the proposed IEEE (Institute of Electrical and Electronics Engineers) standards, allowing for a doubleprecision range of exponents from -308 to +308 . BASIC-Z8000 is fully language-compatible with Microsoft's BASIC-80 and -86 interpreters, Release 5.0. Microsoft BASIC progams can be run on the \(8080,8086,28000\) interpreters without modification. Evaluation copies of BASIC-Z8000 may be purchased for \(\$ 350\) (extended) or \(\$ 600\) (disk), from Microsoft, 10800 NE 8th St, Suite 819, Bellevue WA 98004, (206) 455-8080.
Circle 602 on inquiry card.

\section*{Be a BYTE Author}

With BYTE's recent growth, we are now able to offer you more of the best articles and features about personal computing. Since much of the information in BYTE is supplied by you, the reader, you now have an even better chance to be a pald BYTE author. Our current needs include:
- Articles: BYTE is always looking for well-written articles that cover the field of microcomputing.
- Hardware/Software/System Reviews: BYTE is expanding its review of hardware, software, and computer systems. We are looking for detailed, comprehensive reviews as well as short (one- to three-page) reviews.
- TechnicallEducation/Languages Forums: These forums allow readers to take a stand on various issues or to clarify points made in the magazine.
- Programming Quickies: Do you have a program you'd like to share as a Programming Quickie? Send it in with a page or two of explanation.
- Systems Notes, a new feature, is devoted to sharing both hardware and software tips and techniques that you've found useful for any microcomputer brand or homebrew design. We will pay \(\$ 20.00\) for short submissions and the standard BYTE rate for articles that are one typeset page or longer.
We are interested in material about the Apple, Radio Shack TRS-80, Commodore PET/CBM, Exidy Sorcerer, Atari, Ohio Scientific, Compucolor, Microsoft BASIC, CPIM, and S-100-bus computers,
as well as other computer brands and homebrew designs. Undocumented information about a particular computer (eg: machine-language routine entry points) is also useful.

\section*{General Format and Treatment}

All submissions, including letters and other nonpaid material, should be typed, double spaced, and on white paper. All listings should be computer printouts using a fresh ribbon and unlined white paper only. (Look closely at your printout to make sure that the typeface is as dark and solid as possible so that we can photo-reproduce it for the magazine printing.) Cassette tapes or 5-inch floppy disks are acceptable, as are 8-inch CP/M floppy disks. No unused submissions can be returned without a self-addressed envelope and sufficient postage.

We will accept or reject each submission within three months of receipt, four months for articles. Full payment for short submission or advance partial payment for articles and larger submissions will be sent with the letter of acceptance. Completing payment for articles and longer submissions will be sent at the time of publication. Standard BYTE payment, except where noted above, is \(\$ 50\) per magazine page of material.

We hope to hear from you soon.
Would you like to know more about being a BYTE author? If so, then send a large, stamped, self-addressed envelope to:
Author Information BYTE Publications 70 Maln St Peterborough NH 03458

\title{
Whatis New?
}

\section*{SOFTWARE}

\section*{Civil Engineering Package}

The USA Civil Engineering package from Universal Software Applications Inc, 13001 Cannes Dr, St Louis MO 63141, (314) 878-1277, consists of three independent programs. The first is the USA COGO Civil Engineering Coordinate Geometry program that can be used for right of way surveys, highway design, bridge geometry, interchange design, construction layout, airport design, and other applications. Some of the COGO commands included are distance, locate/azimuth, locate/bearing, inverse/azimuth, points/intersect, azimuth/intersect, arc/line/points, arc/arc/intersect, area, simple/curve, and deflection/LS.
The second program is available for roadway design or subdivision design; it is entitled the USA Earth Design Earthwork Quantities program. It features independent input files for vertical curve, existing ground, proposed section, and design requirements files. Output is by section and includes the station, eleva-
tion of profile grade, assumed factors for cut and fill, area, volume and accumulated volume.
Finally, there is the USA Stress Structural Engineering Systems Solver which performs linear analysis of elastic, statically-loaded plane-framed structures. Structure, number of joints/members/loadings, joint coordinates, member incidences and properties, loading, member and joint loads, tabulate, solve, and stop, and a host of other commands are included. Output consists of the input structure data for each loading condition, the horizontal, vertical, and rotation components of deflection at each joint, the axial forces, shear forces, and moments at the ends of each member or optionally at interior points. The programs will run on Z 80 , 8080, and 6502 systems with a minimum of 32 K bytes of memory. The one-time lease price is \(\$ 1000\) for individual programs, \(\$ 2250\) for all three programs, and \(\$ 1750\) for any two.

Circle 603 on inquiry card.

\section*{Apple II Statistical}

\section*{Program}

Rosen Grandon Associates has announced A-STAT 79, a general-purpose statistical package for the Apple II. The system is a subset language of the P-STAT 78 package for mainframe computers. The program can have as many as forty-five variables for each of 2000 cases. A-STAT is designed for market research, survey analysis, social and economic modeling, simulations, or teaching statistics. Statistical procedures include file definition and descriptive statistics, frequency distributions, bivariate frequency distributions, the
ability to create square correlation matrices, multiple regression and path analysis of linear combinations of variables, permanent file modification, variable transformations, and descriptive statistics file production, and more. A-STAT runs on the Apple II or Apple II Plus systems with 32 K bytes of memory and Applesoft in ROM (readonly memory), or 48 K bytes and Applesoft software. One or more floppy-disk drives are required. It is priced at \(\$ 100\) from Rosen Grandon Associates, 296 Peter Green Rd, Tolland CT 06084

Circle 604 on inquiry card

\section*{COBOL for the TRS-80}

Radio Shack COBOL can make the TRS-80 Model II compatible with many existing COBOL programs, including some written for mainframe computers. This development system offers multikey ISAM (index sequential-access method) files. Features include a one-pass compiler, full screen formatting, full ANSI (American National Standards Institute) Level 2 I/O (input/output), program linkage, and segmentation. The Radio Shack COBOL development system, with a reference manual, user's guide, sample program, and floppy disk is priced at \(\$ 299\) from participating Radio Shack stores and dealers, and Radio Shack Computer Centers.
Circle 607 on inquiry card.

\section*{polyFORTH-CP/M}
polyFORTH-CP/M from FORTH Inc can run on nearly any 32 K-byte or larger CP/M-based system. The program resides on a CP/M floppy disk as a command file. When loaded, it finds and links up to the CP/MI/O (input/output) drivers, initializes itself, and responds "up" on the system console. The program runs in place of \(C P / M\), utilizing only the \(\mathrm{CP} / \mathrm{M} / / \mathrm{O}\) drivers. FORTH Inc's 8080 polyFORTH system on a floppy disk and a manual containing the interface material are provided. A CP/M utility that allows transferring polyFORTH blocks to a \(\mathrm{CP} / \mathrm{M}\) file and transferring a CP/M file to polyFORTH blocks is also provided. Source code is supplied for the entire system. polyFORTH-CP/M is available from M \& B Design, 820 Sweetbay Dr, Sunnyvale CA 94086, (408) 243-0834, for \(\$ 4750\).
Circle 608 on inquiry card.

\section*{Inventory-Control System for Cromemco Computers}

Feith Software has announced the release of its inventory-control system for manufacturers, wholesalers, and retailers. It is designed to run on any Cromemco- or CP/M-compatible system having dual floppy-disk drives, 48 K bytes of programmable memory, and a

132 -column printer. It features parts explosions of finished goods and assemblies, automatic generation of pull sheets, and it will remove parts from stock after a production run. A full audit trail of inventory transactions is maintained. The capacity of the system on a double-density 8 -inch floppy disk is over 2000 inventory items and 2000 transactions per disk. Reports are pro-
vided for economic order quantities, reordering, ABC analysis, and stock status. The package comes on an 8 -inch floppy disk, with a manual and program listings for \(\$ 250\). For details, contact Feith Software, Cedarbrook Hills A-1103, Wyncote PA 19095, (215) 887-9780.

Circle 605 on inquiry card.

\section*{Z8000 Software from Hemenway}

The RAZ8002ML resident assembler, which includes the LINKZ8002 linking loader, comprises a two-pass macroassembler and a one-pass linking loader. They are designed to run under Hemenway Associates Inc (located at 101 Tremont St, Suite 208, Boston MA 02108,
(617) 426-1931) HA-CP/Z8000 operating system in a 32 K -byte system. The RAZ8002ML has full macroassembler facilities and conditional assembly of up to eight nested levels. It produces a listing and a sorted-sy mbol table that generates relocatable and linkable object code. The program uses a hash-coded symbol table and binary search of the mnemonic table, and it allows separately
assembled routines to share data for production of programs suitable for ROM (read-only memory) circuits. All Zilog-defined op codes are recognized, and a set of pseudo-operation instructions is included. The program is priced at \(\$ 350\).

\title{
PRIORITY ONE ELECTRO \\ 
}

\section*{MULTIMODE FLOPPY DISK CONTROLLER 5}
cot \& Tested
- Based on Western Digital 1793
- IBM Format Compatible
- ROM Controlled Addressing
- Allo Boot
- Single and Double Densily
- Write Precompen
- Fast Seek if Voice Coll Type Drives
- Aulo Eject (Persci)
- Read Data Separato
- Data input Fiftering
- 2K Byte EPROM (2716) for Auto Boot
- Auto Wait Port Select
- Led's to Indicate Boot.

Select and Bank

Orive Compatibility
liteitaces to boih \(525^{\prime \prime}\) and \(8^{\prime \prime}\) double- and single-sided drives in any combination up to four drives total is bus-compatible with Shuga and Mernotex \(525^{\prime \prime}\) and \(8^{\prime \prime}\) drives and can be made compatible with wide variely of soft-sectored drives For vorce coll drives, fast seek opetation can be ether soltware or hardware enabled
Disk Controllar Chip
Uses the powerful Western Digital 1793 disk controller chip. This chip piovides BM -compatible single and double density formatting. perpoims the read data separation. provides comprehensive track and sectot status information, etc
Bank Select
Cinl be hardware-assigned to one of eight banks Bank then software selected by ou*nutting bank select byte to port 40 h . Bank-selec system can be disabled entirely or just at power-on and reset so that hoard comes up enabled
On-board ROM
Comes with on-board. 2 K EPROM containing both monitor firmware and a bootstrap loader for loading CP/M from disk Board can be contrgured to etther load in CP/M on systerm. system power-on and eset or on a monitor command After CP/M is loaded, monitor and nootstrap loader are disabled. The monitor firmware contains outines for reading and writing to/from disks, for dumping, moving and changing memory. etc ROM. When selected. generates the PHANTOM line lor memory overlay. ROM's setection handled by addiess decoding ROM


Accessibla Reqiatert
Internal to the 1793 are the Command. Status. Track. Sector, and Data registers. External are the board Control/Status registers 1 and 2 Control registers allow software specification of double or single density formatting. drive size. disk side. drive number. etc. Decoding of register addresses handled by ROM: optıonal ROM availabie for memory mapped \(1 / 0\)

\section*{Wait State Generation}

Software-enabled Auto Waits allow 2422 to force the CPU into a Watt state when data register is busy during either a board status register read or a data register read/write User can select which register access generates Auto Waits Board can also be set to request one Wait state per cycle in which the ROM is selected. or if user's system supports this feature, per cycle in which the ROM is selected and the CPU is operating at 4 MHz .

\section*{On-board Reaad Clock Genteration}

On-board circuitry supplies the controller chip with the Read clock signal it needs to perform read data separation.
Write Precomplensation
Write precompensation provided for double density formatting. Power Supply
Unregulated \(* 8 \mathrm{~V} .+16 \mathrm{~V}\). and -16 V . Draws less than 1 amp at +8 V . Physical Description
Features reliable, easy-to-confiqure plug jumpers - Uses primarily low-power Schottky devices - Sockets for all ICs - Solder-masked on both sides - Gold-plated edge connector tingers - Silkscreen of component outlines, reference numbers, and part designations.

\section*{LOBO DISK DRIVES LOBO 8" DISK DRIVE CABINET}

New from Lobo, a dual Cabinet with power supply and internal data cable hook-up
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SHUGART DRIVES AND SAVE
with power supply and internal data cable
WITH 1 dRIVE
WITH 2 IRIVES

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\section*{CALIFORNIA COMPUTER Z-80 SBC CPU}
- 2 or 4 MHz Operation (selectable)
- Power On Memory Jump
- S-100 Altair/IMSAI Compatiole
- Responds to Wait State Generat
- Responas to
- Front Panel Supported
- IO Address Mirror
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8080 S 100 Systam Compatibility
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\hline CBM WordPro II-16 or 32K, 2040, Printer & 88 \\
\hline CBM WordPro III-32K, 2040, Printer & 178 \\
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\hline CBM Assembler/Editor (disk) & 89 \\
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\hline Programmers Toolkit-PET ROM Utilities & \$ 44.90 \\
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\hline IEEE-Parallel Printer Interface for PET & 79.00 \\
\hline IEEE-RS232 Printer Interlace for PET & 149.00 \\
\hline
\end{tabular}
\begin{tabular}{ll} 
Contronics 737 Proportional Spacing Printer & \(\$ 845\) \\
NEC Spirwwriter-paraliel & 2450 \\
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\end{tabular}

SYM-1 \$209 with 4K RAM \$ 238
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SYM RAE-1/2 Assembler in ROM 85
MDT 1000 Synertek Development System 1345
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KIM-1 (add \$34 for power supply) 159
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Four onboard LEDs indicate the HEX code generated for each key depression. The board requires a single +5 volt supply. Board only \$15.00 Part No. HEX-3. with parts \(\$ 49.95\) Part No. HEX- 3A. 44 pin edge connector \(\$ 4.00\) Part No. 44 P

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\section*{44 BUS MOTHER}

BOARD


Has provisions for ten 44 pin (.156) connectors, spaced \(3 / 4\) of an inch apart. Pin 20 is connected to \(X\) and 22 is connected to \(Z\) for power and ground. All the other pins are connected in parallel. This board also has provisions for bypass capacitors. Board cost \(\$ 15.00\) Part No. 102. Connectors \(\$ 3.00\) each Part No. 44WP.


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- Converts serial to parallel and parallel to serial Low cost on board baud rate generator - Baud rates: \(110,150,300,600\). 1200 , and 2400 . Low power drain +5 volts and -12 volts required - TTL compatible - All characters contain a start bit, 5 to 8 data bits, 1 or 2 stop bits, and either odd or even parity. All connections go to a 44 pin gold plated edge connector - Board only \(\$ 12.00\) Part No. 101. with parts \(\$ 35.00\) Part No. 101A, 44 pin edge connector \$4.00 Part No. 44P

RS-232/20mA INTERFACE


This board has two passive, opto-isolated circuits. One converts RS-232 to 20mA, the other converts 20 mA to RS232. All connections go to a 10 pin edge connector. Requires +12 and -12 volts. Board only \$9.95. part no. 7901, with parts \(\$ 14.95\) Part No. 7901 A

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Drignate, RS-232 and 20 mA compatable, Full duplex, and half duplex, direct connect or acoustic coupled, on board power supply, carmier detect light, DB25 plug, 300 BAUD, Type 103 compatable frequencies. Bare board Part No. 2000, \$19.95, Kit Part No. 2000A, \(\$ 99.95\).
T.V. INTERFACE


Converts video to AM modulated RF Channels 2 or 3 . So powerful almost no tuning is required. On board regulated power supply makes this extremely stable. Rated very highly in Dactor Dobbs' Journal. Recommended by Apple Power required is 12 volts AC C.T., or +5 volts DC - Board only \(\$ 7.60\) part No. 107. with parts \(\$ 13.50\) Part No. 107A
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- Converts TTL to RS 232, and converts RS232 to TTL - Two separate circuits Requires -12 and +12 volts - All connections o to a 10 pin edge connector, kit \$59.95Part No.232A 10Pinedgeconnector \(\$ 3.00\) part. No. 10P.

\section*{DC POWER SUPPLY}
- Board supplies a regulated +5 volts at 3 amps., \(+12,-12\), and -5 volts at 1 amp. Power required is 8 volts AC at 3 amps ., and 24 volts AC C.T. at 1.5 amps . Board only \(\$ 12.50\) Part No. 6085, with parts excluding transformers \(\$ 42.50\) Part No. 6085A

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\section*{- 1 D] \(]_{1}\)}


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There are 8 inputs that can be driven from TTL logic or any 5 volt source. The circuit board can be plugged into any of the 8 sockets of your Apple II. It has a 16 pin socket for standard dip ribbon cable connection. Board only\$15.00. Part No. 120, with parts \$69.95. Part No. 120A.

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\(+16 . \&-16\) VDC at 100 mA , and BVOC at 1A. Part No. 1000A \(\$ 199.95\) kit.

\section*{PARALLEL}
 BOARD FOR APPLE II
This board has 8 triacs capable of switching 110 volt 6 amp loads ( 660 watts per channel) or a total of 5280 watts. Board oniy \$15.00 Part No. 210. with parts \(\$ 119.95\) Part No. 210A

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Baud rate is continuously adjustable from 0 to 30,000 - Plugs into any peripheral connector Low current drain. RS-232 input and output - On board switch selectable 5 to 8 data bits, 1 or 2 stop bits, and parity or no parity either odd or even o Jumper selectable address - SDFTWARE © Input end Output routine from monitor or BASIC to teletype or other serial printer a Program for using an Apple II for a video or an intelligent terminal. Also can output in correspondence code to interface with some salectrics. Also watches DTR Board only \$15.00 Part No. 2, with parts \(\$ 42.00\) Pert No. 2A, assambled \$62.00 Pert No. 2C

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Manual
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\hline M\＆R SUP R TERMINAL 80 COLUMN & & APPLE II 8 APPLE II PLUS \\
\hline BOARD & 339 & Pascal wath lancuage system \\
\hline  & 299 & Foriran to use with language \\
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\hline MOUMTAIM HARDWARE & & CP M tor use with MICROSOFT 180 \\
\hline APPLI CLOCK CALENDER CARD & 229 & SOICARD \\
\hline Supertalmir speech symitisizer & & The CONIROMLER Gemeral Business \\
\hline SYSTEM & 249 & System \\
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\hline VERSA WRITER DIGIIIIER DRAWING & & SARGOW II Chess oa Dishette \\
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\hline VIDEX VIDEOTERM 80 COLUMN CARO & 319 & Bill Budges Spact game album \\
\hline VIDEX VIDEOIR W W GRAPHICS & & SPACE INVADER on casselte \\
\hline EPROM & 339 & SPRCE INYADER on Disherte \\
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\hline MODEM IIB W，INTERFACE & 339 & \(3^{3} 4\) DIGIT BCD ANALOG ID \\
\hline  & 159 & ditigal converier \\
\hline graphics tablet & 659 & GPIB ItEE 488 11978 INIERFACE Ho 1490 \\
\hline SILENTYPE PRINTER & & ASYNCHROMOUS SERIAL INIERFACE \\
\hline W Apple Intelace & 519 & No 7110A \\
\hline APPLE COMPUTER interface cards & & SYMCHRONOUS SERIAL INTEAFACE \\
\hline Prototyping hobby card & 522 & \\
\hline parallel prinier interface card & 145 & parallel interface no 7720a \\
\hline COMmunication iaro a obe Connector Cable & 185 & ARITHMETIC PROCESSOR CARD
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\hline LANGUAGE SYSTEM WITH PASCAL & 429 & SOLDER TAIL BDARD \\
\hline Centronics printer & & EXIENDER BOARD \\
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\hline 4.0 " & 1.42 & 4.44 & 7.94 & 8.0 " & 2.14 & 7.38 & 13.73 & & & & & 500 & 4" & 500 & \(6{ }^{\prime \prime}\) \\
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\hline 5.5 " & 1.58 & 5.38 & 9.72 & 9.5 " & 2.30 & 8.32 & 15.65 & & & & & & & \multicolumn{2}{|l|}{\multirow[b]{2}{*}{\(100041 / 2{ }^{\prime \prime}\)}} \\
\hline 6.0" & 1.65 & 5.66 & 10.31 & 10.0" & 2.39 & 8.71 & 16.28 & 250
500 & 21/2" & 250 & 5" & 1000 & 21/2" & & \\
\hline & & & & & & & & 500 & 3" & 100 & 51/2" & \multirow[t]{2}{*}{\[
\begin{aligned}
& 1000 \\
& 1000
\end{aligned}
\]} & \[
3^{\prime \prime}
\] & \multicolumn{2}{|l|}{1000} \\
\hline \multicolumn{8}{|l|}{\multirow[t]{2}{*}{Kynar precut wire. All lengths are overall, including 1" strip on each end. Colors and lengths cannot be mixed for quantity pricing. Choose from colors}} & \multirow[t]{2}{*}{\[
\begin{aligned}
& 500 \\
& 500
\end{aligned}
\]} & \(31 / 2^{\prime \prime}\) & \multicolumn{2}{|l|}{\(250{ }^{\prime \prime}\)} & & \(31 / 2 "\) & 1000 & 5" \\
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Connectors
\end{tabular}}} & IDH26WR & 3.60 & IDH26SR & 1.85 & 20 & 4.40 & 34.00 \\
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TELEX: 681-367 DELTMAR HTBH
MP/M. GP/M2. 2. PL/I \& CP/NET ARE REG, TM OF DIGITAL RESEARCH INC.
\begin{tabular}{|c|c|c|c|c|c|}
\hline \multirow[t]{3}{*}{\begin{tabular}{l}
Q.T. \\
BOARD SET \#1 (KIT) \\
Z +80 CPU (Rev I) \\
Expandable + with 32 K \\
Monitor-BIOS For Serial Terminal
\end{tabular}} & \multicolumn{2}{|l|}{FOR STARTERS} & \multirow[t]{3}{*}{\begin{tabular}{l}
S.D. \\
BOARD SET \#1 (KIT) \\
SBC-100 \\
Expandoram I with 32K RAM \\
Versafioppy I \\
Monitor For Serial Terminal \\
BIOS for \(8^{\prime \prime}\) or \(51 / 4^{\prime \prime}\) Drives
\end{tabular}} & \multicolumn{2}{|l|}{FOR SERIOUS
HOBBYISTS} \\
\hline & Normal Retail & \$525.00 & & Norma Retail & \$950.00 \\
\hline & \[
\begin{aligned}
& \text { a.t. } \\
& \text { Price }
\end{aligned}
\] & \$425.00 & & \[
\begin{aligned}
& \text { Q.T. } \\
& \text { Price }
\end{aligned}
\] & \$850.00 \\
\hline \multirow[t]{4}{*}{\begin{tabular}{l}
Q.T. \\
BOARD SET \#2 (KIT) \\
Z+CPU (Rev I) \\
Expandable + with 64 K \\
Versafloppy II \\
Monitor BIOS For Serial Terminal
\end{tabular}} & \multicolumn{2}{|l|}{FOR HOBBYISTS} & \multicolumn{3}{|l|}{S.D. FOR SMALL BUSINESSES
BOARD SET \#2 (KIT) \& SOFTWARE DEVELOPERS} \\
\hline & Normal Retail & \$1050.00 & SBC-200
Expandoram II with 32K RAM & Norma Retail & \$1150.00 \\
\hline & & & Versafloppy II & & \\
\hline & а.т.
Price & \$850.00 & Monitor For Serial Terminal BIOS for Disk Drives & \begin{tabular}{l}
а.т. \\
Price
\end{tabular} & \$1000.00 \\
\hline
\end{tabular}

PARTICIPATING IN THIS SALE ALSO ENTITLES YOU TO SUCH BONUSES AS:
1. \(\$ 44.00\) Per set of 4116 's - 200ns devices only
2. Free second page of memory option in all Televideo 912 \& 920 Smart CRT's.

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2716 (5 \& 12V-450 ns)
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TELEVIDEO SMART (CRT) TERMINAL
- Reverse Video - Blinking/blank fields - Upper/ tower case character - Protected fields - Nonglare screen - Underlining - \(12 \times 10\) character resolution - Single stroke editing keys - Function keys - Blinking cursor • TTY keyboard • Numeric pad - 9 Baud rates (75-9600 Baud) - Self-test - Printer port


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920C . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . \(\$ 890.00\)
OPTION:
2nd Page Memory \(\qquad\) \(\$ 24.95\) Frolght Charge \(\qquad\) \(\$ 15.00\) Nationwide Field Service available from General Electric Instrumentation and Communication Equipment Service Shops.

SWITCHABLE 2 or 4 MHz

\section*{THE QT Z+80 REV 1}

Z-80A CPU with Serial I/O Port
The CPU can accommodate a 2708, 2716, or 2732 EPROM in SHADOW Mode, allowing you to use a full 64 K of RAM. The MWRITE signal is generated automatically if you use the board without a front panel. There's also an independent on-board USART to control the RS232 serial port at baud rates from 110 to 9600
\begin{tabular}{|c|c|c|c|}
\hline \multicolumn{3}{|l|}{CPU-Z+80K (KIT) CPU-2+80AT (ABT) CPU-z+808B (BARE BOARD)} & \(\$ 132.00\) \(\$ 189.00\) \(\$ 33.00\) \\
\hline \multicolumn{4}{|l|}{VERBATIM \& MEMOREX} \\
\hline Part No. & Sectoring & Phg. of 2 & Bon of 10 \\
\hline OTMO 52501 & Soh Sector & \$ 2.95 & \$28.95 \\
\hline OTMO 52510 & Mara 10 Sector & \$2.95 & \$29.95 \\
\hline OTMO 52518 & Hard 16 Sector & \$2.95 & \$29.95 \\
\hline QTFO32 1000 & Hard Sector & \$11.95 & \$34.95 \\
\hline OTFO34 1000 & Soft Sector & \$11.95 & \$34.85 \\
\hline
\end{tabular}

KASETTE/10 LIBRARY
Part No.
CAS-10-8
Blue Grey \(8^{\prime \prime}\) Diskette Holder \(\$ 4.50\) Beige Black .............or \(3 / \$ 10.00\) CA8-10-5

Blue Grey 5" Diskette Holder 54.25 Beige Black ............. or \(\mathbf{3 / \$ 1 0 . 0 0}\)
SPECIAL
PACKAGE PRICE
1 Male DB-25, 1 Female DB-25, 1 Cover RS-232 SET

MICROPROCESSORS
\begin{tabular}{|c|c|}
\hline Z \({ }^{0} 0\) (2 MHz) & \$10.95 \\
\hline Z800 (4 MH2) & \$12.95 \\
\hline 6502 & \$11.25 \\
\hline 6800 & \$12.50 \\
\hline 6802 & \$19.50 \\
\hline 8035 & \$20.00 \\
\hline 80804 & \$3.95 \\
\hline 8085A & \$20.00 \\
\hline 8088-4 & \$80.00 \\
\hline 0748-8 & \$70.00 \\
\hline
\end{tabular}

\section*{s080A SUPPORT}


KEYBOARD CHIPS
AY5-2376 .... . 813.75 AY5-3800 ... ... \(\$ 13.75\)
baUd RATE GENERATORS
MC14411 .. \(\$ 11.00\) 1.8432 XTAL . \(\$ 4.95\) BR1941L . \(\$ 10.00\)
DISC CONTAOLLER
1771 . 01 . \(\$ 24.95\) 1791804 . ..... \(\$ 39.95\)
\begin{tabular}{|c|c|}
\hline 1702A & \$4.95 \\
\hline 2708 & 56.75 \\
\hline 2516 & \$22.00 \\
\hline 2758 & \$27.00 \\
\hline 2532 & \$75.00 \\
\hline 2732 & \$75.00 \\
\hline & \\
\hline \(\mathbf{5 2 3 5 0}\) & \$10.95 \\
\hline
\end{tabular}

MISCELLANEOUS
OTHER COMPONENTS
N8T20 ... 3.25
N8T28 . . . \(\$ 2.50\)
N8T97 . . . . \(\$ 2.00\)
N8T98 .. .. ... \(\$ 2.00\)
1488 ... . . ... 51.25
\begin{tabular}{llll}
1489 & \(\ldots\) & & 1.25 \\
D3205 & \(\ldots\) &.. &. \\
\hline
\end{tabular}
D3242 . . \(\$ 10.15\)
D3245 .......... 5.60
P3404 . . \(\$ 8.75\)
DAB131 .. .... . . . \(\$ 3.00\)

CRT CONTAOLLER
MCs845P .... \(\$ 18.00\)
STATIC RAMS
\(2114 \mathrm{~L}(450 \mathrm{nB}) \quad 85.25 \mathrm{ea}\).
.100 ee/ 84.50 ea \(2114 \mathrm{~L}(300 \mathrm{~ns}) \quad \$ 5.50 \mathrm{en}\). .100 ea. \(/ 54.75\) ea. UARTS
TR18028....... 3.75

\section*{CHARACTEA}

GENERATORS
2513 (Upper case) \(\mathbf{\$ 1 0 . 9 5}\) 2513 (Lower case) 510.95 2513 Upper (5 v) \(\$ 0.75\) 2513 Lower ( 5 v) \(\$ 10.95\)

\section*{कौग PRODUCTS}

PB1 2708/2716 EPROM Programmer Kit with Textool sockets \(\$ 134.95\) Assembled \& Tested w/Textools sockets \(\$ 174.95\) - 2708 EPROM (not included) - 2716 EPROM +5 V type (not included) • Any 4 K boundary • Separate 2708 and 2716 sockets • Any 4K/8K boundary above 8000 Hex - Optional Textool sockets (for programming only) - Programming software \& bit for bit verify routines provided
\begin{tabular}{|c|c|}
\hline \begin{tabular}{l}
IO2 Parallel I/O Intertace Kit \\
Assembled \& Tested
\end{tabular} & \[
\begin{aligned}
& \$ 59.00 \\
& \$ 89.00
\end{aligned}
\] \\
\hline MB3 4K 1702 EPROM Board & \\
\hline Kit - without EPROMS. & \$ 65.00 \\
\hline Assembled \& Tested & \$125.00 \\
\hline MB6B 8K Static RAM & \\
\hline 450 nE RAM & \\
\hline Kit & \$139.95 \\
\hline Assembled \& Tested & \$199.95 \\
\hline 250 ns RAM (MB6B) & \\
\hline Kit & \$179.95 \\
\hline Assembled \& Tested & \$245.00 \\
\hline M87 Low Power 16K Static RAM & \\
\hline Kit & \$325.00 \\
\hline Assembled \& Tested & \$375.00 \\
\hline MBAA 16K 2708 EPROM Board & \\
\hline Kit - without EPROMS. & \$99.00 \\
\hline Assembled \& Tested & \$159.00 \\
\hline
\end{tabular}

S-100 KITS
B1A 8080 CPU
Kit
\(\$ 149.95\)
Assembled \& Tested
\(\$ 199.95\)
\(\$ 134.95\)
\(\$ 199.95\)
- Two serial ports with status - Two parallel inputs - Two parallel outputs - Current-loop by optical isolators - UART presets by dip switch -stop bits -word length - parity even and odd - Dip switch addressing of serial I/O to any four port boundary - Dip switch addressing of parallel I/O to any two port boundary

\section*{S-100 BARE BOARDS}

CB1A 8080 CPU
VB2 I/O Mapped Video Interface
102 Parallel I/O Interface
104 2P + 2S I/O Interface
SB1 Music Synthesizer
OB1 Vector Jump \& Prototyping Board
MB3 4K 1702 EPROM Board
MB6B 8K Static RAM
MB7 Low Power 16K Static RAM
MB8A 16K 2708 EPROM Board
T1 Terminator
MTI 15 Slot Molherboard
XB1 Extender Board
\(\$ 33.00\)
\(\$ 33.00\)
\(\$ 33.00\)
\(\$ 33.00\)
\(\$ 40.00\)
\(\$ 29.95\)
\(\$ 30.00\)
\(\$ 27.00\)
\(\$ 30.00\)
\(\$ 30.00\)
\(\$ 26.00\)
\(\$ 40.00\)
\(\$ 13.50\)

\section*{EXPANDORAM I}

EXPANDABLE TO 64K USING 4116 RAMS - For S-100 boards • Bank selectable, PHANTOM provision • Designed to work with Z-80. 8080, and 8085 systems • No wat states required • 16K boundaries \& protect via dip switches - Invisible refresh MEM-18K (16K KIT)
\(\$ 199.00\) MEM-16AT (16K A\&T) \(\$ 249.00\) MEM-32K (32K KIT) . MEM-32AT (32K A\&T) MEM-48K (48K KIT) MEM-48AT (48K A\&T) MEM-64K (64K KIT) MEM-64AT ( 64 K A\&T)

\section*{EXPANDORAM II}
- For S-100 Bus - 4Mnz Operation - Expandable Memory from 16K to 256K - Dip Switch Selectable Boundaries • Uses 16K (4116) or 64K (4164) Memory Devices * Page Mode Operation Allows up to 8 Memory Boards on Bus • For \(\mathbf{Z 8 0}\) CPU's • Phantom Out put Disable - Invisible Refresh
MEMII-16K (KIT)
MEMII-16AT (A\&T)
MEMII-32K (KIT) MEMII-32AT (A8T) MEMII-48K (KIT) MEMII-48AT (A\&T) MEMII-64K (KIT) MEMII-64AT (A\&T)

.5250 .00 \(\$ 300.00\) \(\$ 309.00\) \$360.00 \(\$ 369.00\) \(\$ 409.00\) \(\$ 425.00\) \(\$ 475.00\)
 MPI B-51 SHUGART SA400
\(\$ 295.00\) \(\$ 295.00\)

\section*{8' DISK DRIVES}

\section*{SD SYSTEMS}

\section*{VERSAFLOPPY II}

DOUBLE DENSITY DISK CONTROLLER - For single or double density * 985600 bytes on \(8^{\circ}\) double sided diskettes - 259840 bytes on double sided 51/4" diskettes * S-100 bus * IBM 3740 format in single density \({ }^{\circ} 8^{\prime \prime}\) and \(5^{\prime}{ }^{\prime \prime \prime}\) drives controlled simultaneously - Operates with Z-80. 8080. and 8085 CPU's - Controls up to 4 drives
VF-2K (KIT)
\(\$ 300.00\)
VF-2AT (A\&T)
\(\$ 369.00\)

\section*{SBC-100/200}

2 OR 4 MHz SINGLE BOARD COMPUTER - S-100 bus Z-80 CPU BD - 1 K of on-board RAM - 4 EPROM sockets accommodates 2708. 2716, or 2732 - One parallel and one serial I/O port - 4-channel counter timer chip (Z-80 CTC) - Software programmable serial baud rates

SBC-100K ( 2 MHZ KIT).
\(\$ 255.00\)
SBC-100AT (2 MHZ A\&T).
\(\$ 325.00\)
SBC-200K ( 4 MHZ KIT) .
\(\$ 289.00\)
SBC-200AT (4 MHz A\&T) ................ \(\$ 369.00\)

\section*{SSM APPLE \\ SERIAL/PARALLEL INTERFACE}

Al/O Kil
\(\$ 125.00\)
AI/O ABT
\(\$ 165.00\)

\section*{TEXTOOL}

ZIP* DIP II SOCKETS
16 PIN ZIP: DIP II
\(\$ 5.50\) 24 PIN ZIP* DIP II s 7.50 40 PIN ZIP* DIP II

ZERO INSERTION PRESSURE

\section*{PROM-100}
- For S-100 Bus * Programs the Following EPROMs 2708. Intel 2758. 2716. 2732 and Ti 2516 - Dip Swith Selection of EPROM type - 25 VDC Programming Pulse Generated On Board - Maxımum Programming Time 16.384 Bits in 100 seconds - Software Provides for Reading of Object File from SDOS CP/M or PROM and Programming into EPROM - Program Verification - Verification of Erasure - Zero Insertion Force Socket

PROM-100K (KIT)
\(\$ 175.00\)
PROM-100AT (A\&T)
\(\$ 225.00\)

\section*{Z-80 STARTER KIT}

\section*{COMPLETE Z-80 MICROCOMPUTER}
- On-board keyboard, display. EPROM Programmer, and cassette interface - On-board S-100 interface interface - Wire-wrap area and room for 2 S-100 connectors - Two 8-bit parallel I/O ports, 4-channel CTC. 5 programmable breakpoints - Examine and change memory, l/O ports, or register
Z-80K (KIT)
\$289.00
Z-80AT (A\&T)
\(\$ 349.00\)

\section*{\(\$ 25\) REBATE}
on any SD Systems microcomputer board Offer expires 10-31-80

\section*{STATIC RAM BOARDS}

SR-8K BARE BD (Ithaca Audıo) 21L02. \$ 19.00 SR-16K BARE BD (Problem Solvers) 2114 . S 19.00 SR-16K A\&T (Cal Comp Sys)
2114 L 4 MHZ
SR-32K KIT (Uses 2114L) 4MHz
SR-32K A\&T (Uses 2114L) 4MHz ...... \(\$ \mathbf{\$ 5 0 0 . 0 0}\)

\section*{\$2130.00 COMPUTER} GET A JADE IIFLATION

\section*{JADE Saves You \$1130}

\section*{4 MHz BOARD SET}

SBC-200 CPU with I/O Blo with IIO ........ \(\$ 400.00\) Versafloppy II Dbl density controllr . \(\$ 430.00\) ExpandoRAM II 64 K RAM \(4 \mathrm{MHz}, \$ 1300.00\) Total price
JADE KIT PRICE
Less SD Systems Rebate YOUR COST ONLY.
\$2130.00
\$1074.95 \(\$ 75.00\) \(\$ 999.95\)

\section*{S-100 Boards}

THE BIG Z - Jade
2 or 4 MHz sutuchathe \(Z\)-ko CPU with serial I O CPU-30201K Kit \(\$ 145.00\) CPU-30201A A\& \(T \ldots \ldots .\). CPU-30200B Bare board ............. \(\$ 35.00\)

SBC-100 - SD Systems
2.5 MHz Z.80 CPU wuth serial \& parallel I () ports CPC-30100K Kit \(\$ 257.50\)
CPC-30100A \(A \& T\) \(\$ 325.00\)

SBC-200 - SD Systems
\(4 \mathrm{MHz} \% \mathrm{Bl}\) CPl utith serial \& parallel I () parts.
CPC-30200K Kit
\(\$ 299.95\)
CPC-30200A A\&T
\(\$ 375.00\)
CB2-S.S.M.
2 ar \(\& \mathrm{MHz}\) suitchable Z-NU CPU with RAM. ROM. \& 10 CPU-30300K Kit
\(\$ 185.00\)
CPC-30300A A\&T
\(\$ 249.95\)
ExpandoRAM I - SD Systems
2.5 MHz RAM huard expandable from 16 K to 64 K

MEM-16130K 16 K kit .............. \$189.00 MEM-16130A 16 K A \& \(T\)............. \(\$ 249.00\) MEM-32131K 32K kil .............. \$234.00 MEM-32131A 32K A \& T \(\ldots \ldots .\). MEM-48132K \(48 K\) kit MEM-48132A \(48 K\) A \& T MEM-64133K 64 K kit MEM-64133A \(64 K\) A \& \(T\) \(\$ 279.00\) \(\$ 339.00\) \(\$ 339.00\)
\(\$ 324.00\)

ExpandoRAM II - SD Systems
( MHz RAM besard expandable from 16 K to 256 K MEM-16630A 16 K kit MEM-16630A \(16 \mathrm{~K} A\) \& \(T\) MEM-32631K 32 K kit MEM-32631A \(32 K\) A \& \(T\) MEM-48632K 48 K kit MEM-48631A \(48 K A\) \& \(T\) MEM-64633K 64 K kit MEM-64633A \(64 \mathrm{~K} A\) \& \(T\)

32K STATIC RAM - Jade
2 or 4 MHz expandable static RAM board uses \(2114 L^{\prime}\). MEM-16150K 16 K 2 MHz kit ...... \(\$ 249.95\) MEM-16150A 16 K 2 MHz \& \(T\) MEM-16151K 16 K 4 MHz kit MEM-16151A \(16 \mathrm{~K} 4 \mathrm{MHz} A \& T\) MEM-32150K 32 K 2 MHz kit MEM-32150A 32 K 2 MHz \& \(T\) MEM-32151K 32 K 4 MHz kit\& \(T\) MEM-32151K 32 K 4 MHz A \& \(T\) \(\$ 249.95\)
\(\$ 299.95\) \(\$ 259.95\) \(\$ 309.95\) \(\$ 399.95\) \(\$ 449.95\) \$409.95

16K STATIC RAM - Cal Comp Sys 2 or 4 MHz IfK static RAM - a real memury bargain MEM-16160K 16 K 2 MHz kit ...... \$249.95 MEM-16160A 16 K 2 MHz A \& \(T \ldots \$ 279.00\) MEM-16162K 16 K 4 MHz kit MEM-16162 16 K 4 MHz A \& \(T\) MEM-16160B Bare board

DOUBLE-D - Jade
Duuble density controller with the inside track
\begin{tabular}{|c|c|}
\hline \multicolumn{2}{|l|}{10D-1200K Kit .................. \$29} \\
\hline IOD-1200A \(8^{\prime \prime}\) & \\
\hline \multicolumn{2}{|l|}{10D-1205A \(5^{\prime \prime \prime \prime \prime \prime} A \& T\)} \\
\hline \multicolumn{2}{|l|}{IOD-1200B Bare hoard} \\
\hline \multicolumn{2}{|l|}{\multirow[t]{2}{*}{VERSAFLOPPY 1 - SD Systems Versatile floppy disk cuntroller for \(8^{\prime \prime}\) or \(5^{1 / 4 "}\)}} \\
\hline & \\
\hline \multicolumn{2}{|l|}{\multirow[t]{2}{*}{IOD-1150K
IOt
IOD-1150A
\&
\(T\)}} \\
\hline & \\
\hline \multicolumn{2}{|l|}{\multirow[t]{2}{*}{VERSAFLOPPY II - SD Systems Neu' double density cuntroller for buth \(8^{\prime \prime}\) \& \(5^{1 / s^{\prime \prime}}\)}} \\
\hline & \\
\hline \multicolumn{2}{|l|}{\multirow[t]{2}{*}{}} \\
\hline & \\
\hline \multicolumn{2}{|c|}{\multirow[t]{2}{*}{\begin{tabular}{l}
I/O-4 - S.S.M. \\
() purts plus 2 parallel I O ports
\end{tabular}}} \\
\hline & \\
\hline 10I-1010K Kit & \$12 \\
\hline I-1010A A \& & \\
\hline 010B Bare b & \\
\hline
\end{tabular}

PB-1 - S.S.M.
2708, 2716 EPR()M beard with builten programmer MEM-99510K Kit \(\$ 119.95\) MEM-99510A \(A\) \& \(T\) \(\$ 169.95\)

\section*{PROM-100 - SD Systems}
2708. 2716, 2732, 2758, \& 2516 EPROM programmer
MEM-99520K Kit .................. \$175.00 MEM-99520A \(A \& T \ldots \ldots \ldots \ldots\).

\section*{32K BYTESAVER - Cromemco}

32 K EPROM buard with on-board 2716 prokrammer MEM-32550A \(A \& T \ldots . . . . . . . .\). . \(\$ 295.00\)

\section*{100K DAY CLOCK - Mtn Hardware \\ Crystal controlled S. IOO clock with NiCad backup \\ IOK-1400A \(A\) \& \(T\) \\ \(\$ 329.95\) \\ SB1 - S.S.M. \\ IOS-1005K Kit \(\$ 189.95\) \\ IOS-1005A A \& \(T\)................... \$269.95}

TB-4-Mullen
E.xtremely tersatile extender hoard with logic probe TSX-180K Kit ........................ \$55.00 TSX-180A \(A \& T \ldots, \ldots, \ldots, \ldots, \ldots . .\).

TERMINATOR \& EXTENDER - C.C.S.
Can be used as hoth an S. IO6) extender and terminator
TSX-150K Kit
\(\$ 39.95\)

\section*{S-100 EXTENDER - Cal Comp Sys} Puts problem boards within easy reach
TSX-160A \(A\) \& \(T\)..................... \(\$ 24.95\)
VDB-8024-SD Systems
\(80 \times 2410\) mapped video board with keybuard 10
1OV-1020K Kit
\(\$ 324.95\)
IOV-1020A A \& T
\(\$ 379.95\)
VB3-S.S.M.
k0 24 or \(80 \times 4 \mathrm{t}\) memury mapped with graphics
1OV-1095K Kit, 4 MHz ............ \(\$ 339.95\)
IOV-1095A A\& \(T, 4 \mathrm{MHz}\)......... \(\$ 399.00\)
IOV-1096K \(80 \times 48\) upgrade, 4 MHz . \(\$ 89.00\)
VIDEO BOARD - Jade
\(64 \times 16\) awsembled \& tested S. 100 wideo beard
IOV-1050B Bare hoard ............... \$29.95 IOV-1050A A \& T sale price .......... \$99.95

8K RAM BOARDS - Special Sale Uspss 2ILO2 RAM chips
2 boards \& manual for
\(\$ 30.00\)

\section*{Single Board Computer}

AIM-65 - Rockwell
6.502 computer uzth printer. display. \& keyboard
CPK-50165 IK AIM ................ \$374.95 CPK-50465 4K AIM ................. \$449.95 SFK-74600008E 8 K BASIC ROM ... \(\$ 99.95\) SFK-64600004E 4 K assembler ROM \(\$ 84.95\) PSX-030A Power supply ............. \(\mathbf{\$ 5 9 . 9 5}\) ENX-000002 Enclosure . .............. \(\$ 49.95\)
\(4 K\) AIM. \&K BAsIC', power supply. \& enclosure Special package price
\(\$ 599.00\)
32K RAM - for AIM-65
Dynamic memery hoard to expand your AIM. 65 MEM-99170A A \& T \(w\) out RAM .. \(\$ 275.00\) MEM-16170A \(A\) \& \(T w^{\prime} 16 K \ldots \ldots . . \$ 325.00\) MEM-32170A \(A \& T u, 32 K \ldots . . . \$ 375.00\) MEM-99170B Bare board ............ \(\$ 49.00\)

\section*{DISK CONTROLLER - for AIM-65 \\ Add 5'*" or \(8^{\prime \prime}\) disk drieves to your AIM-6.5 IOD-3013A A\&T \\ \(\$ 575.00\)}

VISIBLE MEMORY - for AIM-65
Video hoard with \(8 K\) nemmery \& graphics for AIM-6.5 IOV-3011A A\&T
\(\$ 239.95\)
MEMORY-MATE - for AIM-65 The master-mater with ARK RAM.I O. PROM. \& musii MEM-52301A \(A\) \& \(T w{ }^{16 K}\)
\(\$ 475.00\)
Z-80 STARTER KIT - SD Systems
2-Ky computer weth RAM. ROM, IO, \& kevboard
CPS-30010K Kit
\(\$ 289.95\)
CPS-30010A \(A \& T\)
\$349.95

\section*{Accessories for Apple}

16K MEMORY UPGRADE
Add I6K of RAM to your TRS-so. Apple, or Exidy
MEX-16100K TRS.80 kit ............. \(\$ 47.95\)
MEX-16101K Apple kit ................ \(\$ 47.95\)
MEX-16102K Exidy kit ................ \(\$ 47.95\)

\section*{DISK DRIVE for APPLE}
\(5^{1},{ }^{4}\) disk drive with controller for your Apple
MSM-12310C with controller ...... \$495.00
MSM-123101 w/out controller ..... \(\$ 425.00\)
\(8^{\prime \prime}\) DRIVES for APPLE
Comeroller. DOS, tuo \(8^{\prime \prime}\) drives, cabinet, \& cable
Special package price ............. \(\$ 1475.00\)

\section*{AIO - S.S.M.}

Parallel \& serial interface for your Apple IOI-2050K Kit \$115.00 IOI-2050A \(A \& T\) \(\$ 155.00\)

SUP'R'TERMINAL - M \& R Assoc
(1) \(x 24\) tideo display beard for your Applu IOV-2100A \(A\) \& \(T\)
\(\$ 359.00\)
SUPERTALKER - Mtn Hardware
Sprech rrcognition synthesizer " sppaker \& mike IOS-2015A A \&T \(\$ 275.00\)

\section*{Z-80 CARD for APPLE}
2.8u CPU card with CP \(M\) for your Apple

CPX-30800A A \& T
\(\$ 345.00\)
MICROMODEM - D.C. Hayes
Auto ansurer dial modem card for Apple or S.tow IOM-2010A Apple modem ......... \$349.95 IOM-1100A S-100 modem .......... \$375.00

SUP'R'MOD II - M \& R Assoc
Colur or \(B\) \& W TV interface recommended for Apple IOR-5050A \(A \& T\)

NOW ONLY \$999.95 ( FIGHTLIG KIT TODAY,

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BASE 2 - Impact Printer
132 cps. bi.directional, tractor feed. \& graphics PRM-13100
\(\$ 625.00\)
DP-9500 - Anadex
\(9 \times 9\) dot matrix, 176 column. 200 cps. \& graphics PRM-10500 Standard DP-9500.... \$1495.00 PRM-10510 with graphics \& \(2 K \ldots \$ 1595.00\)

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\hline \multicolumn{2}{|l|}{\multirow[t]{2}{*}{\begin{tabular}{l}
ISO-BUS - Jade \\
Silent, simple, and on sale - a better motherboard
\end{tabular}}} \\
\hline & \\
\hline \multicolumn{2}{|l|}{6 Slot ( \(51 /{ }^{\prime \prime} \times 8 \%^{\prime \prime}\) )} \\
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\hline MBS-061A \(A\) \& \(T\) & \$49.95 \\
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\hline MBS-121B Bare board & \$29.95 \\
\hline MBS-121K Kit & \$69.95 \\
\hline MBS-121A \(A \& T\) & \$89.95 \\
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\hline MBS-181B Bare board & \$49.95 \\
\hline MBS-181K Kit & \$99.95 \\
\hline MBS-181A \(A\) \& \(T\) & \$139.95 \\
\hline \multicolumn{2}{|l|}{Mainframes} \\
\hline \multicolumn{2}{|l|}{\multirow[t]{2}{*}{MAINFRAME - Cal Comp Sys}} \\
\hline & rsupply \\
\hline ENC-112105 Kit & \$309.95 \\
\hline ENC-112106 A \& T & \$349.95 \\
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\hline \multicolumn{2}{|l|}{ASSEMBLED} \\
\hline \begin{tabular}{l}
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32K, QD, List \$2995
\end{tabular} & \[
\begin{array}{r}
\$ 2279 \\
.2539
\end{array}
\] \\
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\begin{tabular}{|c|c|c|c|}
\hline Article * & Page & Article & Author \\
\hline 1 & 26 & Build a Low-Cost, Remote Data-Entry Terminal & Clarcla \\
\hline 2 & 46 & An 8088 Processor for the S-100 Bus, Part 1 & Cantrell \\
\hline 3 & 76 & Dissecting the TI Speak & \\
\hline 4 & 86 & Penny Pincher's Joystick Interface & We \\
\hline 5 & 102 & Machine Problem-Solving, Part 1: Trial and Error Search, A Plan to Save the Missionaries & \\
\hline 6 & 116 & APL Character Generator & \\
\hline 7 & 126 & Construction of Fourth-Generation Video Terminal, Part 2 & Wlerenga \\
\hline 8 & 180 & FCC Regulation of Personal Computers and Home Computing Devices & M \\
\hline 9 & 206 & Varieties of Threaded Code for Language Implementation & Walker \\
\hline 10 & 230 & Education Forum: New Cultures From New Technologies & Pape \\
\hline 11 & 242 & Khachlyan's Algorithm, Part 2 & Berresford, et al \\
\hline 12 & 270 & Exploring Ballistics With Your Computer & Jenks \\
\hline 13 & 282 & An Interrupt-Driven Real-Time Clock for the TMS 9900 & Morris \\
\hline 14 & 328 & A BASIC Floppy-Dlsk Accounting System & Roehrig \\
\hline
\end{tabular}

\title{
BYTE's Ongoing Monitor Box
}

Article\# Page Article
26 Build a Low-Cost, Remote Data-Entry Terminal Part 1
76 Dissecting the TI Speak \& Spell
Rigsby

86 Penny Pincher's Joystick Interface
Wexler
4
102 Machine Problem-Solving, Part 1: Tria and Error Search, A Plan to Save the Missionarles
116 APL Character Generator
126 Construction of Fourth-Generation Video Terminal, Part 2

Frey
7
180 FCC Regulation of Personal Computers and Home Computing Devices Mahn
206 Varieties of Threaded Code for Language Implementation

Walker
10
11
242 Khachlyan's Algorithm, Part 2
270 Exploring Ballistics With Your Computer

Jenks
13
328 A BASIC Floppy-Dlsk Accounting System

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\section*{Data From the BOMB Output Port}

June BYTE readers communicated their approval of Steve Ciarcia's "I/O Expansion for the TRS-80, Part 2: Serial Ports." An aboveaverage number of responses gave Steve a well deserved first place at 1.51 standard deviations above the mean. Congratulations are also in order to Ronald Parsons for his excellent article "An Answer/Originate Modem," which placed a close second at 1.35 standard deviations above the mean.

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\section*{Inquiry No. Page No.}

194 260
264
103
103

312

AB Computers 368 ABM Products 281 Ackerman Digital 34 Adv Computer Prod 352, 353 AEON 297
ALF Produ
ALF Products Inc 151
Alpha Products Co 362
Altos Computer 3
American Micro Products Inc 251
American Square Comp 76 Ancrona 374
Anderson Jacobson 200
Anderson Jacobson 289
Apparat 157
APF Electronics 140, 14
Apple Computer 12, 13
Applied Logic, Inc 355
Artec Electronics 18
ASAP Computer Prod Inc 357
Atari Personal Computers 24, 25
Atec Systems 358
Automated Equipment Inc (AEI) 165
BADGE-A-MINIT 325
base 2 inc 103
BASF Systems 115
Bay Technical Associates 114
Beckian 375
John Bell Engineering 218
Beta Computer Devices 58
Beta Computer Devices 388
Bishop Management Co Ltd 356
Bits Inc 280
Bits Inc 312
Bower-Stewart \& Associates 224
Buss/Charles Floto 308
BYTE Books 92, 245, 398
BYTE Back Issues 320
BYTE Subscriber 318
BYTE WATS 324
C \& S Electronics Mart 21
California Comp Sys 20, 21
California Data Corp 355
California Digital 385
Cameo Electronics 231
CCM 362
Central Data 215
Centronics 155
CFR Assoc Inc 269
Chrislin Industries 159
Cleveland Consumer Computers
\& Components 162, 163
Color Software 33
CompuMart 316
CompuMart 382, 383
CompuMax 291
CompuServe (MicroNED 263
CompuSoft Publishing 267
Computer Factory 203
Accessories 30
Accessories 30
ComputerLand of Niles Lid 325
Computer Marketing Corp 295
Computer Prof Book Club 192, 193
Computer Programming computer
Computer Service Center 364
Computer Services 293
Computer Shopper 356 Computer Speciatties 28 Computer Specialties 281
Computers "R" Us 145
The Computer stop 28 The Computer Stop 361 Computer Warehouse 190 Computers Wholesale 274 Computex 123
Compu/Time 293
Computique 133
CompuView Products Inc 275
Concord Comp Components 373
Condor Computer Corp 306
Contract Services Assoc 265
Control Data Learning
Center 135
Cover Craft 271
CP Aids 295
The CPU Shop 367
Creative Software 269
Cromemco 1, 2
Custom Bus Computers 364
Cybernetics inc 293
Dal-Comp 365
Data Discount Center 60
Datadisk Systems 323

261

\section*{Digital Multi-Media Control 364}

Digital Pathways 239
Digital Research Comp (TX) 363
Digital Research Corp (CA) 213
Discount Software Group 48
Disc/3 Mart Inc 266
Dual Systems Control Corp 66
Dynacomp Inc 234
Dynamic Microprocessor Assoc 287
Ecosoft 297
Electravalue Industrial 360
Electrolabs 380, 381
Electronic Control Tech 78
Electronic Music Systems 358
Electronics Book Club 169
Electronic Specialists 289
Electronic Systems 369, 370, 371
Electronic Sys Furniture Co 128
Ellis Computer Systems 271
Emtrol Systems Inc 56
Epson 201
Essex Publishers 276
Estimation Inc 364
Eubanks Engineering 356
Exatron Inc 17
Excom 285
Executive Business Sys 247
Factory Direct 297
Fair Com 341
Faragher \& Assoc 362
Farnsworth Comp Center 300
FMG Corp 302
Forethought Products 241
Forth Inc 249
Frederick Computer Prods 324 FUTUREWORLD 40 Gimix 360
Godbout Electronics 175
Mark Gordon Computers 268
GW Computers 218
H \& E Computronics 125
H\& E Computronics 178, 179
Hardhat Software 16
Hardside 226
Harrex Corp 355
Hayden Book Co 27
Hayden Book Co 278
Hazeltine Corp 188, 189
Hazeltine Corp 188,
Heath Company 113
Heath Company
High Technology Inc 110
High Technology Inc 110
Hobbyworld Electronics 376, 377
Hobbyworid Electronics
Houston Instruments 65
Houston Instruments 65
Houston Instruments 65
Houston
IBC 310
IDM 360
Image Resource Corp 89
Industrial Micro Systems 85
Industrial Micro Systems 95
Industrial Micro Systems 147 Industrial Micro Systems
Infosoft Systems Inc 96 Infosort Systems Inc 96
Integral Data Sys 79 Integral Data
Integrand 230
Intelligent Systems Corp 43 intertec Data Sys 33 intertec Data Sys 33
Intertec Data Sys 119
I/O Technology 301
Ithaca Intersystems 9, 277
J \& S Software 355
Jade Comp Prod 390, 391
Jameco Electronics 392, 393
Jini Microsystems 364
Joe Computer 226
Kemco Ltd 109
Robert Kleven \& Co 184
Konan Corp 223
Leapac Services 283
Lexington Books/D C Heath 319
Lifeboat 68, 120, 121
Lobo Drives Int'l 23
Lomas Data Products 271
MacMillan Book Clubs 161
Macrotronics 279
Malibu Electronics 233
Marot Software Systems Inc 167
Marymac Industries 266
Matchless Systems 309
Maxell Data 117
JMcCarron Soffware 364
McGraw-Hill Magazines 72, 73

Meas Sys \& Controls 27, 211 \(227,243,255\)

224 MICAH 30
74 Microamerica Distributing 204
Micro Age Computer Store 111
Micro Ap 253
221 Micro Appl Grp (MAG) 299
157 Micro Appl Grp (MAG) 299
Micro Architect 243
Micro Business World 378, 379
Microbyte Soffware 211
Micro Comp Discount Co 122
Micro Comp Discount Co 279
MicroComputer Serv Corp 291
Microcomputer Tech Inc 157
Microcomputer Tech Inc 157
Microcomputer Tech Inc 225
Microcomputer
MicroDaSys 262
Micro Data Base Sys 93
Micromail 98
Micromail 238
Micro Management Sys 173
Micro Manageme
Micro Mikes 237
Micro Mikes 237,
Micropolis 194, 195
Micro Products Unlimited 356
Micro Pro Int'I 153
Microsoft 59
Microsoft (Cons Prod Div) 41
Micro Software Distributors 362
Microtax Software 48
MicroTech Exports 362
Microtek Inc 47
The Micro Works 54
Micro World 77
Mighty Micros 286
335 Mikos 374
201 Mini Computer Suppliers 285
12 Mini Micro Mart 22
349 Mini Micro Mart 394
350 Mini Micro Mart 395
351 Mini Micro Mart 396
352 Mini Micro Mart 397
96 Mittendorf Engineering 154
17 Morrow/Thinker Toys 31
Morrow/Thinker Toys 31
Morrow/Thinker Toys 71
Mountain Computer Inc 19
165 Mountain Computer Inc 256
mpi 305
MTI 360
MT Microsystems 221
Multi Business Comp Sys 360
MVT Microcomputer Sys 61
The National Comp Shows 101
National Small Comp Show 170,
National Small Comp
171
Nautilis Systems 314
242 Na
242
122
124
NEECO 204
Netronics 6, 6, 10, 181
New England Business Service
(NEBS) 196

\section*{(NEBS) 196}

NJ Computer Show 291
Noble Computer Corp 364
Noesis Computing Co 328
North Star Computers Inc 8
North Star Computers Inc 81
Northwest Comp Services 356
Northwest Comp Services 35 Novation 264
NR Scholl 177
Ohio Scientific Instr 44, 45
Okidata Corp 53
OK Machine \& Tool 63
Omega Research 277
Omegasoft 358
Omikron 297
OnComputing 257
Organic Software 232
Osborne/McGraw-Hill 259
OSM Computer Corp 268
Owens Associates 182, 183
Page Digital 384 , Systems 235
Pan American Electronics ( \(A\) Radio Shack Auth Sales C(r) 368 PCD Systems inc 38
Pensadyne Computer Serv 364 Percom Data Co Inc 15 Peritek Corp 308
Personal Computer Sys 158 Personal Micro Comp Inc 199 Personal Programs by Victor 355
24 Personal Software 39
Petersen Manufacturing Co 34 Phase One Systems 185
252 Pickies \& Trout 322
Potomac Micro Magic 258
Power One Inc 14
262 Priority One 349
263 Priority One 350, 351

116
S. 100 Computing Inc 362

RNB Enterprises 36
SC Digital 321
Scientific Engineering Labs 360
Scion Corp 5
SciTronics 269
Scottsdale Systems 82
SD Systems 55
Service Technologies 94
Shepardson Microsystems 150
Michael Shrayer Soffware 70
Shugart 7
Sierra Software 362
Sinclair Research 209
Sirius Systems 217
Small Business Appl 107
Small Sys Design 299
Smoke Signal Broadcasting 87
Smoke Signal Broadcasting
(Dealers Only) 87

\section*{Softech 83}

Software 48
Software Concepts 295
Software Consultants 358
Software Development \&
Training 227
The Software Exchange 229
The Software Farm 356
Software Labs 381
The Software Works 315
Solid State Sales 366
Solid State Surplus 364 Sorcim 88
Sorrento Valley Assoc 172
Southwest Tech Prod Corp C II
Spectrum Software 222
SP Electronics 360
SSM 11
Structured Systems Group 174
Struclured Systems Group 176 SubloGic 301
Summagraphics 272

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All standard C3 features including:
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- System boots from floppies or hard disk on power up
- 74 megabytes end user workspace under OS-65U, 80 megabytes unformatted
- Ultra-high performance disk

74 millisec worst case access
38 millisec average
10 millisec access on cylinder (215K user workspace) 8 megabits per second transfer rate
- Simple on/off disk operation with elaborate internal protection from improper temperature, line voltage and controller failures
- Features spindle brake and designated head landing ares for much longer operational life than the newer low-cost Winchesters
- Highly advanced OS-65U operating system: Multiple level pass word security Multiple operating systems on disk Ultra-high speed "FIND" command for high speed strin' searches (Associative Access) Upward compatible with multi-user and network systems with full file, peripheral and communications arbitration* between users

- Available factory configured for up to 8 users and network data base operation
- Expandable to CP/M operation by adding 4K (CM-2 memory)

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\section*{C3-C}

A medium performance Winchester disk based system which provides the ideal cost/performance ratio in typical small business applications. The C3-Cuses the Shugart SA4008 29 megabyte Winchester disk.
Performance specifications, hardware configuration and software is identical to the C3-B with the following exceptions:
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- 29 megabytes unformatted capacity
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